

ಕರ್ನಾಟಕ ಸರ್ಕಾರ

# **GOVERNMENT OF KARNATAKA**

ಕಾಲೇಜು ಮತ್ತು ತಾಂತ್ರಿಕ ಶಿಕ್ಷಣ ಇಲಾಖೆ

DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

C-20 2020-21

# Diploma in Electronics & Communications Engineering





With Effect from 2020-21 C-20 Curriculum Development Cell

Department of Collegiate & Technical Education

#### Vision

#### [(To be drafted individually at institution level)]

Create an Environment to acquire skills through learning and practicing in the relevant domain to become effective and successful technician to augment the societal needs, upholding ethics and environmental concern.

#### Mission

#### (To be drafted individually at institution level)

- **M1:** Identification of relevant courses and their content necessary for the skill Development in Electronics and Communication Engineering.
- **M2:** Providing adequate emphasis for practical learning augmented by the relevant theoretical concepts.
- **M3:** Facilitating continuous evaluation and outcome assessment.
- **M4:** Opportunity to develop applications.
- **M5:** Facilitating an environment for interactive and interdisciplinary learning.
- **M6:** Exposure to industries, professional bodies and social activities.

# Programme Educational Objectives (PEOs)

(To be drafted individually at institution level)
(After 2/3 years of graduation, the students will have the ability to)

PEO1	Demonstrate, Update and adapt domain knowledge in the area of electronics and communication engineering and the allied fields to propose solutions for the core industry in the ever changing global enterprise with ethical practices.
PEO2	Assume leadership roles and succeed in their chosen career path, in industry or public service through engineering ability, life skills and multidisciplinary skill set acquired.
PEO3	Pursue higher education institutes of national level.

# **PROGRAM OUTCOMES (POs)**

- 1. **Basic and Discipline specific knowledge:** Apply knowledge of basic mathematics, science and engineering fundamentals and engineering specialization to solve the engineering problems.
- 2. **Problem analysis:** Identify and analyze well-defined engineering problems using codified standard methods.
- 3. **Design/ development of solutions:** Design solutions for well-defined technical problems and assist with the design of systems components or processes to meet specified needs.
- 4. **Engineering Tools, Experimentation and Testing:** Apply modern engineering tools and appropriate technique to conduct standard tests and measurements.

- 5. **Engineering practices for society, sustainability and environment:** Apply appropriate technology in context of society, sustainability, environment and ethical practices.
- 6. **Project Management:** Use engineering management principles individually, as a team member or a leader to manage projects and effectively communicate about well-defined engineering activities.
- 7. **Life-long learning:** Ability to analyze individual needs and engage in updating in the context of technological changes.

### **CONSISTENCY MATRIX OF PEO'S WITH MISSION**

	PEO statements	Adapt to Industry	Higher Learning	Team Spirit	Self-Learning	Leadership Qualities	Societal Needs	Environmental Concern
1	Demonstrate, Update and adapt domain knowledge in the area of electronics and communication engineering and the allied fields to propose solutions for the core industry in the ever changing global enterprise with ethical practices.	*	*			*	*	*
2	Assume leadership roles and succeed in their chosen career path, in industry or public service through engineering ability, life skills and multidisciplinary skill set acquired.	*		*	*	*	*	*
3	Pursue higher education institutes of national level.		*		*			

### PROGRAM SPECIFIC OUTCOMES (PSOs)

# Program shall specify 2-4 Program Specific Outcomes (To be drafted individually at institution level)

PSO1	Design and simulate basic electronic circuits and make use of the measuring instruments
	in the design of analog and digital circuits
PSO2	Apply principles of mathematics, signals and communication theory to analyze different
	types of signals and operations on signals.

#### 1.0 GENERAL PROGRAMME STRUCTURE AND CREDIT DISTRIBUTION

- 1. **Definition of Credit:**Credit is a kind of weightage given to the contact hours to teach the prescribedsyllabus, which is in a modular form. For courses, one credit is allocated to one contact hour for theory / tutorial per week and one credit is allocated to 02 contact hours for practical.
- 2. **Choice-Based Credit System (CBCS):** CBCS is a flexible system of learning that permits students to learn at their own pace, choose electives from a wide range of elective courses

andadopt an inter-disciplinary approach in learning and make best use of the expertise of available faculty.

# 3. Range of Credits

1 Hr. Lecture (L) per week	1 credit
1 Hr. Practical (P) per week	0.5 credit
1 Hr. Tutorial (T) per week	1 credit
4 Hrs. Theory (T) per week	4 credit
3 Hrs. Practical (P) per week [1 Hr. Tutorial +2 Hrs. Practical]	2 credit

4. **Programme**: Programme means Diploma Programme that is Diploma in Electronics &Communications Engineering, which is of three years duration.

### 2.0 PROGRAMME STRUCTURE

- 1. **Course:** A Course is a component (a paper) of a Programme. All the courses need not carrysame weightage. The course should define Course objectives. A course may be designed to involve lectures / tutorials / laboratory work / seminar / project work/ Internships / seminar or a combination of these, to meet effectively the teaching and learning needs and the credits may be assigned suitably.
- 2. Course Code: Each course shall have an alphanumeric code, which includes last two digits of year of introduction such as 20 subject code EC (EC for Electronics & Communication engineering, CH for Chemical Engineering etc. ), then first two digits for example 12 (where 1 represents first semester and 2 represents the course number in incremental order) and the last alphabet represent Theory (T), Practical/Internship/Project (P), Drawing (D), Programme / Open Electives (A, B, C, E, F, G ...).
- 3. **Programme Courses:** Each Programme will consist of Communication skills and Social Sciences (HS), Engineering Mathematics, Statistics and Analytics (BS), Engineering Sciences (ES), Professional Core (PC), Professional Electives (PE), Open Electives (OE), Employability Enhancement Courses (EEC) and Internships.
  - 1. Communication Skills and Social Sciences: Communication Skills and Social Science courses are incorporated in the curriculum to meet the desired needs of communication and life skills amongst students.
  - 2. Engineering Mathematics, Statistics and Analytics: Common to all Engineering Programme to develop reasoning and analytical skills amongst students.
  - 3. Engineering Sciences: Engineering Science shall create awareness on different specializations of engineering studies. The goal of these courses are to create engineers of tomorrow, who possess the knowledge of all disciplines and can apply their interdisciplinary

- knowledge in every aspect. It could be any branch of engineering Civil, Computer Science and Engineering, Electrical, Mechanical, etc.
- 4. **Professional Core:** Core Courses designed in the programme which are major courses of the discipline, required to attain desired outcomes and to ignite critical thinking skills amongst students.
- 5. **Professional Elective:** Generally a course can be chosen from a pool of courses and which may be very specific or specialized or advanced or supportive to the discipline or nurtures the candidate's proficiency/skill is called Professional Elective Course.
- 6. **Open Electives:** An elective course chosen generally from other discipline/ subject, with an intention to seek interdisciplinary exposure is called an open elective. While choosing the electives, students shall ensure that they do not opt for the courses with syllabus contents of which are similar to that of their departmental core/elective courses.
- 7. Audit / Non-Core Courses: An audit / Non-core course is one in which the student attends classes, does the necessary assignments, and takes exams. The Institute encourages students towards extra learning by auditing for additional number of courses. The results of audit courses shall not be considered for prescribed "carry over courses" limit, however students need to pass audit courses for awarding the diploma.
- 8. **Employability Enhancement Courses**: It contains the following courses:
  - a. Mini Project: Mini Project is a laboratory oriented course which will provide a platform to students to enhance their practical knowledge and skills by development of small systems/application.
  - b. **Seminar:** Seminar should be based on thrust areas in state of art technologies. Students should identify the topic of seminar and finalize in consultation with Guide. Students should understand the topic and compile the report in standard format and present in front of Panel of Examiners respective Programme.
  - c. Major Project: Every student must do one major project in the Final year of their program. The minimum duration of project is 6 months. Students can do their major project in Industry or R&D Lab or in house or combination of any two.

# 3.0 COURSE CODE AND DEFINITION:

Course code	Definitions	Teaching Dept. Code	Name of the Teaching Department	Teaching Dept. Code	Name of the Teaching Department
L	Lecture	SC	Science	MI	Mechanical Engineering [Instruments]
Т	Tutorial	СР	Commercial Practice / English	CR	Ceramic Engineering
P	Practical	ME	Mechanical Engineering	EN	Civil Environmental Engg.
HS	Humanities & Social Sciences Courses	EE	Electrical & Electronics Engg.	AN	Aeronautical Engg.
BS	Basic Science Courses	CE	Civil Engineering	MN	Mining & Mine Surveying
ES	Engineering Science Courses	EC	Electronics &Commn. Engg.	MM	Modern Office Management
PC	Program Core Courses	CS	Comp Science &Engg.	LI	Library and Information Science
PE	Program Elective Courses	IS	Info Science &Engg.	FT	Apparel Design and Fabrication Technology
OE	Open Elective Courses	AT	Automobile Engg.	СН	Chemical Engineering
AU	Audit Courses	MC	Mechatronics	PO	Polymer Technology
SI	Summer Internship	MT	Metallurgical Engg.	PT	Printing Technology
PR	Project	НР	Mechanical Engineering [HPT]	TX	Textile Technology
SE	Seminar	WS	Mechanical Engineering[ Welding & Sheet Metal]	EI	Electronic Instrumentation & Control Engg.
CIE	Continuous Internal Evaluation	CN	Cinematography	LT	Leather & Fashion Technology
SEE	Semester End Examination	SR	Sound Recording &Engg.	WH	Water Technology & Health Science
		РН	Civil (Public Health & Environment) Engg.	MY	Mechanical Engineering [Machine Tools]
		TD	Tool & Die Making	AR	Architecture
		ID	Interior Decoration	EG	English

#### 4.0 INDUCTION PROGRAMME

The Essence and Details of Induction program can also be understood from the "Detailed Guide on Student Induction program', as available on AICTE Portal, although that is for Diploma students of Engineering & Technology. Suggestive schedule for induction program is given below

(Link:https://www.aicteindia.org/sites/default/files/Detailed%20Guide%20on%20Studen t%20Induction%20program.pdf)

# **Induction Program Schedule (Suggestive only)**

(Induction program for students to be offered right at the start of the first year)

SL NO	DAY	TIME	ACTIVITY	VENUE
1	1	09.30- 12.30	Registration, Formation of Mentor-mentee groups – Introduction of mentors with-in group.	Class rooms of respective programs
1	1	01.30- 04.30	Screening of Institute video clips of various functions held and Photos of various events, Institution Excursion	Seminar hall
		09.30- 12.30	Prayer- Physical activities such as yoga; Presentation cum Interactive Session with: Important Institution Functionaries like Principal, HoDs etc.	Play ground and seminar hall
2	2	01.30- 04.30	Visit to Central facilities such as Reading room,library,Sport centre, computer centre, hostel, NSS/NCC cell, community development cell functioning in polytechnic	Tour
		01.30- 04.30	Lecturer sessions about importance of NSS/NCC/Youth red cross activities and their contribution towards national building and personality and character development	Seminar hall
		09.30- 12.30	Personality development talk on Human values	Seminar hall
4	4	01.30- 04.30	Interaction with Alumni students of polytechnic of different programs and interaction with few alumina and sharing their experiences	Seminar hall
1	_	09.30- 12.30	Introduction to Swatch bharathabhiyan-Importance of abhiyan-Clean drive in around college	Campus
5	5	01.30- 04.30	Talent hunt-Music/Antakshri/Instrument play/ Dance/Team Activity	College Auditorium
		09.30- 12.30	Talent hunt Activity: Essay/Debate/Best out of Waste/Pick and speak, other	Seminar hall
6	6	01.30- 04.30	Screening of Movie related: personality development, character building, motivational ,Environmental concern, Public health, rural sanitation	College Auditorium
7	7	09.30- 12.30	Exchange of views between students and faculty about their Institute/program/carrier opportunities	Seminar hall
7	/	01.30- 04.30	Games/Sports Activity	Sports ground

8	8	09.30- 12.30	Talk by training and placement cell: Carrier opportunities for diploma students, placement activities in college; placement process	Training and placement cell
		01.30- 04.30	Talents hunt Activity: (Street Play/Mime/Acting/Stand Up Comedy /Dance etc.)	Seminar hall
	09.30- Personality development talks by eminent speakers on - 12.30 Leadership styles/How to handle failures/stress management		Seminar hall	
9	l —		Importance of student union, student union activities, Student insurance, How to make student insurance by Student welfare officer of college	Seminar hall
10	10	09.30- 12.30	Awareness on: Student scholarship- introduction to SSP portal – e-pass portal-Authenticated documents, how to apply in portal: Talk by Taluk/District social welfare officer	Seminar hall
		01.30- 04.30	Local visits to surrounding places/Industry	Tour
11	11	09.30- 12.30	<u>Talk on</u> Respective Program scheme of studies and detail of courses, Diploma examination pattern, Passing and eligibility criteria, attendance requirements by respective program coordinator	Department Class rooms
		01.30- 04.30	Visit to respective programs lab/work shops of institution	Tour
12	12	09.30- 12.30	Awareness camp on human health ,Community health, Personal hygiene-By Local Taluk medical officer/Community medical officer	Seminar hall
12	12	01.30- 04.30	Collection of student feedback on induction program- Make a report Valedictory of two weeks Induction program by collecting student feed back	Seminar hall

Induction Program ( mandatory)	Two- week Duration
Induction program for students to be	Physical activity
offered right at the start of the first	Creative arts
year.	<ul> <li>Universal human values</li> </ul>
	Literacy
	<ul> <li>Proficiency modules</li> </ul>
	<ul> <li>Lectures by Eminent People</li> </ul>
	Visits to Local Areas
	<ul> <li>Familiarization to Dept./Branch &amp; Innovations</li> </ul>

# **5.0 MANDATORY VISITS/WORKSHOP/EXPERT LECTURES:**

- 1. It is mandatory to arrange one industrial visit every semester for the students of each branch.
- 2. It is mandatory to conduct a One-week workshop during the winter break after fifth semester on professional/industry/entrepreneurial orientation.
- 3. It is mandatory to organize at least one expert lecture per semester for each branch by inviting resource persons from domain specific industry.

#### **6.0 EVALUATION SCHEME:**

# A. For Theory Courses:

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The student has to obtain minimum of 40% marks individually both in CIE and SEE to pass. Theory Semester End Exam (SEE) is conducted for 100 marks (3 Hours duration). Based on this grading will be awarded

#### B. For Practical Courses:

The weightage of Continuous Internal Evaluation (CIE) is 60% and for Semester End Exam (SEE) is 40%. The student has to obtain minimum of 40% marks individually both CIE and SEE to pass. The practical Semester End Exam (SEE) is conducted for 100 marks (3 Hours duration exams). Based on this grading will be awarded.

# C. For Summer Internship / Projects / Seminar etc.

1. Evaluation is based on work done, quality of report, performance in viva-voce, presentation etc.

#### Note:

- A. The Continuous Internal Evaluation (CIE) is based on the student's performance in Internal Assessment tests, student activity, mini project, quizzes, assignments, seminars, viva-voce in practical, lab record etc as specified in respective course curriculum.
- B. Major Project/Mini Project: Students can do their major project in Industry or R&D Labor in house. Mini Project is a laboratory oriented course which will provide a platform to students to enhance their practical knowledge and skills by development of small systems/application.
- C. Personality and character development: It is mandatory for the students from 1s semester to enroll in any one of the personality and character development programmes (NCC/NSS/YRC/Yoga/Technical Club) and undergo training for their Personality and character development.
  - National Cadet Corps (NCC).
  - National Service Scheme (NSS) will have social service activities in and around the Institution.
  - Youth Red Cross (YRC) will have activities in and around the institution.
  - Yoga
  - Technical Clubs.
- D. **Internship:** A minimum of 10 credits (400 Hrs) of Internship/ Entrepreneurial activities / Project work/ Seminar and Inter/ Intra Institutional Training may be counted toward three-year diploma programme.
- E. Mapping of Marks to Grades: Each course (Theory/Practical) is to be assigned 100 marks, irrespective of the number of credits, and the mapping of marks to grades may be done as per the following table:

1			2	
4 ]	L	U	, 4	U

Range of Marks	Range of Marks Level		Grade Point
91-100	Outstanding	A+	10
81-90	Excellent	A	09
71-80	Very Good	B+	08
61-70	Good	В	07
51-60	Above Average	C+	06
45-50	Average	С	05
40-44	Satisfactory	D	04
<40	Fail	F	00
Fail due to shortage of atte		F*	00
Fail in Continuous inte	rnal Evaluation (CIE).	F**	00

Note: Those Candidates who have not obtained requisite minimum pass marks in CIE are not eligible to take up SEE in that course until they get requisite minimum pass marks in the CIE. They may re-register for the CIE in the subsequent regular semesters by paying prescribed examination fee.

# **SGPA** and **CGPA** Calculations

Semester Grade Point Average	$\sum$ [(Course Credits earned)X(Grade Points)] for all the courses in that semester				
(SGPA)=	$\Sigma[\mbox{Total Course credits applied}]$ for all the courses in that semester				
Cumulative Grade Point Average	$\sum$ [(Course Credits earned)X(Grade Points) for all courses, excluding those with $F^*/F^{**}$ grades until that semester				
(CGPA)=	$\Sigma$ [Total Course Credits earned] for all Courses excluding those with F*/F** grades until that semester				
Note: The SGPA and CGPA shall be rounded off to 2 decimal points and reported in the semester Diplom marks /grade card.					

A. S	A. SGPA and CGPA Calculations: An illustrative example for one academic year							
Semest er	Course Code	Credits Applied (CA)	Result Grade	Grade Points (GP)	Credits Earned (CE)	Credit points (CP=CE x GP)	SGPA, CGPA	
I	Course 1	4	В	7	4	4x7=28		
I	Course 2	4	F	0	0	0x0=00	SGPA=CP/CA	
I	Course 3	4	Absent (F)	0	0	0x0=00		
I	Course 4	4	A	9	4	4x9=36	=110/22	
I	Course 5	2	A+	10	2	2x10=20		
I	Course 6	2	D	4	2	2x4=08	= 5.00	
I	Course 7	2	A	9	2	2x9=18		
	Total	22			14	110	SGPA = 5.00	

Note: In 1s semester grade/marks card only SGPA is reported. From 2 d semester onwards both SGPA & CGPA will be reported in the grade/marks card.

Semest er	Course Code	Credits Applied (CA)	Result Grade	Grade Points (GP)	Credits Earned (CE)	Credit points(CP=C E x GP)	SGPA, CGPA
II	Course 1	4	В	7	4	4x7=28	SGPA=CP/CA
II	Course 2	4	A	9	4	4x9=36	400/40
II	Course 3	3	D	4	3	3x4=12	=100/19
II	Course 4	3	Absent (F)	0	0	0x0=00	= 5.26
II	Course 5	2	A+	10	2	2x10=20	CGPA
II	Course 6	1	D	4	1	1x4=04	= CP/CE
II	Course 7	2	F	0	0	0x0=00	=(110+136)/ (14+22)
		19			14	100	= 246/36
I Semeste	er Back log c	ourses					=6.83
I	Course 2	4	С	5	4	4x5=20	
I	Course 3	4	D	4	4	4x4=16	
	Total	27			22	136	

Total credits of the semester excluding the credits of the courses under F/F\*/F\*\* grade are considered for the calculation of CGPA of the two consecutive semesters under consideration.

B. CGPA Calculation of the entire programme: An Illustrative Example.								
Semester	I	II	III	IV	V	VI	Total	
Credits of the Semester	22	19	24	24	24	24	137	
∑CP	110	136	184	155	191	188	964	

$$\frac{\text{CGPA} = \frac{[110+136+184+155+191+18]}{22+19+22+24+24+24} = \frac{964}{137} = 7.04$$

# P=Percentage Conversion= (CGPA-0.75) X 10 **Class Declaration:**

After the conversion of final CGPA into percentage of marks (P), a graduating student is declared to have passed in:

- (i) First Class with Distinction (FCD) if  $P \ge 70\%$
- (ii) First Class (FC) if  $P \ge 60\%$  but <70% and
- (iii) Second Class (SC) if P < 60%.

# SCHEME OF STUDIES **DIPLOMA IN ELECTRONICS & COMMUNICATIONS ENGINEERING** (C-20)

#### **CURRICULUM STRUCTURE**

#### I Semester Scheme of Studies - Diploma in Electronics & Communications Engineering [C-20] CIE SEE SGPA and CGPA Hours per week **Fotal contact hrs** CIE **Assigned Grade** Course Category Min Marks for Marks Marks **Fotal Marks Grade Point** Passing Credits (including Teaching Course Code Course Title Т P Max Min Max Min THEORY COURSES ES/EC 20EC11T **Digital Electronics** 20 0 0 50 100 4 20 50 40 PRACTICAL COURSES Only SGPA for 1st Semester BS/SC 20SC02P **Statistics and Analytics** 2 0 4 4 60 24 40 16 100 40 6 **Computer Aided Engineering** 20ME02P ES/ME 2 0 4 6 4 60 24 40 16 100 40 **Graphics** ES/EE/EC 20EC01P Fundamentals of Electrical & 2 0 4 6 4 60 24 40 16 100 40 Electronics Engineering. **AUDIT COURSES** AU/SC **Environmental Sustainability** 2 50 20 50 20 20AU01T Student shall enrol in any one of these activities in 1st semester and shall participate actively. The AU Sports/NCC/NSS/Youth Red student shall obtain 'Participation Certificate' in the activity to get eligible for the award of Diploma. Physical Cross/Yoga/ Technical club. **Activity**

T:- Theory P:- Practical D:- Drawing E:- ElectiveBS- Basic Science:: ES-Engineering Science:: HS-Humanities & Social Science:: AU-Audit Course:: EG: English :: SC: Science Note:

0

14

12

24

270

108

18

72

180

450

180

1. Assigned Grade, Grade Point, SGPA and CGPA to be recorded in the Grade/Marks card.

Total

- 2. AU- Physical Activity- Student participation in the selected physical activity shall be monitored and the participation record shall be maintained by the respective Programme Coordinator (Head of Section).
- 3. Theory course Semester End Examination (SEE) is conducted for 100 marks ( 3 Hours duration)
- 4. Practical course CIE and SEE is conducted for 100 marks (3 Hours duration)

	II Semester Scheme of Studies - Diploma in Electronics &CommunicationsEngineering [C-20]																
Sl. No	ory s t	Course Code	Course Title	Hour	s per	week	ct		Cl Ma		SE Mai			for	ade	ıt	
	Course Category / Teaching Department			L	T	P	Total contact hrs/week	Credits	Max	Min	Max	Min	Total Marks	Min Marks for Passing	Assigned Grade	Grade Point	SGPA and
			TI	HEOR	Y C	OUR	SES										
1	SC/EC	20PM01T	Project Management Skills	2	0	4	6	4	50	20	50	20	100	40			
2	BS/SC	20SC01T	Engineering Mathematics	4	0	0	4	4	50	20	50	20	100	40			ster
			PRA	CTIC	CAL	COU	RSES										Semester
3	EG/SC /EC	20EG01P	Communication Skills	2	0	4	6	4	60	24	40	16	100	40			of 2nd S
4	ES/CS	20CS01P	IT Skills	2	0	4	6	4	60	24	40	16	100	40			CGPA
5	ES/EC	20EC21P	Electronic Components & Devices	2	0	4	6	4	60	24	40	16	100	40			SGPA &
	AUDIT COURSES							\ \cdot \									
6	AU/KA	20KA21T	Kannada-I/ಸಾಹಿತ್ಯಸಿಂಚನ I /ಬಳಕೆಕನ್ನಡ - ।	2	0	0	2	2	50	20	1	,	50	20			
			Total	12	0	20	32	22	340	136	210	84	550	220			

T:- Theory P:- Practical D:- Drawing E:- Elective BS- Basic Science:: ES-Engineering Science:: HS-Humanities & Social Science:: AU-Audit Course:: EG: English :: SC: Science

#### Note:

- 1. Assigned Grade, Grade Point, SGPA and CGPA to be recorded in the Grade/Marks card.
- Theory course Semester End Examination (SEE) is conducted for 100 marks (3 Hours duration)
- Practical course CIE and SEE is conducted for 100 marks (3 Hours duration)

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20EC11T	Semester	I
Course Title	DIGITAL ELECTRONICS	Course Group	Core
No. of Credits	4	Type of Course	Lecture (Theory and Demonstration /practice)
Course Category	EC	Total Contact Hours	4Hrs Per Week 52Hrs Per Semester
Prerequisites	Arithmetic, basic of electronics	Teaching Scheme	(L:T:P)= 4:0:0
CIE Marks	50	SEE Marks	50

# i) RATIONALE

Innumerable logical and complex problems prevail in the real world which need quick and accurate solutions at low cost. The examples include: Counting number of people entering cinema hall; digital clock; playing video; phone call; transmission of document from one place to other; searching your unique ID in Aadhaar database; withdrawing money from ATM; booking railway ticket; and to check if a 25-digit number is a prime-number or not.

Inherent mapping of real-world problems to digital domain, ability of electronic circuits to process digital signals/binary signals and the support of Boolean algebra/relevant mathematical theories for simplification of circuits and reduction of time-complexity have made digital electronics the most suitable option for solving real-world problems. In fact, digital electronics can provide solutions at electronic-speed and low-cost owing to the enhancements in circuit design, fabrication technology and mass production. And the fact that the hardware of computer is digital electronic circuits elucidates the relevance of digital electronics and it learning. In this context, it is very essential to learn the basics of digital electronics to be a competent electronics professional.

# ii) **COURSE SKILL SET**

The goal of the course is to help the student to attain the following industry-need competencies through various teaching-learning processes.

 To understand the simple real-world logical problems and Learning to solve them through established methods.

- ii) Perform analysis, design and troubleshoot well-known simple digital circuits in practical environment.
- iii) To acquire the basic knowledge digital electronic integrated circuits and specifications.

# iii) INSTRUCTIONAL STRATEGY

- 1. Teachers are suggested to take measures to create interest and enhance learning confidence in students.
- 2. Teachers should give examples from daily routine/realistic/real-world as well as relate to engineering/technology applications on various concepts and principles in each topic so that students are made to understand and grasp the concepts and principles. Wherever applicable SI units are followed.
- 3. Demonstration can make the subject interesting and develop scientific temper in the students. Student activities should be planned on all the topics.
- 4. Theory Demonstrate/practice-Activity approach may be followed throughout the course so that learning may be outcome and employability based.
- 5. All demonstrations/Hand-on practices are under simulated environment (may be followed by real environment as far as possible).

# iv) COURSE OUTCOMES (COs)

On successful completion of the course, the students will be able to

CO1	Identify and apply arithmetic and conversion operations on different number systems
CO2	Formulate, simplify and implement simple logic functions
CO3	Build/design and analyze various combinational circuits
CO4	Identify and select digital integrated circuits (ICs) for simple applications

# v) **COURSE TOPICS**

Unit No	Unit Name	Hours
1	Number Systems and Codes	8
2	Basic Logic Circuits	14

3	Arithmetic Circuits	9
4	Multiplexers and Demultiplexer	8
5	Encoders and Decoders	7
6	Logic Families	6
	Total	52 hrs

# vi) COURSE CONTENTS

The following topics/sub topics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets

Course Content Delivery	Learning Method	Duration L:P (Hr)
UNIT -I: NUMBER SYSTEM	MS AND CODES (8Hr)	
1.1Comparison between analog and digital		
signals with real-world examples.	Teaching, examples and	1:0
Number systems: Binary, Octal, Decimal and	exercises	1:0
Hexadecimal. Relevance and examples.		
1.2 Conversion between number systems with examples	Teaching, examples and exercises	2:0
1.3 Arithmetic operations-Addition, Subtraction, Multiplication and Division on binary numbers with examples.	Teaching, examples and exercises	1:0
1.4 Addition and subtraction of Hexadecimal numbers. 1's & 2's complement of binary numbers with examples.	Teaching, examples and exercises	1:0
1.5 Application of Complement numbers: Representation of signed binary numbers and Example for realizing subtraction using addition.	Teaching, examples and exercises	1:0
1.6 Codes: Relevance, types (BCD, Gray, Excess-3, ASCII and EBCDIC) with examples and applications.	Teaching, examples and exercises	1:0

1.7 BCD Addition, Conversion between BCD and Decimal, Binary and Gray Numbers, Decimal and Excess-3 with examples.	Teaching, examples and exercises	1:0
UNIT -2: BASIC LOGIC CIRC	UITS (14Hr)	
2.1 Boolean algebra: Constants, variables, functions, Logic-gates (NOT, OR, AND, NOR, NAND, EX-OR and EX-NOR): Symbol, function, expression and truthtable.	Teaching, examples and exercises. Hands-on demonstration/practic e for all logic gates	1:1
2.2. Boolean identities and laws with proof and examples.	Teaching, examples and exercises	1:0
2.3 De Morgan's and Duality Theorem with proof and examples.	Teaching, examples and exercises  Hands-on demonstration/practic e for De Morgan's theorem	1:1
2.4 Universal gates: Concept, examples, relevance and realization of all logic gates using NAND gate.	Teaching, examples and exercises	1:0
2.5 Realization of all logic gates using NOR gate.	Teaching, examples and exercises	1:0
2.6 Simplification of Boolean expressions using Boolean algebra and build the logic circuit.	Teaching, examples and exercises	1:0
2.7 SOP and POS forms, Conversion into standard SOP and POS forms.	Teaching, examples and exercises	1:0
2.8 Translate SOP and POS expressions into truth-table, Convert truth-table to SOP and POS expressions (maximum 4 variables).	Teaching, examples and exercises	1:0
2.9 SOP to POS & POS to SOP conversion	Teaching, examples and exercises	1:0

2.10 Karnaugh Map: Need, K-map for 2 variable, 3 variable and 4 variable Boolean expression.	Teaching, examples and exercises	1:0
2.11 Simplification of Boolean expression using K- map and realization of logic circuit for 2 and 3 variable.	Teaching, examples and exercises	1:0
2.12 Simplification of Boolean expression using K- map and realization of logic circuit for 4 variable	Teaching, examples and exercises	1:0
UNIT -3: ARITHMETIC	C CIRCUITS (9Hr)	
3.1 Features of combinational circuits and examples.  Half adder (HA): Concept, truth-table, logical expression, gate-level implementation and application.	Teaching, examples and exercises	1:0
3.2 Full adder (FA): Concept, truth-table, logical expression, gate-level implementation and application. List of FA ICs.	Teaching, examples and exercises.  Hands-on demonstration/practice FA using gates	1:1
3.3 Half Subtractor (HS): Concept, truth-table, logical expression, gate-level implementation and application.	Teaching, examples and exercises	1:0
3.4 Full Subtractor (FS): Concept, truth-table, logical expression, gate-level implementation and application.	Teaching, examples and exercises	1:0
3.5 Serial & Parallel adders: Concept, comparison & applications.	Teaching, examples and exercises.	1:0
3.6 Three-bit parallel adder circuit: Given the circuit, analyze it's working.	Teaching, examples and exercises.	1:0
3.7 Two-bit magnitude comparator: Concept, truth-table, logical expression, gate-level implementation and application. Identify ICs	Teaching, examples and exercises.  Hands-on demonstration/practice of 2-bit Magnitude Comparator using IC or gate-levelcircuit.	1:1

UNIT -4: MULTIPLEXERS AND	DEMULTIPLXERS (8 Hr)	
4.1Multiplexers (Mux): Concept, relevance and applications, 2:1 Mux: Symbol, truthtable, logical expression, gate-level implementation and application. Identify ICs	Teaching, examples and exercises	1:0
4.2High-order Mux: Concept, examples (4:1, 8:1, and 16:1), Relation between number of inputs and control lines.	Teaching, examples and exercises.  Hands-on demonstration/practice:  4:1 using 2:1 Mux or 8:1 using 4:1 Mux, using ICs.	1:1
4.3 Realization of high-order (4:1) Mux using low-order (2:1) Mux. List Mux ICs.	Teaching, examples and exercises	1:0
4.4Realization of logic gates and simple logic equations using multiplexers. (Max. 3variables)	Teaching, examples and exercises	1:0
4.5Demultiplexer (Demux): Concept, relevance and applications. 1:2 Demux: Symbol, truth-table, logical expression, gate-level implementation and application.	Teaching, examples and exercises	1:0
4.6High-order Demux: Concept and examples (1:4, 1:8, 1:16), relation between number of outputs and control lines.  Analysis of Demux: Given 1:4 Demux, write logical expressions and truth table.	Teaching, examples and exercises Hands-on practice for 1:8 using 1:4 Demux, using ICs	1:1
UNIT -5: ENCODERS AN	D DECODERS (7 Hr)	
5.1 Encoders and Decoders: Relevance and applications.	Teaching, examples and exercises	1:0
5.2 Decimal-to-BCD encoder: Logic diagram, working, truth-table and applications. List ICs	Teaching, examples and exercises.  Hands-on demonstration/practic e of an Decimal to BCD encoder	1:1

	T	1
5.3 Priority Encoder: Relevance, Logic diagram, working and Truth Table. Identify IC	Teaching, examples and exercises	1:0
5.4 BCD-to-Decimal decoder: Logic diagram, working and truth-table	Teaching, examples and exercises	1:0
5.5 Seven-segment display: Principle and types.  Identify ICs for 7-segment display and Decoder.	Teaching, examples and exercises.	1:0
5.6 BCD-to-seven segment decoder: Logic diagram, working and truth table	Teaching, examples and exercises. Hands-on demonstration/ practice on BCD to Seven Segment decoder	0:1
UNIT -6: LOGIC FA	MILIES (6Hr)	
6.1 ICs: Concept, advantages and disadvantages.  IC classification: Based on scale of integration. Concept, need and types of logic families	Teaching, examples and exercises	1:0
6.2 Logic family specifications: Propagation delay, fan-out, fan-in, power dissipation, noise margin, speed and speed-power product.	Teaching, examples and exercises.  Demonstration of IC	1:0
6.3 IC data sheet: Identify the specifications in typical standard TTL IC	datasheet interpretation	0:1
6.4 Features of Standard TTL, CMOS & ECL. Identify TTL/CMOS/ECL NAND gate ICs and compare their specifications.	Teaching, examples and exercises.	1:0
6.5 Interfacing between TTL and CMOS: Need, concept and precautions.  Handling of ICs and ESD.	Teaching, examples and exercises.  Demonstration of ICs' handling / placement /removal on IC base/ sockets with anti-ESD gloves	1:1

# > SUGGESTED SPECIFICATION TABLE WITH CO'S, HOURS & MARKS.

Sl No.		UNITS	TEACHING	DISTRIBUTION (THEORY MARKS )				
SIT (0.	COURSE OUTCOME	LINKED	LINKED HOURS		U LEVEL	A LEVEL	TOTAL	
1	Identify and apply arithmetic and conversion operations on different number systems	1	08	5	10	15	30	
2	Formulate, simplify and implement simple logic functions	2	14	15	20	15	50	
3	Build/design and analyze various combinational circuits	3,4,5	24	25	35	40	100	
4	Identify and select digital ICs for simple applications	6	06	10	5	5	20	
Cog	Cognitive-level marks share (Total) →		52	55	70	75	200	

**Legends:** R = Remember; U = Understand; A = Apply and above levels (Bloom's revised taxonomy).

# > MAPPING OF COS, POS, COGNITIVE LEVELS, LECTURES AND PRACTICES.

CO No.	Course Outcome	POs Mapped	Units Linked	Cognitive Level R/U/A	Lecture Sessions in Hrs	Demonstration/ Hands-on practice in Hrs	TOTAL
CO1	Identify and apply arithmetic and conversion operations on different number systems	1,2,5	1	R/U/A	08	0	08
CO2	Formulate, simplify and implement simple logic functions	1,2,3,4	2	R/U/A	12	2	14
CO3	Build/design and analyze various combinational circuits	1,2,3,4	3,4,5	R/U/A	18	6	24
CO4	Identify and select digital ICs for simple applications	1,5	6	R/U/A	04	2	06
				Total	42	10	52

#### vii) **UNIT SKILL-SETS**

U ni	Unit Name	Skill Set
t		Set
1	Number Systems and codes	Comprehend the number systems, operate (conversion, addition and subtraction) on different number systems, identify and select the codes for different applications
2	Boolean Algebra	Translate the problem to truth-table, simplify thelogical expressions using Boolean identities/ laws/K-maps, and implement the logical functions.
3	Arithme tic Circuits	Given simple arithmetic problems, solve using digital circuits and vice-versa (analysis). Identify arithmetic circuits ICs for simple applications.
4	Multiplexers and Demultiplexer	Solve simple multiplexing and demultiplexing problems, viceversa (analysis). Identify multiplexing ICs for simple multiplexing applications.
5	Encoders and Decoders.	Solve simple coding/decoding problems, and identify coding ICs for simple coding applications.
6	Logic Families	Identify and select the ICs from different IC families based-on application specifications.

#### viii) **MAPPING BETWEEN COS AND POS**

Cour	COs		Prog	ramn	ie Out	comes	(POs	)
se	COS	1	2	3	4	5	6	7
50	С	3	2	0	0	1	0	0
Digital Electronics	1							
	С	3	2	1	1	0	0	0
	0							
	2							
	С	3	2	1	2	0	0	0
	0							
	3							
	С	3	0	0	0	1	0	0
	0							
	4							

#### Legends:

**Level 3-** Highly Mapped, **2-**Moderately Mapped, **1-**Low Mapped, **0-** Not Mapped

# ix) **SUGGESTED LEARNING RESOURCES**:

# **Reference Books**

- i) Digital fundamentals Thomas L. Floyd, PEARSON EDUCATION publication, Eleventh edition Global Edition, ISBN 10: 1-292-07598-8, ISBN 13:978-1-292-07598-3.
- ii) Digital Electronics principles and integrated circuits. Anil K. Maini. Wiley publications, first edition. ISBN:978-81-265-1466-3.
- iii) Digital principles and applications. Donald P Leach, Albert Paul Malvino, GoutamSaha, McGraw Hill Publisher, 7th edition, ISBN:978-0-07-014170-4.
- iv) Digital Systems-principles and applications. Ronald J. Tocci, Neal S.Widmer, Gregory L. Moss, Prentice Hall Publications, 8th edition, ISBN:0-13-085634-7.
- v) Digital Computer Fundamentals,-Thomas C Bartee, McGraw-Hill Publisher,4th edition. ISBN 0-07-003892-9.

# Web-based/onlineResources

- https://www.electronics-tutorials.ws/
- 2. <a href="https://learn.sparkfun.com/">https://learn.sparkfun.com/</a>
- 3. <a href="https://www.allaboutcircuits.com/textbook/digital/">https://www.allaboutcircuits.com/textbook/digital/</a>
- 4. <a href="http://electronicstheory.com/COURSES/ELECTRONICS/e101-1.htm">http://electronicstheory.com/COURSES/ELECTRONICS/e101-1.htm</a>
- 5. <a href="https://www.gadgetronicx.com/electronic-circuits-library/">https://www.gadgetronicx.com/electronic-circuits-library/</a>
- 6. <a href="https://www.electronics-lab.com/">https://www.electronics-lab.com/</a>
- 7. <a href="https://learn.adafruit.com/">https://learn.adafruit.com/</a>
- 8. <a href="https://www.instructables.com/circuits/">https://www.instructables.com/circuits/</a>
- 9. https://www.digitalelectronicsdeeds.com/
- 10. https://www.electrical4u.com/digital-electronics/
- 11. https://www.tutorialspoint.com/digital circuits/index.htm

# x) Major Equipment/Instruments

- 1. Digital trainer kits.
- 2. Electronics simulation software's.
- 3. Computers.
- 4. IC tester, logic probes.

# xi) SUGGESTED LIST OF STUDENTS ACTIVITYS for CIE

Note: The following activities or similar activities (as suggested by teacher/ identified by student in coordination with teacher) for assessing CIE (IA) for 20 marks (any one)

- 1. Simulate the working of a logic circuit using a suitable software tool.
- 2. Performing hands-on practice on design and simulation of digital circuits.
- 3. Motivate students to take case study on different ASICs (Application specific ICs) digital circuits to inculcate self and continuous learning.
- 4. Open end activities like

Simulate a realistic digital circuit containing combination of logic gates. Collect the specification sheets of various logic ICs & prepare a Report. Record the best practices used in the disposal of E-waste and Precautions in the operation of digital systems.

- 5. Draw the pin diagram of IC's used for (a) Basic Gates (b) Combinational circuits.
- 6. Realize higher order Multiplexers/Demultiplexerusing lower order Multiplexers/Demultiplexer and experiment them under simulated environment.
- 7. Collect the real-world applications where combinational digital circuits are involved.

### **Execution Mode**

- Maximum of 4 students in each batch.
- Write qualitative report not exceeding 8 pages; one report per batch.
- Each of the activity can be carried off class, and shall be presented to the teacher using suitable presentation mode
- Assessment shall be made based on quality of activity presentation/demonstration and report (Equal weightage for Information collection/Application, execution, report, and presentation and role in team) or the rubrics table may be followed for assessment purpose.

# xii) COURSE ASSESSMENT AND EVALUATION CHART

Sl. No	Assessme nt	Mode	Schedul e	Duratio n (Minute s)	Max. mark s	Conversion of Max Marks
1.	CIE-IA1	Written-test	3 <sup>d</sup> Wee k	80	30	<b>A</b> =(IA1+IA3+IA6)/3
2	CIE-IA2	MCQs/Quiz	5 Wee k	60	20	Max. of A is 30 <b>B</b> =(IA2+IA4+IA5)/3
3.	CIE-IA3	Written-test	7 Wee k	80	30	Max. of B is 20
4	CIE-IA4	Open- Book Written-	9 Wee k	60	20	A+B=50

		test				
5	CIE-IA5	Activity/Assignme nt	11 Wee k	60	20	
6	CIE-IA6	Written-Test	13 Wee k	80	30	
				Total CIE	50	50
7.	SEE	Written	BTE Schedu le	3 hrs	100	50
	•		Tota	(CIE+SEE)		100

#### Note:

- i) Semester-end exam (SEE) is conducted for 100marks.
- ii) Continuous internal evaluation (CIE) is for 50marks.
- iii) IA1, IA3 and IA6 tests shall be conducted for 30 marks each; average of these IAs will b A.
- iv) IA2 (Quiz/MCQs), IA4 (Open-book test) and IA5 (assignment/student activity) are conducted for 20 marks each; average of these IAs will be B. Appropriate rubrics may be used for evaluation. Open-book written test shall be to assess the analytical, reasoning, evaluation and creative skills/abilities of students.
- v) Total CIE is average of A and B; any fraction shall be rounded-off to the next higher digit.
- vi) Lecture: Practice sessions shall begin only after two weeks of Induction Program in First semester. The schedule of assessment week shall be counted only after 2 weeks of Induction Program.

# **RUBRICS FOR ACTIVITY**

	RUBRICS FOR ACTIVITY (10marks) (Example only) Concerned faculty shall device appropriate rubrics as per the activity									
Dimensio	Beginni ng	Developi ng	Satisfacto ry	Go od	Exempla ry	Studen t				
n	4	8	12	16	20	Score				
Collecti on of data	Does not collect any information relating to the topic	Collects very limited information ; some relate to the topic	Collect much informatio n; but very limited relate to the topic	Collects some basic informatio n; most refer to the topic	Collects a great deal of informati on; all refer to the topic	8				
Fulfil l team 's roles & dutie s	Does not perform any duties assigned to the team role	Performs very little duties but unreliable.	Performs very little duties	Perfor ms nearly all duties	Performs all duties of assigned team roles	6				

Shar es work equa lly	Always relies on others to do the work	Rarely does the assigned work; often needs reminding	Usually does the assigned work; rarely needs reminding	Norma lly does the assign ed work	Always does the assigned work without having to be reminded.	8
Listen to other Team mates	Is always talking; never allows anyone else to speak	Usually does most of the talking; rarely allows others to speak	Talks good; but never show interest in listening others	Listens, but sometimes talk too much	Listens and speaks a fair amount	8
		-	Average	/ Total Marks:	(8+6+8+8)/4	7.5 = 8 marks

# xiii) Model Question Paper I A Test (CIE)

Progra		Semes				
Course			arks :3			
Course Code : Duration : 1 Hr. 20min					nutes	
Name o		t :I/II/				
	Note: Answer one full question from each section. One full question carries 10 marks.					
Qn.	Questi	С	С	P	Mar	
No	on	L	0	0	ks	
	Section	•	•	•	•	
	-1					
1.a)						
b)						
c)						
2.a)						
b)						
c)						
	Section					
	-2					
3.a)						
b)						
c)						
4.a)						
b)						
c)						

	Section -3		
5.a)			
b)			_
c)			
6.a)			_
b)			
c)			

**Model Question Paper** 

# **Model Question Paper Semester End Examination**

Programme:	Semester: I
Course :	Max Marks: 100
Course Code:	Duration: 3 Hrs

**Instruction to the Candidate:** Answer one full question from each section. One full question carries 20 marks.

Qn.N	Question	CL	CO	Marks
o				
	Section-1			
1.a)				
b)				
2.a)				
b)				
l	Section-2	<u> </u>		
3.a)				
b)				
4.a)				
b)				
l .	Section- 3	l	I	1

5.a)			
b)			
6.a)			
b)			
	Section-4		
7.a)			
b)			
8.a)			
b)			
	Section-5		
9.a)			
b)			
10.a)			
b)			

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20SC02P	Semester	I/II
Course Title	STATISTICS AND ANALYTICS	Course Group	Core
No. of Credits	4	Type of Course	Lecture and practice
Course Category	Practice	Total Contact Hours	6 Hrs. Per Week
		nours	78 Hrs. Per Semester
Prerequisites	10 <sup>TH</sup> LEVEL MATHEMATICS	Teaching Scheme	(L: T:P)-1:0:2
CIE Marks	60	SEE Marks	40

#### **RATIONALE**

Statistics and analytics help the learner to use the proper methods to collect the data, employ the correct analyses, effectively present the results and conduct research, to be able to read and evaluate journal articles, to further develop critical thinking and analytic skills, to act as an informed consumer and to know when you need to hire outside statistical help. The python language is one of the most accessible programming languages available because it has simplified syntax and not complicated, which gives more emphasis on natural language.

# **COURSE OUT COMES**

At the end of the course, student will be able to

CO1	Understand the tools of data collection, classification and cleaning of data.
CO2	Able to summarize the given statistical data
CO3	Understand the measure of location and dispersion of data.
CO4	Learn the basics of Python programming.

# **DETAILS OF COURSE CONTENT**

The following topics/subtopics is to be taught and assessed in order to develop Unit Skill Sets for achieving CO to attain identified skill sets.

UNIT NO	Unit skill set (In cognitive domain)	Topics/Subtopics	Hours L-T-P
UNIT-1 STATISTICAL DATA COLLECTION AND TYPES	<ol> <li>Able to collect statistical data.</li> <li>Able to distinguish the data types.</li> <li>Understands the usage of data collection tools</li> <li>Able to specify problem statement for data collection</li> <li>Able to collect data pointing the root cause of the problem statement.</li> </ol>	a Definition of data and classification (qualitative quantitative discrete and continuous data). b Data collection tools iv) Questionnaires. v) Survey. vi) Interviews. vii) Focus group discussion. 1.3 Data cleaning.	4-0-8
UNIT-2 SUMMARIZATION OF DATA	<ul> <li>6. Sketches bar, pie and histograms on Microsoft Excel spread sheet.</li> <li>7. Sketches frequency curve and frequency polygon for the data set on Microsoft Excel spread sheet.</li> <li>8. Sketches bar, pie and histograms on Microsoft Excel spread</li> </ul>	a Descriptive statistics viii) Datatabulation(frequency table ix) Relative frequency table. b Grouped data x) Bar graph xi) Pie chart xii) Line graph xiii) Frequency polygon xiv) Frequency curve xv) Relative frequency polygon xvi) Histograms xvii) Box plot xviii) Leaf-stem plot To be done in Microsoft excel.	8-0- 16

	sheet. 9. Sketches frequency		
	curve and frequency polygon for the data set on Microsoft Excel spread sheet.		
UNIT-3  MEASURE OF LOCATION AND DISPERSION	<ul> <li>Able to determine the descriptive statistical variables using Microsoft Excel.</li> <li>Able to determine the absolute measures of dispersion of the given data set.</li> <li>Explain the symmetry and asymmetry of the distributed data.</li> </ul>	<ul> <li>a Determination of central tendencies Range, Mean, Mode and Median for the data in Microsoft excel.</li> <li>b Determination of absolute measures of dispersion for data like range quartile deviation, mean deviation, standard deviation and variance in Microsoft Excel.</li> <li>c Skewness and kurtosis graphs in Microsoft excel and interpretations of results.</li> </ul>	6-0- 12
UNIT-4 INTRODUCTION TO PYTHON PROGRAMMING	➤ Able Install and run the Python interpreter. Create and execute Python programs. ➤ Understand the concepts of file I/O. ➤ Able to read data from a text file using Python. ➤ Learn variable declarations in Python. ➤ Learn control structures.	<ul> <li>4.1 Introduction to PYTHON.</li> <li>4.2 Syntax of PYTHON.</li> <li>4.3 Comments of PYTHON.</li> <li>4.4 Data types of PYTHON.</li> <li>4.5 Variables of PYTHON.</li> <li>4.6 If-else in PYTHON.</li> <li>4.6 Loops in PYTHON.</li> <li>4.7 Arrays and functions in PYTHON.</li> </ul>	8-0- 16

Diploma in	<b>Electronics &amp;</b>	. Communica	tions Engine	eering
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CI	The state of the s						
SL NO	Practical outcomes/Practical exercises	Unit no	PO	СО	L:T:P		
1	Prepare a questionnaire (closed end) containing 25 questions for a specified problem statement: for example experience of an individual in a restaurant.	1	1,2,4,5,7	1	0:0:2		
2	Prepare a Google form for a specified problem statement to collect the dataset. (for example questionnaire to conduct online quiz)	1	1,2,4,5,7	1	0:0:2		
3	Send out a survey on your problem statement to number of 50 (By Google forms) and collect the data.	1	1,2,4,5,7	1	0:0:2		
4	Remove duplicate or irrelevant observations. Remove unwanted observations from the dataset provided, including duplicate observations or irrelevant observations.	1	1,2,4,5,7	1	0:0:2		
5	In Microsoft Excel spread sheet draw the frequency distribution table for the given data (data set should contain minimum 50 data).	2	1,2,4,5,7	2	0:0:2		
6	In Microsoft Excel spread sheet draw the relative frequency distribution table for the given data (data set should contain minimum 50 data).	2	1,2,4,5,7	2	0:0:2		
7	Using Microsoft Excel spread sheet plot bar graph for the data collected from 100 people( for example, conduct a survey on the favorite fruit of a person in your locality(restricting to 5 to 6 fruits). Explain the bar graph with minimum 30 words.	2	1,2,4,5,7	2	0:0:2		
8	Using Microsoft Excel spread sheet plot pie chart for the data collected from 50 people( for example, conduct a survey on the smokers with respect to their ages in your locality. Explain the pie chart with minimum 30 words.	2	1,2,4,5,7	2	0:0:2		
9	Using Microsoft Excel spread sheet draw a line graph for the given dataset.	2	1,2,4,5,7	2	0:0:2		
10	Using Microsoft Excel spread sheet draw frequency polygon and frequency curve for the data collected from 50 people. (For example, marks obtained by the students in your class in 5 subjects in previous examination). Explain your observations from the graph in minimum 30 words.	2	1,2,4,5,7	2	0:0:2		
11	Using Microsoft Excel spread sheet construct a box plot for the given dataset. (For example dataset can be the number of passengers in a flat form at different time in a day).	2	1,2,4,5,7	2	0:0:2		
12	Using Microsoft Excel spread sheet construct a leaf plot for the given dataset. Explain the graph with minimum 30 words.	2	1,2,4,5,7	2	0:0:2		

Total Hours							
26	Write a python program to create a labeled pie chart using matpoltlib. pyplot.	4		1,	2,4,5,7	4	0:0:2 <b>0:0:52=5</b>
25	Write a python program to create a labeled bar graph using matpoltlib. pyplot.	4		1,	2,4,5,7	4	0:0:2
24	Write a python program to display student marks from the record.	4		1,	2,4,5,7	4	0:0:2
23	Write a python program to find the variance and standard deviation for the given data	4		1,	2,4,5,7	4	0:0:2
22	Write a python program to find whether the number is odd or even.	4		1,	2,4,5,7	4	0:0:2
21	Write a python program to find the sum of first 10 natural numbers.	4		1,	2,4,5,7	4	0:0:2
20	Write a python program to add 2 integers and 2 strings and print the result.	4		1,	2,4,5,7	4	0:0:2
18	Using Microsoft Excel spread sheet draw a Skewness graph and kurtosis graph for randomly generated dataset.	3		1,	2,4,5,7	3	0:0:2
17	Collect the data of two wheeler (with a rider and a pillion) crossing a busy junction in your locality in the peak hours (problem statement can be changed according to priorities of the tutor) and determine the variance of the data in Microsoft excel spread sheet and brief your inference with less than 30 words.	3		1,	2,4,5,7	3	0:0:2
16	Collect the data of any 2 livestock population from 50 different houses in your locality (problem statement can be changed according to priorities of the tutor) and determine standard deviation for both the two separately in Microsoft excel spread sheet and brief your inference with less than 30 words.	3		1,	2,4,5,7	3	0:0:2
15	Collect the current yield of a crop from 50 different persons (problem statement can be changed according to priorities of the tutor) in your locality and determine mean deviation and Quartile deviation in Microsoft excel spread sheet and brief your inference with less than 30 words.	3		1,	2,4,5,7	3	0:0:2
14	Generate a 50 random data sample (even and odd number dataset) using Microsoft Excel spread sheet and determine the range and Quartiles.	3		1,	2,4,5,7	2	0:0:2
13	Using Microsoft Excel spread sheet find the Mean, Mode and Median for the data (univariate data) given and also represent them in a Histogram.		3		1,2,4,5,7	2	0:0:2

# MAPPING OF CO WITH PO

со	Course Outcome	PO Mapped	Experi ment Linked	Cognitive Level R/U/A	Tutorial & Practical Sessions in Hrs.	TOT AL
CO1	Understand the tools of data collection, classification and cleaning of data.	1,2,4,5,7	1-4	A	12	12
CO2	Able to summarize the given statistical data	1,2,4,5,7	5-12	A	33	33
CO3	Understand the measure of location and dispersion of data.	1,2,4,5,7	13-18	A	12	12
<b>CO4</b>	Learn the basics of Python programming.	1,2,4,5,7	19-26	A	21	21
					78	78

Course	CO's	Programme Outcomes (PO's)								
	COS	1	2	3	4	5	6	7		
Statistics & Analytics	CO1	3	3	0	3	3	0	3		
	CO2	3	3	0	3	3	0	3		
	CO3	3	3	0	3	3	0	3		
	CO4	3	3	0	3	3	0	3		

Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0- Not Mapped

### **SUGGESTED LEARNING RESOURCES:**

- 1. Statistical Analysis with Excel For Dummies (For Dummies Series) Paperback Import, 9 April 2013 by <u>Joseph Schmuller</u> (Author)
- 2. https://www.brianheinold.net/python/A Practical Introduction to Python Progra mmingHeinold.pdf
- 3. http://www.bikeprof.com/uploads/9/0/6/5/9065192/excel stats handout npl.pdf
- 4. https://adminfinance.umw.edu/tess/files/2013/06/Excel-Manual1.pdf
- 5. <a href="https://www.brianheinold.net/python/A Practical Introduction to Python Progra">https://www.brianheinold.net/python/A Practical Introduction to Python Progra</a> mmingHeinold.pdf
- 6. Introduction to Python programming for beginners by Vivian Baily Kindle edition.
- 7. PYTHON PROGRAMMING: Python programming: the ultimate guide from a beginner to expert by Clive Campbell.
- 8. Open source for python: <a href="https://hub.gke2.mybinder.org/user/jupyterlab-">https://hub.gke2.mybinder.org/user/jupyterlab-</a> jupyterlab-demo-zfkdwy4y/lab

### SUGGESTED LIST OF STUDENT ACTIVITY

Note: The following activities or similar activities for assessing CIE (IA) for 10 marks (Any one)

Describe the data collection activity itself (interviews, surveys, library research, etc.) AND why this specific form of data collection was chosen. Be sure to explain why you think this kind of data will help you in your design process. Also be sure to provide details about the activity: how many interviews, how long they took, where they took place, how many questions asked in a survey, how many respondents, etc.

Present the results of your data collection. You do not have to have completely analyzed all your data, but do make sure you present the results of your research. If you did a survey, please attach a copy of the survey as an appendix; if you did interviews, please attach a copy of the interview questions.

Discuss any preliminary analysis of your data. What have you learned thus far from the data should be discussed from an analytical perspective (rather than a data dump). For example, if you surveyed people about their use of the local bus system, and 90% of your respondents said they take the bus when it is raining, and 60% of your respondents said they usually wait more than 10 minutes for a bus, think about what this teaches you rather than just the information itself. In this instance. you can see that people are generally waiting for several minutes in the rain for a bus, so a covered bus stop might be a good idea. Keep in mind that your findings from data should lead directly to the conclusions you make about your design recommendations. This is the time to begin thinking very specifically about your research in those terms. This is also an opportunity to think about your definition of "better" and how it applies to your design goals and your choice of research activities (for example, if you are choosing to make something better by making it cheaper, maybe you are interviewing people to see how much loss of functionality or decrease in features for a technology they are willing to tolerate).

https://ils.unc.edu/courses/2013 spring/inls541 001/Assignments.html#Assign ment 9

2

	DOWNLOAD a dataset from the above link and use data visualization tools to
	analyze it.
3	Acquire the dataset from <a href="https://www.kaggle.com/datasets">https://www.kaggle.com/datasets</a> (For example acquire the data of IPL ball by ball scores and find the standard deviation and variance of score of a batsmen) and clean the data for the root cause of the problem statement and summarize the date and explain the inference.

# **COURSE ASSESSMENT AND EVALUATION CHART**

Meth	What		То	When/Wh	Max	Evidence	Course
od			whom	ere	Mar	collected	outcomes
				(Frequenc y in the	ks		
				course)			
_	CIE	Mode	Studen	Two IA	20	Blue Book	1,2,3.
	(Continuo	ls	ts	Tests			
E	us Internal			(Written)			
DIRECT ASSESSMENT	Evaluation			Three Skill	20	Model	1,2,3
SIN	)			tests			
SES				Student	20	Model/Rep	
ASS				Activity		ort	
E				TOTAL	60		
EC	SEE	End		End of the	100	Models	1,2,3
) IR	(Semester	Exam		course			
-	End						
	Examinati						
F .	on) Student Fee	l odback	Studen	Middle of		Feedback	1,2,3,
E	on cour		ts	the course		forms	Delivery of
ME	on course			the course		1011113	course
SSE	End of Co	urse		End of the		Questionnai	1,2,3
SSI	Surve	У		course		res	Effectiveness
Y J							of
<u>5</u>							Demonstratio
I							ns&
INDIRECT ASSESSMENT							Assessment
							Methods

Sl.No	Assessment	Duration	Max	Conversion
			marks	
1	CIE Assessment 1 (Written Test -1-theory)	60	20	Average of
1	- At the end of <b>3<sup>rd</sup> week</b>	minutes	20	two written
2	CIE Assessment 2 (Written Test -2-theory)	60	20	tests
Z	- At the end of <b>13</b> th <b>week</b>	minutes	20	20
3	CIE Assessment 3 (Skill test)	3 Hrs	20	
J	- At the end of <b>5<sup>th</sup> week</b>	31113	20	Average of
4	CIE Assessment 4 (Skill test)	3 Hrs	20	three skill
4	- At the end of <b>7</b> <sup>th</sup> <b>week</b>	31113		tests
5	CIE Assessment 5 (Skill test)	3Hrs	20	20
J	- At the end of <b>9</b> <sup>th</sup> <b>week</b>	31118	20	
6	CIE Assessment 6 (Student activity)	_	- 20	20
0	- At the end of <b>11<sup>th</sup> week</b>	_	20	20
7	7 Total Continuous Internal Evaluation (CIE) Assessment			60
8	Semester End Examination (SEE) Assessment	21140	100	40
δ	(Practical Test)	3Hrs	100	40
	100			

### Note:

- 1. CIE written test is conducted for 20 marks (Two sections). Each section shall have two full questions of same CL, CO. Student shall answer one full question (10 marks) from each section.
- 2. CIE Skill test is conducted for 100 marks (3 Hours duration) as per scheme of evaluation and the obtained marks are scaled down to 20 marks.
- 3. SEE is conducted for 100 Marks (3 Hours duration) as per scheme of evaluation.

# **MODEL QUESTION PAPER**

# CIE, SKILL TEST AND SEMESTER END EXAMINATION

**Course & Programme:** Common to all Engineering Programmes. Semester: II

**Subject:** Statistics and Analytics Practice Max Marks: 100 Course Code: 20SC21P **Duration**: 3Hrs

# Instruction to the Candidate: Answer both questions

Qn.No	Question	CL	СО	PO	Marks
1	For the given ungrouped data set plot the bar graph by grouping the data in Microsoft excel spread sheet and interpret the obtained results. (Dataset. bar graphs and interpretation have to be entered in the answer script).  OR  Generate a random data set in Microsoft excel spread sheet containing 50 data and find the mean mode and median in Microsoft excel spread sheet and interpret the obtained results. (Dataset, bar graphs and interpretation have to be entered in the answer script).	A	2,3	1,2,4,5,7	50
2	Write the python program to enter two integers and two strings and to print the sum two integers and two strings.	A	4	1,2,4,5,7	50

Questions are not framed from Unit 1 in the final SEE. Short questions can only be asked from that unit.

### SCHEME OF EVALUATION FOR BOTH CIE AND SEE

Sl. No	Particulars	Marks
1	Short questions from Unit 1	10
2	Observation	30

3	Conduction	20	
4	4 Output and Interpretation of result		
5	5 Viva-voce		
	Total	100	

# **EQUIPMENT LIST**

# FOR STATISTICS AND DATA ANALYTICS LAB

 $2\ laboratories.$  Each containing  $30\ computers$  (Desktop) with the following system requirements.

	SYSTEM REQUIREMENTS						
SL NO	REQUIREMENTS	MINIMUM	RECOMMENDED				
1	RAM	4GB FOR FREE RAM	8GB OF TOTAL SYSTEM RAM				
2	DISK SPACE	2.5 GB AND 1 GB FOR CACHES	SSD DRIVE WITH AT LEAST 5 GB OF FREE SPACE				
3	MONITOR RESOLUTION	1024x768	1920×1080				
4	OS(OPERATING SYSTEM)	OFFICIALLY RELEASED 64-BIT VERSIONS OF THE FOLLOWING: MICROSOFT WINDOWS 8 OR LATER	LATEST 64-BIT VERSION OF WINDOWS				

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20ME02P	Semester	I/II
Course Title	Computer Aided Engineering Graphics	Course Group	CS,EC,EE & Other allied branches
No. of Credits	4	Type of Course	Lecture & Practice
Course Catagory	D.C.	Total Contact House	6Hrs Per Week
Course Category	PC	Total Contact Hours	78Hrs Per Semester
Prerequisites	Enthusiasm to learn the subject/Visualizing/Creativity	Teaching Scheme	(L: T:P) = 1:0:2
CIE Marks	60	SEE Marks	40

#### 1. COURSE RATIONALE:

Engineering Drawing is an effective language of engineers. It is the foundation block which strengthens the engineering & technological structure. Moreover, it is the transmitting link between ideas and realization.

### 2. LIST OF COMPETENCIES:

The course content should be taught and implemented with the aim to develop different types of skills leading to the achievement of the following competencies:

- 1. Prepare engineering drawings both manually and using CAD with given geometrical dimensions using prevailing drawing standards and drafting instruments.
- 2. Visualize the shape of simple object from orthographic views and vice versa

### 3. COURSE OUT COMES:

CO1	Adopt the standards, dimensioning and construct appropriate drawing scales, in technical					
	drawing development.					
CO2	Visualize objects in all planes and learn displaying techniques for graphical					
	communication in design process.					
CO3	Sketch orthographic projections into isometric projections and vice versa.					
CO4	Use computer software and Apply computer aided drafting tools to create 2D /3 D					
	engineering drawings					

### 4. INSTRUCTIONAL STRATEGY:

- 1. Teacher should show model of real of the component/part whose drawing is to be made. Emphasis should be given on cleanliness, dimensioning and layout of sheet.
- 2. Focus should be on proper selection of drawing instruments and their proper use.
- 3. The institute should procure AutoCAD or other engineering graphics software for practice in engineering drawings.
- 4. Separate labs for practice on Engineering graphics Software should be established.

# **5 COURSE DETAILS:**

The following topics/sub topics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets

Unit	Major Learning Topics and Sub- Topics	Outcomes (in cognitive domain)	Hours L-T-P
UNIT-1  Basic elements of Drawing	1.1 List the different drawing instruments and application 1.2 Convention of lines and its application (Thick, Thin, Axis etc.) 1.3 Practice use of drawing instruments 1.4 Representative fraction Scales - Full Scale, Reduced Scale and Enlarged Scale 1.6 Dimensioning a) Aligned system and Unidirectional system in the Sketches b) Chain dimensioning and Parallel dimensioning 1.7 Construct different polygons	instruments and materials.  2. Equipment's-types, specifications, method to use them, applications.  3. Instruments-types, specifications, methods to use themandapplications.  4. Pencils-grades, applications, Different typesoflines.  5. Scaling technique used	4-0-8
UNIT-2 CAD Interface	22.1 Introduction to CAD- Hardware requirements. 2.2 Various CAD software available 2.3 Familiarization of CAD window - Commands like New file, Saving the file, opening an existing drawing file, Creating templates 2.4 Setting up new drawing: Units, Limits, Grid, Snap. Standard sizes of sheet. 2.5 Selecting Various plotting parameters such as Paper size, paper units, drawing orientation, plot scale, plot offset, plot area, print preview	CAD-Definition-Importance.     Familiarization with CAD Environment and utilities.     Setting up layout in CAD software's by taking plotting parameters	4-0-8
UNIT-3 Exposure to CAD Commands	3.1 Draw basic entities like Line, Circle, Arc, Polygon, Ellipse, Rectangle, Multiline, Dimensioning, Inserting text Applying constraints - horizontal, vertical, parallel, concentric, perpendicular, symmetric equal, collinear 3.2 Insert title block for the drawing and take the Print out 3.3Create objects by applying constraints and convert the objects to full scale, reduced scale and enlarged scale 3.4 Apply copy, mirroring, array, fillet and trim on the object created	<ol> <li>Computer graphics &amp; its terminology.</li> <li>CAD definition, concept &amp; need.</li> <li>Commands used in CAD</li> <li>Functional areas of CAD Coordinate systems.</li> <li>Familiarization of Cad commands</li> <li>Draw simple Geometrical figures using CAD</li> </ol>	6-0-12
UNIT-4 Orthographic projections	4.1 Introduction to orthographic projection 4.2 Conversion of pictorial view into Orthographic Views	1. Types of projections- orthographicconcept and applications. 2 Various term associated	4-0-8

UNIT-5 Isometric projections	5.1 Introduction to Isometric Projections 5.2 Isometric Scales and Actual Scale 5.3 Isometric View and Isometric Projection 5.4 Conversion of Orthographic Views into Isometric	with orthographic projections.  (a) Theory of projection. (b) Methods of projection. (c) Orthographic projection. (d) Planes of projection. 3. Conversion of simple pictorial views into Orthographic views. Illustrative problems on orthographic projection. Note: (1) Problem should be restricted up to - Front view/Elevation, Top view/Plan and Side views only. Use First Angle Method only.  1. Isometric axis, lines and planes. 2. Isometric scales. 3. Isometric view and isometric drawing. 4. Difference between isometric projection and isometric drawing. 5. Illustrative problems limited to Simple elements	4-0-8
UNIT-6 CAD Drafting	6.1 Draw different types of 2D/3D modeling entities using viewing commands, to view them (Problems solved in chapter no 3 and 4 i.e Orthographic, isometric projection). 6.2 2D/3D modeling for Branch specific components	1 Difference between 2D & 3D models. 2.2D/3D modeling – concept, Simple objects	4-0-8
		TOTAL	26-0-52

# **6. LIST OF PRACTICAL EXERCISES:**

The exercises/practical/experiments should be properly designed and implemented with an attempt to develop different types of skills leading to the achievement of the competency. Following is the list of exercises/practical/experiments for guidance.

Sr. No	Unit No.	Practical Exercises (Outcomes in Psychomotor Domain)	Hours
1	1. Teacher will demonstrate a: Use of a. Drawing instruments. b. Planning and layout as per IS. c: Scaling technique.  2. Draw following. Problem – 1 Drawing horizontal, vertical, 30 degree, 45 degree, 60 & 75 degrees lines using Tee and Set squares/ drafter. (Sketch book)		1-0-2
		Problem – 2 Indicate different convention of lines on the drawing.(SketchBook)	1-0-2
		Problem – 3 Copy the sketch to the required scale and dimensioning adopting right system and positioning of dimensions using Tee and Set squares / drafter. (SketchBook)	1-0-2

		Problem 4. Draw regular geometric constructions Pentagon, Hexagon, Square, circle, Triangle and other shapes. (SketchBook)	1-0-2
2	2	Use of CAD commands, plotting the drawing	4-0-8
3	3	Problem 5: Drawing basic entities: Circle, Arc, Polygon, Ellipse, Rectangle, Multiline	6-0-12
4	4	Problem 6: Draw Orthographic views for the given object. (CAD Drawing) (Minimum 5 Problems)	4-0-8
5	5	Problem 7: Draw Isometric projections for the given Orthographic views(CAD Drawing) (Minimum 5 Problems)	4-0-8
6	6	Problem 8: Produce Orthographic (2D) Drawings in CAD – Chap 3 Problem 14: Produce Isometric and 3D Drawings in CAD – Chap 4 (CAD Drawings and Printout) (Minimum 5 Problems)	2-0-4
6	Panel box (Minimum 3 Problems related to Pro	Problem 9: create 3D models of Program specific Elements such as Panel box (Minimum 3 Problems related to Program specific)) (CAD Drawings and Printout)	2-0-4
		TOTAL	26-0-52

- 1 Theory & practice should be in first angle projections and IS codes should be followed wherever applicable.
- 2 The dimensions of line, axes, distances, angle, side of polygon, diameter, etc. must be varied for each student in batch so that each student will have same problems, but with different dimensions.
- 3 The sketchbook has to contain data of all problems, solutions of all problems and student activities performed.
- 4 Students activities are compulsory to be performed.

### 7. SUGGESTED LIST OF STUDENT ACTIVITIES:

SL.NO.	ACTIVITY			
1	Sketch the combinations of set squares to draw angles in step of $15^{\circ}$ . $30^{\circ}$ , $45^{\circ}$ , $60^{\circ}$ , $75^{\circ}$ , $90^{\circ}$ , $105^{\circ}$ , $120^{\circ}$ , $135^{\circ}$ , $150^{\circ}$ , $165^{\circ}$ , $180^{\circ}$ .			
2	Take two simple objects. Sketch isometric of them.			
3	Take two simple objects. Sketch Pictorial orthographic views of them.			
4	Prepare a 2D drawing using AutoCAD and 2D parametric sketcher environment.			
5	Prepare 3D solid models using AutoCAD any one mechanical component (Four components).			

### **8. SUGGESTED LEARNING RESOURCES:**

- 1. Bureau of Indian Standards. *Engineering Drawing Practice for Schools and Colleges IS: Sp-46.* BIS. Government of India, Third Reprint, October 1998; ISBN: 81-7061-091-2.
- 2. Bhatt, N. D. *Engineering Drawing*. Charotar Publishing House, Anand, Gujrat 2010; ISBN: 978-93-80358-17-8.
- 3. Jain &Gautam, Engineering Graphics & Design, Khanna Publishing House, New Delhi (ISBN: 978- 93-86173-478)
- 4. Jolhe, D. A. *Engineering Drawing*. Tata McGraw Hill Edu. New Delhi, 2010; ISBN: 978-0-07-064837-1
- 5. Dhawan, R. K. *Engineering Drawing*. S. Chand and Company, New Delhi; ISBN: 81-219-1431-0.

- 6. Shah, P. J. *Engineering Drawing*. S. Chand and Company, New Delhi, 2008, ISBN:81-219-2964-4.
- 7. Kulkarni, D. M.; Rastogi, A. P.; Sarkar, A. K. *Engineering Graphics with AutoCAD*. PHI Learning Private Limited-New Delhi (2010); ISBN: 978-8120337831.
- 8. Jeyapoovan, T. *Essentials of Engineering Drawing and Graphics using AutoCAD*. Vikas Publishing HousePvt. Ltd, Noida, 2011; ISBN: 978-8125953005.
- 9. Autodesk. AutoCAD User Guide. Autodesk Press, USA, 2015.
- 10. Sham, Tickoo. *AutoCAD 2016 for Engineers and Designers* .Dreamtech Press; Galgotia Publication, New Delhi, 2015; ISBN 978-9351199113.

# **9.SOFTWARE/LEARNING WEBSITES:**

- 1. <a href="https://www.youtube.com/watch?v=TJ4jGyDWCw">https://www.youtube.com/watch?v=TJ4jGyDWCw</a>
- 2. <a href="https://www.youtube.com/watch?v=dmt6">https://www.youtube.com/watch?v=dmt6</a> n7Sgcg
- 3. <a href="https://www.youtube.com/watch?v="MQScnLXL0M">https://www.youtube.com/watch?v= MQScnLXL0M</a>
- 4. <a href="https://www.youtube.com/watch?v=3WXPanCq9LI">https://www.youtube.com/watch?v=3WXPanCq9LI</a>
- 5. <a href="https://www.youtube.com/watch?v=fvjk7PlxAuo">https://www.youtube.com/watch?v=fvjk7PlxAuo</a>
- 6. <a href="http://www.me.umn.edu/coursesme2011/handouts/engg%20graphics.pdf">http://www.me.umn.edu/coursesme2011/handouts/engg%20graphics.pdf</a>
- 7. https://www.machinedesignonline.com

# 10. Mapping of Course Outcomes with Programme Outcomes (Suggestive only):

Course	CO's		Programme Outcomes (PO's)					
		1	2	3	4	5	6	7
Engineering Graphics	CO1	3	0	0	3	0	0	0
	CO2	3	0	0	3	0	0	0
	CO3	3	0	0	3	0	0	0
	CO4	3	0	0	3	0	0	0

# Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0-Not Mapped

Method is to relate the level of PO with the number of hours devoted to the CO s which maps the given PO.

If ≥50% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is mapped at Level 3

If 30 to 50% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is mapped at Level 2

If 5 to 30% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is mapped at Level 1

If < 5% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is considered not mapped i.e. Level 0

### 11. COURSE ASSESSMENT AND EVALUATION CHART:

Sl.	Assessment	Time frame in	Duration	Max marks	Conversion
No		semester			
1.	Portfolio Evaluation of	Entire Duration	-	20	20
	Drawings (CAD				
	Practice Exercises)				
2	Skill Test-1				Average of two skill
	(Skill test 1 -	At the end of 4 week	3 Hrs	100	tests 1and 2 (Both
	Unit-1& 2)				skill tests are to be
3	Skill Test-2				reduced to
	(Skill test 2 is of CAD	At the end of 8 week	3 Hrs	100	weightage of 20 independently)
	based-Unit,3,4)				20

4	Skill Test-3 (Skill test 3 is of CAD based Unit 5,6)	At the end of 13 week	3 Hrs	100	Skill tests-3 is to be reduced to weightage of 20
5	Total Continuous Internal Evaluation (CIE) Assessmen				60
6	Semester End Examina	3 Hrs	100	40	
	conducted for 100 ma				
	40 marks				
				TOTAL	100

### **Scheme of Valuation for End Examination**

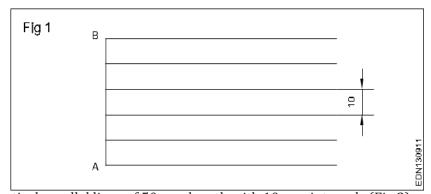
SL NO	QUESTIONS	MARKS			
1.	Create Orthographic views for the given Pictori	50			
	all Dimensions and Annotations.(CAD)	-			
	OR				
	Create Isometric Projections for the given Orthographic views (CAD)				
2.	2. Create 3D drawing for the given Sketch (CAD)				
		TOTAL	100		

# 12. CAD Laboratory and Other Requirements to Conduct Engineering Graphics Course

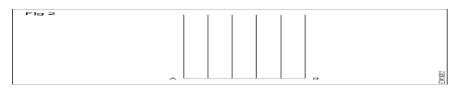
- 1. Latest Configuration Computers which can be able to run latest any Computer Aided Drafting Software. (At least One Computer per student in practical session.)-30 no
- 2. Any latest Authorized Computer Aided Drafting Software (30 user licenses)
- 3. Plotter of size A2/A3
- 4. LCD Projector.

# **MODEL QUESTION BANK (Suggestive only)**

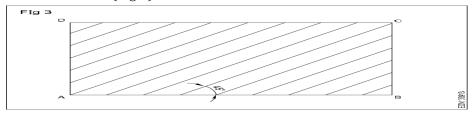
1. Draw six horizontal parallel lines of 50 mm long with 10 mm intervals (Fig 1).



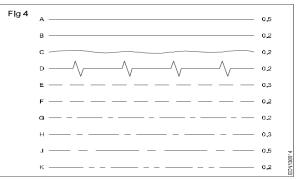
2. Draw six vertical parallel lines of 50 mm length with 10 mm intervals (Fig 2)



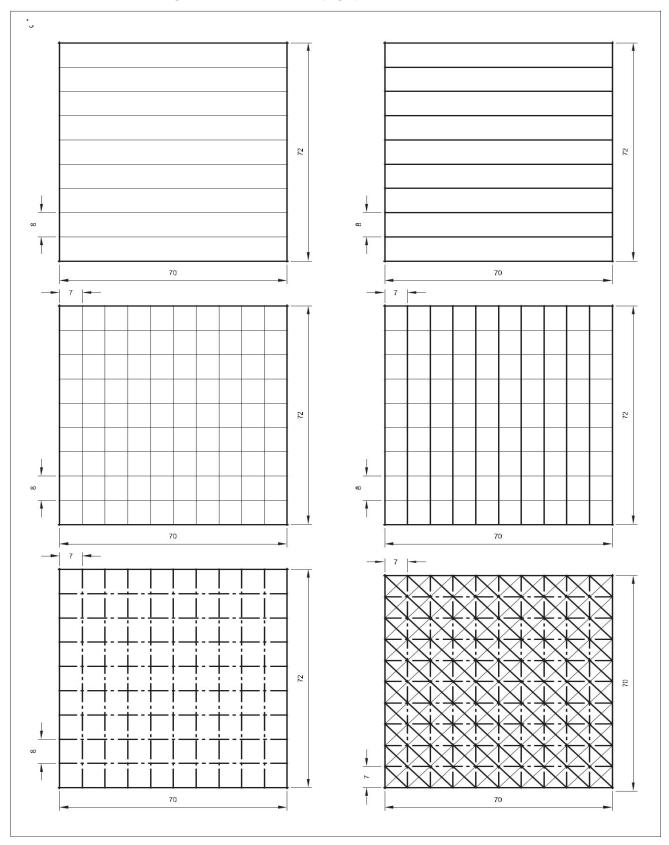
3. Draw 45° inclined lines (Fig 3).



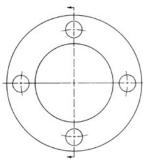
4.Draw the given types of lines using 0.5 range thickness of line according to the specification (Fig 4).



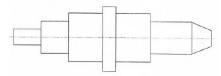
5. Draw the following Exercises in A4 sheet (Fig 5).



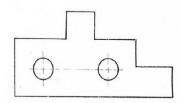
- 6a ) Illustrate the elements of dimensioning with the help of a sketch.
  - b) Illustrate the dimensioning of given common features: diameter, radius, chord, Arc and angle.
- 7. Copy the sketch to 1:1 scale and dimension it using Aligned system.



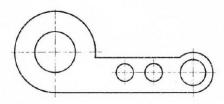
8. Copy the sketch to 1:1 scale and dimension it using unidirectional system with Parallel dimensioning method.



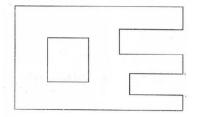
9. Copy the sketch to 1:1 scale and dimension it using Aligned system with Chain dimensioning method.



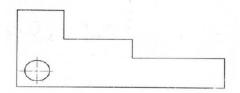
10.Copy the sketch to 1:1 scale and dimension it using Aligned system with Parallel dimensioning method.



11. Copy the sketch to 1:1 scale and dimension it using unidirectional system with Chain dimensioning method

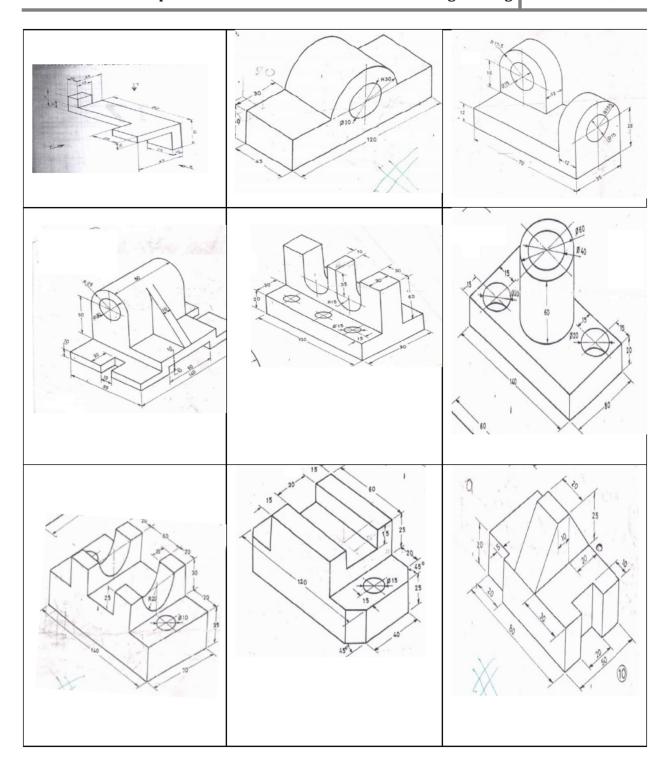


 $12. \ Copy \ the \ sketch \ to \ 1:1 \ scale \ and \ dimension \ it \ using \ unidirectional \ system \ with \ Parallel$ dimensioning method.



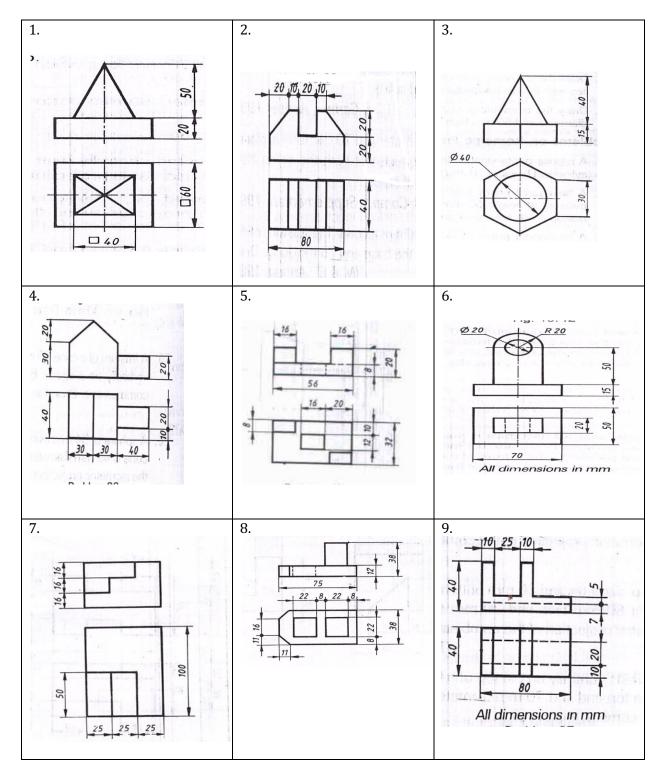
# **ORTHOGRAPHIC ROJECTIONS**

1. Draw the three principal views of the component as shown in the figure.

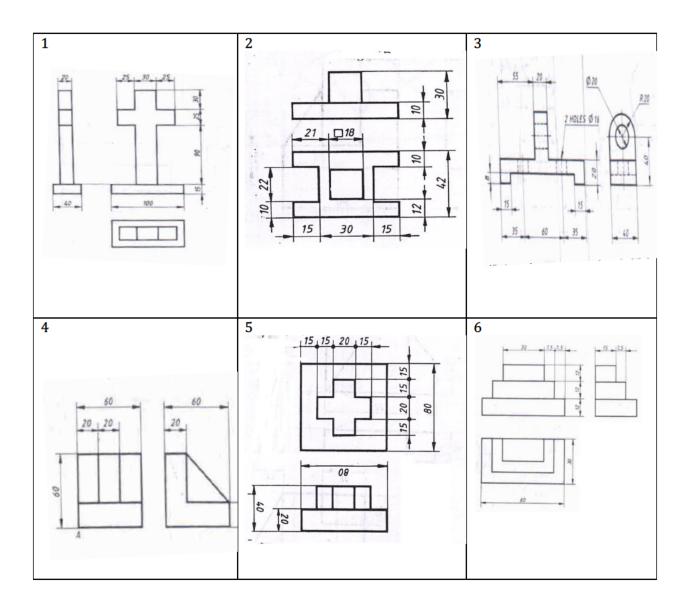


# ISOMETRIC PROJECTIONS

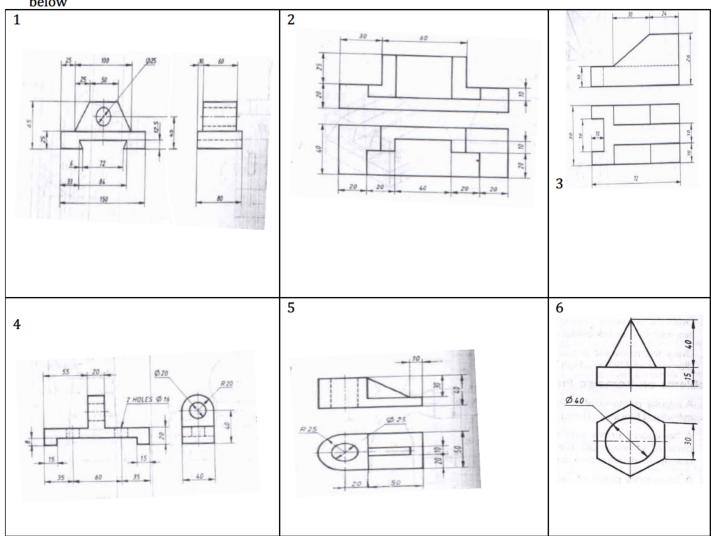
1. Draw the isometric view of the machine component whose orthographic views are given below:



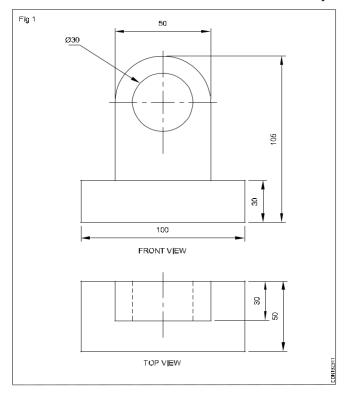
2. Draw the isometric Projection of the machine component whose orthographic views are given below:

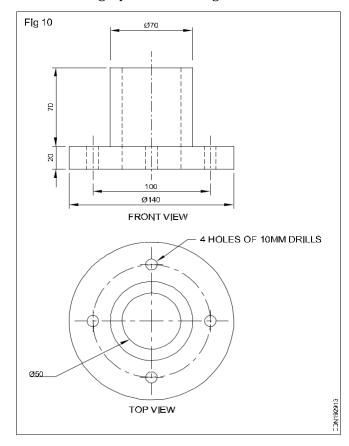


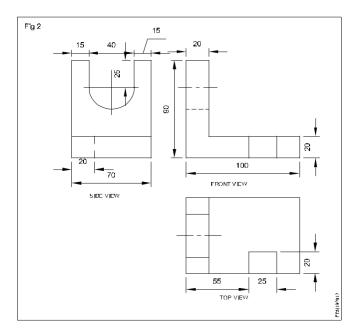
3. Draw the isometric Projection of the machine component whose orthographic views are given below

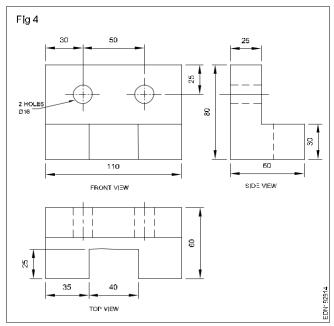


4. Draw the isometric View of the machine component whose orthographic views are given below









# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20EC01P	Semester	I/II
Course Title	FUNDAMENTALS OF ELECTRICAL & ELECTRONICS ENGINEERING	Course Group	Core
No. of Credits	4	Type of Course	Lecture & Practice
Course Cotogowy	PC	<b>Total Contact Hours</b>	6Hrs Per Week
Course Category	l PG		78Hrs Per Semester
Prerequisites	Basic Science	Teaching Scheme	(L:T:P)= 1:0:2
CIE Marks	60	SEE Marks	40

### 1. RATIONALE

Fundamentals of Electrical and Electronics Engineering is essential for all streams of diploma engineering to work in any industry as it covers basic electrical safety, troubleshooting and repairing of simple electrical systems. Basic knowledge of electrical wiring circuits, protective devices, electrical machines and basic electronics devices is required to work in any engineering field.

#### 2. COURSE SKILL SET

The aim of the course is to help the student to attain the following industry identified competency through various teaching –learning experiences

- 1. Perform and test domestic wiring
- 2. Can operate electrical machine
- 3. Test different electronics devices

### 3. INSTRUCTIONAL STRATEGY

- 1. Expose to different learning tools used in respective labs, Operational safety and Procedure to be followed in the laboratory.
- 2. Instructor should give examples from daily routine as well as, engineering/technology applications on various concepts and principles in each topic so that students are able to understand and grasp these concepts and principles. In all contents, SI units should be followed.
- 3. Activity- Theory Demonstrate/practice approach may be followed throughout the course so that learning may be skill and employability based.

### **4.COURSE OUT COMES**

On successful completion of the course, the students will be able to

CO1	Comply with the safety procedures
CO2	Apply the fundamentals of electricity.
CO3	Install and test electrical wiring system.
<b>CO4</b>	Identify and Operate electrical machines, Batteries and UPS.

CO5	Identify and test the different electronic devices.

# **5. COURSE TOPICS:**

Unit No	Unit Name	Hours
1	Electrical Safety	6
2	Electrical Fundamentals	15
3	Protective Devices and Wiring circuits	15
4	Electric Machines and Batteries and UPS	15
5	Introduction to Electronic Devices and Digital Electronics	27
	Total	78Hr

### **6. COURSE CONTENT**

The following topics/sub topics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets

Sl No	Unit skill set (In cognitive domain) On successful completion of the class, the students will be able to	Topics/Sub topics	Practical	Hours L-T-P
		UNIT-1		
		Electrical Safety		
1	Comply with the Electrical safety	<ol> <li>Electrical Symbols</li> <li>Electrical safety</li> <li>Identify Various types of safety signs and what they mean</li> <li>Demonstrate and practice use of PPE</li> <li>Demonstrate how to free a person from electrocution</li> <li>Administer appropriate first aid to victims, bandaging, heart attack, CPR, etc.</li> <li>Fire safety, causes and precautionar y activities.</li> <li>Use of appropriate fire extinguishers on different types of fires.</li> <li>Demonstrate rescue techniques applied during fire hazard, correct method to move injured people during emergency</li> <li>Inform relevant authority about any abnormal situation</li> <li>Earthing: Types</li> </ol>	<ol> <li>Electrical symbols related to electrical engineering.</li> <li>Electrical safety</li> <li>Electrical earthing</li> </ol>	02-00- 04

		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		
		http://nreeder.com/Flash/symbols.htm		
		http://bouteloup.pierre.free.fr/iufm/as/de/house/safety.html		
		jamijusjucjilousejsuccjilom		
		UNIT-2		
2	4 11 .:6 1 1	Electrical Fundamentals		1.0.2
2	<ol> <li>Identify and select the different measuring devices.</li> <li>Identify different electrical supply systems</li> <li>Identify open circuit, close circuit and short circuit conditions.</li> </ol>	<ol> <li>Describe the sources of electrical energy.</li> <li>Electrical current, voltage, emf, potential difference, resistance with their SI units.</li> <li>Mention the meters used to measure different electrical quantities.</li> <li>Identification Measuring devices         <ul> <li>Ammeter</li> <li>Voltmeter</li> <li>Wattmeter</li> <li>Digital Multimeter</li> <li>Megger</li> <li>Tong tester</li> </ul> </li> </ol>	1. Connect voltmeter and ammeter in a simple circuit. (Practicing of identification and connection of different meters)	1:0:2
		4. Explain supply systems like AC, DC.  ➤ <a href="http://nreeder.com/Flash/units.ht">http://nreeder.com/Flash/units.ht</a> m		
3	Calculate basic electrical quantities	<ul> <li>Relationship between V, I and R. (Ohms law)</li> <li>Behavior of V, I in Series and Parallel DC circuits.</li> <li>Describe open circuit, close circuit and short circuit</li> </ul>	Measure current,     voltage and analyze     effective resistance in     seriescircuit     Demonstrate effects of     shorts and opens in a     circuit	1:0:2
		• http://nreeder.com/Flash/oh msLaw.htm		
4	Connect resistances in different combination	1. Equation to find the effective Resistances connected in series 2. Equation to find effective Resistances connected in parallel 3. Resistances connected series and parallel combinations 4. Simple problems.	1. Determine the equivalent Resistance of parallel connected resistances.	1:0:2
5	Calculate and measurement of different parameters of an AC quantity.	Ac sinewave: Sinusoidal voltage, current, amplitude, time-period, cycle, frequency, phase, phase difference, and their units.  http://nreeder.com/Flash/freqPeriod.htm http://nreeder.com/Flash/oscill	Generate and demonstrate the measurement of frequency, time period and phase difference of	1:0:2

		<u>oscope.htm</u>	AC quantity using CRO and function generator.	
6	Calculate and measure electric power and energy     Identify and differentiate Single phase and Three phase supply	<ul> <li>1. Electrical work, power and power factor <ul> <li>SI units</li> <li>Mention the meters used to measure them</li> </ul> </li> <li>http://nreeder.com/Flash/powerLaw.htm</li> </ul>	• Measure the voltage, current, powerusing relevant measuring instruments in a Single-phase load.	1:0:2
7.		<ul> <li>1. Electrical energy</li> <li>SI units</li> <li>Mention the meters used to measure them</li> <li>2. Single phase and Three phase supply.</li> </ul>	1. Measure single phase energy using relevant measuring instruments in a Single-phase load. 2. Measure the voltages in Three phase supply.	
		UNIT-3 Protective Devices and Wiring circuit	s	
8.	1. Identify and select Protective Devices for given current and voltage rating 2. Identify and select the various electrician tools	<ul> <li>Necessity of Protective Devices</li> <li>Various Protective devices and their functions</li> <li>fuse wire,</li> <li>Glass cartridge fuse</li> <li>HRC fuse</li> <li>Kit-kat fuse</li> <li>MCB</li> <li>MCCB</li> <li>RCCB</li> <li>ELCB</li> <li>Relay</li> <li>Different types of electrician tools and their function.</li> <li>Describe various wiring tools.</li> <li>State procedure of care and maintenance of wiring tools.</li> </ul>	1. Wire up and test PVC Conduit wiring to control one lamp from two different places using suitable protective devices.	1:0:2

9	<ol> <li>Identify and select         Wiring systems for a         given applications</li> <li>Identify and select the         cables used for different         current and voltage         ratings.</li> <li>Draw the wiring diagram</li> </ol>	<ol> <li>Describe different types of wiring systems.         <ul> <li>Surface conduit</li> <li>concealed conduit</li> <li>PVC casing capping</li> </ul> </li> <li>Wiring systems and their applications.</li> <li>Describe the types of wires, cables used for different current and voltage ratings.</li> </ol>	1. Wire up and test PVC Conduit wiring to control of 2 sockets and 2 lamps.	2:0:4
10	Estimate and plan electrical wiring	Explain Plan and estimate the cost of electrical wiring for one 3m × 3m room consisting of 2 lamps, 1ceiling fan, 2 three pin sockets.	Prepare the estimation and plan	1:0:2
	E	UNIT-4 Electrical Machines and Batteries and U	IPS	
11	1. Identify the types of transformer.	Transformer  • working principle	Connect the Single- phase transformer as Step-Up,	1:0:2
	2. verify the transformation ratio.	<ul> <li>Working principle</li> <li>Transformation ratio</li> <li>Types and applications with their ratings</li> </ul>	Step-Down transformer and verify the transformation ratio.	
12	<ol> <li>Start and run the induction motor.</li> <li>Troubleshoot DOL/Stardelta starter and induction motor</li> </ol>	<ul> <li>1. Induction motor</li> <li>Single phase and three phase Induction motor.</li> <li>Necessity of starters.</li> <li>Describe DOL AND STAR-DELTA starters.</li> <li>2. What are different causes and remedies for a failure of starter and induction motor.</li> </ul>	<ol> <li>Construct a suitable circuit to start and reverse the direction of three phase induction motor using DOL/ Stardelta starter.</li> <li>Troubleshoot the DOL/ Stardelta starter and induction motor</li> </ol>	2:0:4

13	Select and test the battery for a given application	<ul> <li>Battery</li> <li>Types of batteries (Lead acid battery, lithium, sealed maintenance free (SMF) battery, Modular battery).</li> <li>Selection criteria of batteries for different applications.</li> <li>Ampere-Hour Capacity.</li> <li>Efficiency</li> </ul>	Testing Condition of charging and discharging of a Lead-acid battery	1:0:2
14	Select the size of the UPS for a given application	<ul> <li>UPS</li> <li>List the types and applications</li> <li>Selection criteria of UPS</li> <li>Sizing of UPS</li> </ul>	Sizing of UPS	1:0:2
	Introduc	UNIT-5 ction to Electronic Devices and Digital E	lectronics	
15	Identify and differentiate Conductors, insulators and semiconductors.	<ul> <li>1.Compare Conductors, insulators and semiconductors with examples.</li> <li>2. Identification of types and values of resistors-color codes.</li> <li>http://nreeder.com/Flash/resistor.htm</li> </ul>	Determine the value of resistance by color code and compare it with multimeter readings.	1:0:2
16	Identify and test PN junction Diode	<ul> <li>PN junction diode</li> <li>Symbol</li> <li>Characteristics</li> <li>Diode as switch.</li> <li>Types of diodes and ratings</li> <li>Applications</li> </ul>	Identify the terminals of a Diode and test the diode for its condition.	1:0:2
17	Build and test bridge rectifier circuit	<ul> <li>Rectifier</li> <li>Need for AC to DC conversion</li> <li>Bridge rectifier with and without C filter,</li> <li>Rectifier IC.</li> </ul>	Construct and test bridge rectifiers using semiconductor diode and rectifier IC. Compare the waveforms using CRO.	1:0:2
18	<ol> <li>Identify and test         Transistor</li> <li>Build and test         transistor as an         electronic switch</li> </ol>	Transistor (BJT)  • Symbol  • Structure  • Working principle	I. Identification of transistor terminals and test.     Construct and test the transistor as an electronic switch	1:0:2
19.	Identify and test     different digital IC	<ul> <li>Comparison of analog and digital signal</li> <li>Digital systems, examples.</li> <li>Binary numbers, Boolean identities and laws.</li> <li>Digital system building blocks: Basic logic gates, symbols and truth tables.</li> <li>IC-Definition and advantages.</li> </ul>	<ul> <li>Test a Digital IC.</li> <li>Identification and selection of suitable ICs for basic gates.</li> <li>Verify NOT, AND, OR, NOR, EXOR and NAND gate operations (two inputs).</li> </ul>	2:0:4

	T	T	T	T
20	Identify and test various	1.Sensors	2. Connect and test an IR	2:0:4
	Sensors and actuators.	<ul> <li>Concept</li> </ul>	proximity sensor to a	
		Types: Temperature, Pressure,	Digital circuit.	
		Water, Light, Sound, Smoke,	• Connect and test a	
		proximity Sensors, Flow,	relay	
		humidity, voltage, vibration, IR	circuit using an Opto-	
			coupler. (Photo Diode	
		(Principle/working, ratings/	•	
		specifications, cost, and	& Transistor)	
		applications)	Refer note	
		2.Actuators		
		<ul> <li>Concept</li> </ul>		
		<ul> <li>Types and applications.</li> </ul>		
		Relay as an actuator.		
21	Know the application of	Microcontroller as a	Identify different	1:0:2
	Microcontroller and PLC	programmable device, and list	application	
	The beattrainer and 1 Ed	of real-world applications.	microcontroller.	
		PLC and Their applications.	Identify commercially	
		(Activity based learning)	available PLC	
			and their	
			specifications	
			TOTAL	26-0-
				52=78
				Hours

# 7. PRATICAL SKILL EXERCISES

Sl. No.	Practical Out Comes/Practical exercises	Unit No.	PO	CO	L: T:P Hrs.
1	<ul> <li>Identify Various types of safety signs and what they meanDemonstrate and practice use of PPE</li> <li>Demonstrate how to free a person from electrocution appropriate first aid to victims, bandaging, heart attack, CPR, etc.</li> <li>Fire safety, causes and precautionary activities.</li> <li>Use of appropriate fire extinguishers on different types of fires.</li> <li>Demonstrate rescue techniques applied during fire hazard.</li> <li>Inform relevant authority about any abnormal situation during fire hazard.</li> </ul>	1	1,4	1	0:0:2
2	<ul><li>Demonstrate different types of earthing/using videos.</li><li>Prepare a Report on types of Earthing</li></ul>	1	1,4	1	0:0:2
3	Connect voltmeter and ammeter in a simple circuit. (Practicing of identification and connection of different meters)	2	1,4	2	0:0:2
4	<ul><li>1.Determine the equivalent Resistance of series connected resistances.</li><li>2.Demonstrate effects of shorts and opens in a circuit</li></ul>	2	1,4	2	0:0:2

5	Determine the equivalent Resistance of parallel connected resistances.	2	1,4	2	0:0:2
6	Generate and demonstrate the measurement of frequency, time period and phase difference of AC quantity using CRO and function generator.	2	1,4	2	0:0:2
7	Measure the voltage, current, power using relevant measuring instruments in a Single-phase load.	2	1,4	2	0:0:2
8.	<ul><li>1.Measure single phase energy using relevant measuring instruments in a Single-phase load.</li><li>2. Measure the voltages in Three phase supply.</li></ul>				
9.	Wire up and test PVC Conduit wiring to control one lamp from two different places using suitable protective devices.	3	1,4	3	0:0:2
10	2. Wire up and test PVC Conduit wiring to control of 2 sockets and 2 lamps.	3	1,4	3	0:0:2
11	Wire up and test PVC Conduit wiring to control one lamp from two different places.	3	1,4	3	0:0:2
12	Plan and estimate the cost of electrical wiring for one 3mx3m room consisting of 2 CFL 1ceiling fan, 2 three pin sockets.	3	1,4	3	0:0:2
13	Connect the Single- phase transformer as Step-Up, Step-Down transformer and verify the transformation ratio.	4	1,4	4	0:0:2
14	Construct a suitable circuit to start and reverse the direction of three phase induction motor using DOL/star-delta starter.	4	1,4	4	0:0:2
15	Troubleshoot the DOL/Star-delta starter and induction motor	4	1,4	4	0:0:2
16	Testing Condition of charging and discharging of a Lead-acid battery.	4	1,4	4	0:0:2
17	Estimate the UPS rating for a computer lab with 50 computers/domestic.	4	1,4	4	0:0:2
18	Determine the value of resistance by color code and compare it with multimeter readings	5	1,4	5	0:0:2
19	Identify the terminals of a Diode and test the diode for its condition.	5	1,4	5	0:0:2
20	Construct and test bridge rectifiers using semiconductor diode and rectifier IC. Compare the waveforms using CRO.	5	1,4	5	0:0:2
21	Identification of transistor terminals and test. Construct and test the transistor as an electronic switch.	5	1,4	5	0:0:2
22	Test an IC. Verify the truth-table AND, OR, NOT logic gates.				
23	Verify the truth-table NAND, NOR, EX-OR, EX-NOR logic gates.	5	1,4	5	0:0:2
24	Connect and test anIR proximity sensor to a Digital circuit. <b>NOTE:</b> Any sensor listed in the theory may be used for condition appropriately.				

25	Connect and test a relay circuit using an Optocoupler. (Photo Diode & Transistor)	5	1,4	5	0:0:2
26	1.Identify MCS-51 variants 2.Identify commercially available PLC and their specifications.	5	1,4	5	0:0:2
	Total				0:0:52 =52Hrs

### 8.MAPPING OF CO WITH PO

со	Course Outcome	PO Mapped	Experimen t	Cognitive Level <b>R/U/A</b>	Lecture & Practical Sessions in Hrs	TOTAL
CO1	Comply with the safety procedures	PO1, PO4	1-2	A	6	
	1					
<b>CO2</b>	Apply the fundamentals of	PO1,	3-7	Α	15	
	electricity.	PO4				
CO3	Install and test electrical wiring	PO1,	8-12	A	15	
	system and protective devices.	PO4				
<b>CO4</b>	Identify and Operate electrical	PO1,	13-17	Α	15	
	machines, Batteries and UPS.	PO4				
<b>CO5</b>	Identify and test the different	PO1,	18-26	Α	27	
	electronic devices.	PO4				

Course	CO's		Programme Outcomes (PO's)					
		1	2	3	4	5	6	7
Fundamentals of Electrical	CO1	3	0	0	3	0	0	0
and Electronics	CO2	3	0	0	3	0	0	0
Engineering	CO3	3	0	0	3	0	0	0
	CO4	3	0	0	3	0	0	0

Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0-Not Mapped

### 9. SUGGESTED LEARNING RESOURCES:

### **Reference Books:**

- 1. ABC of Electrical Engineering by B. L. Theraja and A. K. Theraja, S Chand Publishers, New Delhi, 2014 Edition.
- 2. Basic Electrical and Electronics Engineering by S. K. Bhattacharya, Pearson Education India, 2012 Edition.
- 3. Electronic Devices and Circuits by I. J. Nagrath, PHI Learning Pvt. Ltd., 2007 Edition.
- 4. Basic Electrical Engineering by V. Mittle and ArvindMittle, McGrawHill Companies, 2005 Edition.
- 5. The 8051 Microcontroller & Embedded systemsusinkbnnnjbbh bbvvvvg assembly and C (2ndEdition)–M.A.Mazidi , J.C. Mazidi&R.D.McKinlay ISBN: 81-317-1026-2
- 6. Programmable Logic controllers, W BOLTON

# e-Resources

- 1. https://www.youtube.com/watch?v=mc9790hitAg&list=PLWv9VM947MKi 7yJ0 FCfzTBXpQU-0d3K
- 2.https://www.youtube.com/watch?v=CWulQ1ZSE3c
- 3. en.wikipedia.org/wiki/Transformer
- 2. www.animations.physics.unsw.edu.au//jw/AC.html
- 3. www.alpharubicon.com/altenergy/understandingAC.htm
- 4. www.electronics-tutorials
- 5. learn.sparkfun.com/tutorials/transistors
- 6. www.pitt.edu/~qiw4/Academic/ME2082/Transistor%20Basics.pdf
- 7. www.technologystudent.com/elec1/transis1.htm
- 8. www.learningaboutelectronics.com
- 9. www.electrical4u.com
- 10.https://www.youtube.com/watch?v=zLW 7TPf310
- 11. https://www.youtube.com/watch?v=8PTNjw-hQIM

### **10.SUGGESTED LIST OF STUDENTS ACTIVITYS for CIE**

### Note: the following activities or similar activities for assessing CIE (IA) (Any one)

Each student should conduct different activity and no repeating should occur

	dent briedra corrador arror ent activity and no repeating briedra eccur
1	Using suitable meters/ instruments give the practical working circuits to measure
2	Resistance, Current, Voltage, Power and Energy in DC and AC (Single phase) Circuits.
3	List out the different types of wiring systems used in your laboratories or house with
	their representation.
4	Mini-Projects: Like preparing extension box, switch box and wiring models,
5	List out the different protective devices used in your laboratories or house with their
	ratings.
6	Applications of Electro Magnetic Induction, statically induced and dynamically induced
	emf, self and mutual induced emfs.
7	Prepare a report on types of starters and enclosures used for various industrial
	applications of AC motors.
8	Types of Cells and Battery maintenance
9	Visit nearby Battery charging shop or show room and prepare a report of the visit.
10	Prepare a report on various types of diodes used for various industrial applications.
11	Prepare a report on various types of sensors and actuators used for various industrial
	applications.
12	Mini-Projects: Connect and test a sensor (domain application) to a Digital circuit

### 11. COURSE ASSESSMENT AND EVALUATION CHART

Sl.No	Assessment	Duration	Max marks	Con	version
1.	CIE Assessment 1 (Written Test -1-theory) - At the end of 3 <sup>rd</sup> week	60 minutes	20	two	erage of written tests
2.	CIE Assessment 2 (Written Test -2-theory) - At the end of 13 <sup>th</sup> week	60 minutes	20	20	
3.	CIE Assessment 3 (Skill test) - At the end of <b>5</b> <sup>th</sup> week	3 Hours	100		Average of three
4	CIE Assessment 4 (Skill test) - At the end of <b>7</b> <sup>th</sup> week	3 Hours	100	20	skill tests
5	CIE Assessment 5 (Skill test) - At the end of 9 <sup>th</sup> week	3 Hours	100		20
6	CIE Assessment 6 (Student activity) - At the end of 11 <sup>th</sup> week	-	20		20
7. Total Continuous Internal Evaluation (CIE) Assessment					60
8.	Semester End Examination (SEE) Assessment 3 Hours 100 (Practical Test)				40
		100			

### Note:

- 1. CIE written test is conducted for 20 marks (Two sections). Each section shall have two full questions of same CL, CO. Student shall answer one full question (10 marks) from each section.
- 2. CIE Skill test is conducted for 100 marks (3 Hours duration) as per scheme of evaluation and the obtained marks are scaled down to 20 marks

# 12. SCHEME OF VALUATION FOR SKILL TEST (CIE) & SEE

# (CONTINOUS INTERNAL & SEMESTER END EXAMINATION)

Sl. No.	Particulars	Marks
1.	Identification of meters/ equipment/wires/tools etc.	10
2.	Writing Circuit/writing diagram and Procedure*	25
3.	Conduction	35
4.	Results	10
5	Viva-voce	20
	Total	100

# 12. RUBRICS FOR ACTIVITY

	RUBRICS FOR ACTIVITY (Example only) Faculty need to develop appropriate rubrics for respective activity						
Dimension	Beginning	Developing	Satisfactory	Good	Exemplary	Student	
	1	2	3	4	5	Score	
Collection of data	Does not collect any information relating to the topic	Collects very limited information; some relate to the topic	Collect much information; but very limited relate to the topic	Collects some basic information; most refer to the topic	Collects a great deal of information; all refer to the topic		
Fulfil team's roles & duties	Does not perform any duties assigned to the team role	Performs very little duties but unreliable.	Performs very little duties	Performs nearly all duties	Performs all duties of assigned team roles		
Shares work equally	Always relies on others to do the work	Rarely does the assigned work; often needs reminding	Usually does the assigned work; rarely needs reminding	Normally does the assigned work	Always does the assigned work without having to be reminded.		
Listen to other Team mates	Is always talking; never allows anyone else to speak	Usually does most of the talking; rarely allows others to speak	Talks good; but never show interest in listening others	Listens, but sometimes talk too much	Listens and speaks a fair amount		
				Average /	Total Marks:		

# **Lab Equipment Requirement**

The following are the specification of the apparatus required for FEEE lab and number of apparatus required for the batch of 20 students.

Sl. No.	Name of Equipment and Specification	Quantity Required
1	Dual Channel 30 V, 2 A continuously variable DC Regulated Power Supply with Current and Overload Protection	05 Nos.
2	+/- 15 V, 2 A, fixed DC Regulated Power Supply	05 Nos.
3	Portable Moving Coil DC Voltmeters a) 0 - 1 V b) 0 - 10 V c) 0 - 30 V	Each 05 Nos.

r		1
4	Portable Moving Iron AC Voltmeters	Each 05 Nos.
	a) 0 - 300 V	
	b) 0 - 600 V	
5	Portable Moving Coil DC Ammeters	Each 05 Nos.
	a) 0 - 100 mA	
	b) 0-1A	
	c) 0-2A	
6	Portable Moving Iron AC Ammeters	Each 05 Nos.
	a) 0 - 2 A	
	b) 0-5A	
	c) 0-10 A	
7	Watt-meters	Each 02 Nos.
	a) 150/300V, 2 A, UPF	
	b) 300/600 V, 5/10 A, LPF	
8	Rheostats – 25 Ohms, 50 Ohms, 150 Ohms, 220 Ohms (all rated	Each 05 Nos.
	at 3 A)	
9	Rheostat Loads s – 1 KW, 230 V	02 Nos.
10	Wire wound Resistors- 5 Ohms 2 Watts, 25 Ohms 5 Watts, 330	Each 05 Nos.
	Ohms 2 Watts, 560 Ohms 2 Watts, etc.	
11	Soldering Iron 60 W	05 Nos.
13	Single Phase Energy meter 10 A, 230 V, 50 Hz, Digital type	05 Nos.
14	Multi-meter Digital ¾"	06 Nos.
15	Duel Trace Oscilloscope – 30 MHz	02 Nos.
16	Three Phase Induction Motors :1 HP – 440 V 50 Hz,2 HP – 440 V	Each 02 Nos.
	50 Hz.	
17	Three phase DOL, Star-Delta, Auto transformer starter	Each 02 Nos.
18	UPS 1 KVA	01 Nos.
19	Battery Lead-Acid type, 140 A-hr and Hydrometers	02 Nos.

Sl. No.	Name of Equipment and Specification	Quantity Required	
20	I C Trainer kit	05 Nos	
21	Digital IC's 7400, 7402, 7404, 7408, 7486 etc	Each 10 Nos.	
22	Wooden Wiring board (2x3) ft	10	
23	Wiring accessories		

		T =
	a) PVC conduit - ¾" - 10 lengths	Each 10 Nos.
	b) Cap and casing - ¾" - 10 lengths	
	c) Switches Single Pole- 5A, 230 V	
	d) Switches two way – 5 A, 230 V	
	e) 3 Pin Sockets 5A, 230 V	
	f) Bulb Holders – 5 A, 230 V	
	g) 3 Pin Plug 5A, 230 V	
	h) 60 Watts Lamps	
	i) 100 Watts Lamps	
	j) 15 W CFL lamps	
	k) Copper Wires of sizes	
	1.5 mm <sup>2</sup> , 2.5 mm <sup>2</sup> , 4 mm <sup>2</sup> – 1 coil each	
	l) Gang boxes (1+1, 2+1, 2+2)	
	m) Kit -Kat fuses 5A, 15 A	
	n) MCB 16 A & 32 A/ 230 V, Single and Double Pole	
	o) ELCB 16 A & 32 A/ 230 V, Double Pole	
	p) Neutral link- 16 A, 230 V	
	q) Screws of assorted sizes	
	r) Testers	
24	Electronic Components	Each 10 Nos.
	a) Diodes - BY 127 and IN 4001	
	b) Zener Diodes – 6.2 V, 5.6 V, 7.8 V	
	c) Relays – solid state Sugar cube type, SPST, Coil 6V,	
	Power circuit 230 V, 5 A.	
	d) Spring Boards	
	e) Bread Boards	
	f) Tag Boards.	
25	Simple PANEL BOARD/ CUBICAL consisting of bus-bars,	1 No
	CB/MCB/ELCB, meters, HRC fuses, magnetic contactors,	
	cables, earthing points.	

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20AU01T	Semester	I	
Course Title	ENVIRONMENTAL SUSTAINABILITY	Course Group	Audit	
No. of Credits	2	Type of Course	Lecture	
Course Catagory	AII	Total Contact House	2Hrs Per Week	
Course Category	AU	Total Contact Hours	26Hrs Per Semester	
Prerequisites	Basic Environmental Science	Teaching Scheme	(L:T:P)= 2:0:0	
CIE Marks	50	SEE Marks	No	

### **COURSE OBJECTIVES:**

Technicians working in industries or elsewhere essentially require the knowledge of environmental science so as to enable them to work and produce most efficient, economical and eco-friendly finished products.

- 1. Solve various engineering problems applying ecosystem to produce eco friendly products.
- 2. Use relevant air and noise control methods to solve domestic and industrial problems.
- 3. Use relevant water and soil control methods to solve domestic and industrial problems.
- 4. To recognize relevant energy sources required for domestic and industrial applications.
- 5. Solve local solid and e-waste problems.

### **COURSE OUTCOMES:**

At the end of the course student will be able to know:

CO1	Importance of ecosystem and terminology.
CO2	The extent of air pollution, effects, control measures and acts.
CO3	The extent of noise pollution, effects, control measures and acts.
CO4	The water and soil pollution, effects, control measures and acts
CO5	Different renewable energy resources and efficient process of harvesting.
CO6	Solid Waste Management and Environmental acts.

### **COURSE CONTENT:**

Marks: 15	Unit-1 Ecosystem	Allotted Hrs: 03			
Structure of ecosystem, Biotic & Abiotic components, Aquatic (Lentic and Lotic) and terrestrial ecosystem.					
Global warming - Causes, effects, Green House Effect, Ozone depletion.					
Marks: 20	Unit-2Air Pollution Allotted Hrs: 03				
Air pollution, Natural and	manmade sources of air pollution, Effects of air pollution	n.Air Pollutants and Types.			
Control of air pollutants b	y Cyclone separator and Electrostatic Precipitator, Air	(prevention and control of			
pollution) act 1981					
Marks: 10	Unit-3 Noise Pollution:	Allotted Hrs: 02			
Noise pollution: sources of	f pollution, measurement of pollution level, Effects and	Control of Noise			
pollution, Noise pollution	(Regulation and Control) Rules, 2000				
Marks: 20	Unit- 4Water and Soil Pollution:	Allotted Hrs: 06			
Water pollution and Source	ces of water pollution, Types of water pollutants, Charac	cteristics of water			
pollutants,control measur					
Definition and list unit of	perations in water and WasteWater Treatment proce	ss,Water (prevention and			
control of pollution) act 19	974, Water conservation – Importance of Rain Water Ha	arvesting.			
Soil pollution, Causes, Effe	ects and Preventive measures of Soil Pollution due to E	xcessive use of Fertilizers,			
Pesticides and Insecticides	S				
Marks: 20	Unit-5 Renewable sources of Energy	Allotted Hrs: 07			
Solar Energy: Basics of Sol	ar energy. Definition and advantages of advanced solar	collectors. Solar water			
heater and Solar stills and	their uses.				
Biomass: Overview of biomass as energy source. Thermal characteristics of biomass as fuel.					
Wind energy: Current status and future prospects of wind energy. Wind energy in India.					
Need of new Energy sources, Different type's new energy sources. Environmental benefits of New Energy					
Sources-Hydrogen energy, Ocean energy resources, Tidal energy conversion.					
Marks: 15	Unit-6 Solid Waste Management and	Allotted Hrs: 05			
	Environmental Acts				
Solid waste generation, Sources and characteristics of Municipal solid waste, Solid Waste Management					

rules 2016-3R in SWM.

E- Waste generation, Sources and characteristics, E waste management rules 2016 Plastic Waste generation, Sources and characteristics, Recycled plastic rules 2016 Importance of Environment (protection) act 1986 Occupational health and safety measures.

Unit No & Name	Detailed Course Content	со	PO	Contact Hrs
1.	Structure of ecosystem, Biotic & Abiotic components, Aquatic (Lentic and Lotic) and terrestrial ecosystem.	CO1	1,5,7	1
Ecosystem	Global warming - Causes, effects.	CO1	1,5,7	2
	Green House Effect, Ozone depletion - Causes, effects	CO1	1,5,7	3
	Air pollution, Natural sources of air pollution, Man Made sources of air pollution	CO2	1,5,7	4
2. Air and Pollution	Air pollutants and Types, Effects of Particulate Pollutants and control by Cyclone separator	CO2	1,5,7	5
Air and Polition	Effects of Particulate Pollutants and control by Electrostatic Precipitator, Air (prevention and control of pollution) act 1981.	CO2	1,5,7	6
3.	Noise pollution: sources of pollution, Measurement of Noise pollution level.	CO3	1,5,7	7
Noise Pollution	Effects and Control of Noise pollution. Noise pollution (Regulation and Control) Rules, 2000	CO3	1,5,7	8

	Sources of water pollution. Types of water pollutants, Characteristics of water pollutants.	CO4	1,5,7	9
	Control measures of water pollution.	CO4	1,5,7	10
4. Water and Soil	Definition and list unit operations in water and WasteWater Treatment process, Water (prevention and control of pollution) act 1974.		1,5,7	11
Pollution:	Water conservation – Importance of Rain Water Harvesting	C04	1,5,7	12
	Soil pollution, Causes and Effects due to Fertilizers, Pesticides and Insecticides	CO4	1,5,7	13
	Preventive measures of Soil Pollution due to Excessive use of Fertilizers, Pesticides and Insecticides.	CO4	1,5,7	14
	Solar Energy: Basics of Solar energy. Solar collectors and advantages of Advanced solar collectors.	CO5	1,5,7	15
	Solar water heater, Solar stills and their uses.	CO5	1,5,7	16
	Biomass: Overview of biomass as energy source. Thermal characteristics of biomass as fuel.			17
5.	Wind energy: Current status and future prospects of wind energy. Wind energy in India.	CO5	1,5,7	18
Renewable sources of Energy	Need of new Energy sources, Different type's new energy sources. Environmental benefits of New Energy Sources-Hydrogen energy	CO5	1,5,7	19
	Environmental benefits of New Energy Sources- Ocean energy resources	CO5	1,5,7	20
	Environmental benefits of New Energy Sources-Tidal energy conversion.	CO5	1,5,7	21
	Solid waste generation, Sources, Characteristics of solid waste Solid Waste Management rules 2016	C06	1,5,7	22
6. Solid Waste	E- Waste generation Sources and characteristics, E waste management rules 2016	C06	1,5,7	23
Management and Environmental	Plastic Waste generation Sources and characteristics, Plastic Waste Sources and characteristics	C06	1,5,7	24
Acts	Recycled plastic rules 2016,Importance of Environment (protection) act 1986,	C06	1,5,7	25
	Occupational health and safety measures.	C06	1,5,7	26
			Total	26

### **References:**

### (a) Suggested Learning Resources:

### **Books:**

- 1. S.C. Sharma & M.P. Poonia, Environmental Studies, Khanna Publishing House, New Delhi
- 2. C.N. R. Rao, Understanding Chemistry, Universities Press (India) Pvt. Ltd., 2011.
- 3. Arceivala, Soli Asolekar, Shyam, Wastewater Treatment for Pollution Control and Reuse, Mc-Graw Hill Education India Pvt. Ltd., New York, 2007, ISBN:978-07-062099.
- 4. Nazaroff, William, Cohen, Lisa, Environmental Engineering Science, Willy, New York, 2000, ISBN 10: 0471144940.
- 5. O.P. Gupta, Elements of Environmental Pollution Control, Khanna Publishing House, New Delhi

- 6. Rao, C. S., Environmental Pollution Control and Engineering, New Age International Publication, 2007, ISBN: 81-224-1835-X.
- 1. Rao, M. N.Rao, H.V.N, Air Pollution, Tata Mc-Graw Hill Publication, New delhi, 1988, ISBN: 0-07-451871-8.
- 2. Frank Kreith, Jan F Kreider, Principles of Solar Engineering, McGraw-Hill, New York; 1978, ISBN: 9780070354760.
- 7. Aldo Vieira, Da Rosa, Fundamentals of renewable energy processes, Academic Press Oxford, UK; 2013. ISBN: 9780123978257.
- 3. Patvardhan, A.D, Industrial Solid Waste, Teri Press, New Delhi, 2013, ISBN:978-81-7993-502-
- 4. Metcalf & Eddy, Waste Water Engineering, Mc-Graw Hill, New York, 2013, ISBN: 077441206.
- 5. Keshav Kant, Air Pollution & Control, Khanna Publishing House, New Delhi (Edition 2018)

### (b) Open source software and website address:

- 1) www.eco-prayer.org
- 2) www.teriin.org
- 3) www.cpcp.nic.in
- 4) www.cpcp.gov.in
- 5) www.indiaenvironmentportal.org.in
- 6) www.whatis.techtarget.com
- 7) www.sustainabledevelopment.un.org
- 8) www.conserve-energy-future.com

### Teachers should use the following strategies to achieve the various outcomes of the course.

- Different methods of teaching and media to be used to attain classroom attention.
- Massive open online courses (MOOCs) may be used to teach various topics/subtopics.
- 15-20% of the topics which are relatively simpler or descriptive in nature should be given to the students for self-learning and assess the development of competency through classroom presentations.
- Micro-projects may be given to group of students for hand-on experiences
- Encouraging students to visit sites such as Railway station and research establishment around the institution.

### **Mapping of Course Outcomes with Programme Outcomes**

СО	Course Outcome	PO Mapped	Cognitive Level	Theory Sessions In Hrs	Allotted marks for CIE on cognitive levels		TOTAL
			R/U/A		R	U	
CO1	Importance Of ecosystem and terminology	1,5,7	R,U	03	02	02	04
CO2	The extent of air pollution, effects, control measures and acts.	1,5,7	R,U	03	03	02	05
CO3	The extent of noise pollution, effects, control measures and acts.	1,5,7	R,U	02	03	02	05
CO4	The water and soil pollution, effects, control measures and acts	1,5,7	R,U	06	03	02	05

CO5	Different renewable energy resources and efficient process of harvesting.	1,5,7	R,U	07	03	02	05
C06	Solid Waste Management and Environmental acts.	1,5,7	R,U	05	02	04	06
	Total Hours of instruction			26		30	

R-Remember; U-Understanding;.

### Level of Mapping PO's with CO's

Course			Programme Outcomes (PO's)					
	CO's	1	2	3	4	5	6	7
	CO1	3	0	0	0	2	0	1
	CO2	3	0	0	0	2	0	1
Francisco establication of	CO3	3	0	0	0	2	0	1
Environmental Science	<b>CO4</b>	3	0	0	0	2	0	1
	CO5	3	0	0	0	2	0	1
	C06	3	0	0	0	2	0	1

Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0- Not Mapped

Method is to relate the level of PO with the number of hours devoted to the CO s which maps the given PO. If  $\geq$ 50% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is mapped at Level 3 If 30 to 50% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is mapped at Level 2 If 5 to 30% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is mapped at Level 1 If < 5% of classroom sessions related to the CO are addressing a particular PO, it is considered that PO is considered not mapped i.e. Level 0

### **Course Assessment and Evaluation Chart**

Sl.	Assessment	Duration	Max marks	Conversion
No				
1.	CIE Assessment 1 ( Written Test -1 - At the end of	80 minutes	30	Average of
	3 <sup>d</sup> week			three written
2.	CIE Assessment 2 (Written Test -2) - At the end	80 minutes	30	tests
	of 7 week			30
3.	CIE Assessment 3 (Written Test -3) - At the end of	80 minutes	30	
	13 week			
4	CIE Assessment 4 (MCQ/Quiz) - At the end of 5	60 minutes	20	Average of
	week			three
5	CIE Assessment 5 ( Open book Test) - At the end	60 minutes	20	20
	of 9 week			
6	CIE Assessment 6 (Student activity/Assignment)-	60 minutes	20	
	At the end of 11 week			
7.	Total Continuous Internal Evaluation (CI	E) Assessment		50
				50
	Total Marks			

#### Note:

- 1. Average marks of Three CIE shall be rounded off to the next higher digit.
- 2. Assessment of assignment and student activity is evaluated through appropriate rubrics by the respective course coordinator. The secured mark in each case is rounded off to the next higher digit.

### MANDATORY STUDENT ACTIVITY: EACH STUDENT HAS TO SELECT ANY ONE OF THE LISTED

- 1. Students chose one thing to reduce at home each week and write journal entries about their successes and challenges implementing the change. In class, they form groups and create "Do You Know?" posters.
- 2. Students pretend they are architects, and come up with a series of design changes to make their school more environmentally friendly. They then grade their projects according to a rubric.
- 3. A presentation for Green Team Club members to introduce themselves and the purpose of their club. They explain how to use their new recycling bins, in the classroom and in the cafeteria.
- 4. Ever wonder what's in your school's waste? This hands-on activity helps students assess their school's waste in order to think of ways to reduce it. The results can be incorporated into the school's recycling plan.
- 5. How do we measure climate change? What activities contribute to climate change?
- 6. Start a compost or worm bin. Composting is a hands-on way to learn about important life science concepts such as ecosystems, food webs and biodegradation. Students experience how worms and other decomposers recycle fruits and vegetable scraps into compost. Use the compost in your college garden! Have green team students make up a skit and present details about the new composting program to all classrooms. Have them make signs for the bins (compost, recycle, and landfill), monitor the waste collection at lunchtime, cart the food waste to the compost, and decide how and where the compost will be used.
- 7. Paint posters and decorate bulletin boards or the doors to the cafeteria with waste- free lunch messages to announce or support a waste-free event, and have students vote for their favorite poster.
- 8. Conduct a classroom audit to identify waste and look for ideas to reduce and reuse. Empower the student to set goals, search for solutions and review progress.
- 9. Go on a field trip. Visit your local landfill, recycling center, or a nearby composing facility where the students can see first-hand what is happening to waste, and learn about the lifecycle of waste and its affect on the environment.
- 10. Home energy audit: Have students make a list of all the appliances and light bulbs in their house. How much energy does their house use if all the lights are on for 4 hours per day? If their appliances are on for 2 hours per day? How much energy could they save if they switched to energy-efficient appliances or lightbulbs?
- 11. Use recycled material in art projects:Recycled materials can make beautiful art projects such as jewelry, planters, and bird houses. Incorporating materials that would otherwise be thrown away into art projects can show your students how to find new uses for these items.

12. Life cycle :One way to show students what happens when you put something in the trash versus recycling or reusing the object is to do a life cycle analysis. This is a flow chart that shows the environmental impacts of an object, from extracting the raw materials to decomposition and everything in between. When something is put in the trash instead of being reused or recycled, the life cycle assessment will show a bigger environmental impact. When something is reused or recycled, the environmental impact is less because raw materials don't need to be extracted to create something new.

## Model Question Paper I A Test (CIE)

Program	nme :			Sem	ester: I
Course	:			Max M	arks : 30
Course	Code :	Durati	on : 1	Hr 20	minutes
Name of	f the course coordinator:			Test	: I/II/III
Note: An	swer one full question from each section. One full question carries 1	l0 marks.			
Qn.No	Question	CL	CO	PO	Marks
	Section-1				
1.a)					
b)					
c)					
2.a)					
b)					
c)					
	Section-2				
3.a)					
b)					
c)					
4.a)					
b)					
c)					
	Section-3				
5.a)					
b)					
c)					
6.a)					
b)					
c)					

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20PM01T	Semester	II
Course Title	Project Management Skills	Course Group	PM
No. of Credits	4	Type of Course	Activity based study
Course Category	Theory with Activities	Total Contact Hours	6 Hrs Per Week (2Theory +4 hrs of classroom activities) 78 Hrs Per Semester
Prerequisites	10 <sup>th</sup> Level Mathematics	Teaching Scheme	4 hrs per week classroom sessions dedicated to case studies & activities
CIE Marks	50	SEE Marks	50

### RATIONALE

**Project Management is a confluence of Management principles and Engineering subject area. This course enables** the students to develop conceptualisation of Engineering Management principles and apply the same for their engineering projects, in their domains, example, Software Development project or Construction Project and so on. The course integrates three core areas of Planning, Execution and Auditing of Projects.

### 1. COURSE SKILL SET

Student will be able to:

- 8. Understand what constitutes a project, Plan for the execution of the project by breaking into manageable work units, and Prepare necessary project artifacts
- 9. Track and control the Project while preparing verifiable records for Project Inspections and Audits
- 10. Inspect and Audit projects for Milestones or other project completion criteria and other metrics, Defects and remediation, Project learnings
- 11. Gain knowledge and develop curiosity on latest technology trends in Project management

### 2. COURSE OUT COMES

At the end of the course, student will be able to

CO1	Apply the concepts of Project Management to real projects which are expressed in the form of the Project reports or Engineering drawings					
CO2	Estimate Project resources needed Time, Material and Effort, and Plan for execution					
СО3	Understand, analyse and assess the risks involved in a project and plan for managing them					
CO4	Use Project Management Software and processes to track and control Projects					
CO5	Conduct inspection of Projects and audit progress and bills					
C06	Understand the Digital Technology trends in Project management and concepts like Smart cities					

### 3. SUGGESTED SPECIFICATION TABLE WITH HOURS & MARKS

HALLT		TEACHING	MARKS DISTRIBUTION(THEORY)					
UNIT NO	UNIT TITLE	HOURS (L-T-P)	R LEVEL	U LEVEL	A LEVEL	TOTAL		
1	Introduction	02-00-04	8	8	4	20		
2	Project Administration	06-00-12	8	12	20	40		
3	Project Lifecycle	04-00-08	8	12	20	40		
4	Project Planning, Scheduling and Monitoring	06-00-12	8	12	20	40		
5	Project Control, Review and Audit	06-00-12	8	12	20	40		
6	Digital Project Management	02-00-04	8	8	4	20		
	Total	26-00- 52=78	48	64	88	200		

**Legends:** R = Remember; U = Understand; A = Apply and above levels (Bloom's revised taxonomy)

### 4. DETAILS OF COURSE CONTENT

The following topics/subtopics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets.

UNIT NO	Unit skill set		Hours
	(In cognitive	<b>Topics / Subtopics</b>	L-T-P
	domain)		
1 Introduction	Use Basic Science, Maths skills to understand Project management and project planning, execution and control.	Introduction and definition, Features of a Project, Types of Projects, Benefits and Obstacles in Project Management, Project Management Profession, Role of Project manager, Consultants, Project and Operation, Project Management Process, Project Scope	02-00- 04
2 Project Administration	Able to develop WBS, PEP and PM processes for Project with given inputs	Project Administration, Project Team, Project Design, Work Breakdown Structure (WBS), Project Execution Plan (PEP), Systems and Procedure Plan, Project Direction, Communication and Co- ordination, Project Success	06-00- 12
		Case Study I	
3 Project Lifecycle	Use project administration and project lifecycle knowledge to Assess and plan for project risk	Project Life Cycle, Phases - Project Planning, Project Execution, Project Closure, Project Risks, Project Cost Risk Analysis, Time and Cost overruns	04-00- 08
		Case Study 2a	
4. Project Planning, Project Scheduling and Project Monitoring and Implementation	given the inputs on manpower, funds availability and time availability	Project Planning Function, Structure, Project Scheduling, Project monitoring and Project evaluation Case Study 2b	06-00- 12
5.Project Control, Review and Audit	Use Project Management lifecycle knowledge to Control project parameters, review and audit project performance	Project Control, Problems of Project Control, Gantt Charts, Milestone Charts, Critical Path Method (CPM), Network Technique in Project Scheduling, Crashing Project Duration through Network, Project Review, Initial Review, Performance Evaluation,	06-00- 12

		Abandonment Analysis, Project Audit Case Study 2c	
6.Digital Project Management	Understand latest trends of digital technologies impacting the domain of project management and application of the same in multiple scenario	Digital Technology trends in Project management, Cloud Technology, IoT, Smart cities, Data and analytics, case studies Case study 3	02-00- 04

### 5. MAPPING OF CO WITH PO

CO	Course Outcome	PO	UNIT	CL	Sessions	TOT
		Mapped	Linked	R/U/A	in Hrs	AL -
						Marks
	Understand the concepts of	1, 2, 5, 7	1, 2	R/U/A		
	Project Management in relation					
	to real projects which are				06	10
CO1	expressed in the form of the					
001	Project reports or Engineering					
	drawings					
	Case Study - I					
CO2	Estimate Project resources	1, 2, 3, 7	2, 3	R/U/A		
	needed Time, Material and				18	20
	Effort, and Plan for execution					
	Case study 2a					
CO3	Evaluate the risks involved in a	1,2,3,7	2,3	R/U/A	12	20
	project and Plan for managing					
	them					
	Case Study - 2a					

CO4	Use Project Management methods with Software and/or processes to track and control Projects  Case Study 2b	1, 4, 6, 7	4	R/U/A	18	20
CO5	Conduct inspection of Projects and audit progress and bills  Case Study 2c	1, 2, 5, 7	5	R/U/A	18	20
C06	Understand the Digital Technology trends in Project management, and Engineering Industries Case Study 3	1, 5, 7	6	R/U/A	06	10
	,	,		1	78	100

	CO's		Programme Outcomes s) (PO					
		1	1 2 3 4 5 6 7					
Project Management	CO1	3	3	0	0	2	0	1
	CO2	3	3	3	0	0	0	1
	CO3	3	0	0	3	0	3	1
	CO4	3	0	0	3	0	3	1
	CO5	3	2	0	0	2	0	1
	C06	3	0	0	0	2	0	2

Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0-**Not Mapped** 

### 7. INSTRUCTIONAL STRATEGY

These are sample Strategies, which teacher can use to accelerate the attainment of the various course outcomes

1. Explicit instruction will be provided in intervention classes or by using different differentiation strategies in the main classroom.

- 2. Lecturer method (L) does not mean only traditional lecture method, but different type of teaching method and media that are employed to develop the outcomes.
- 3. Observing the way their more proficient peers use prior knowledge to solve current challenges and persevere in problem solving will help struggling students to improve their approach to engaging with rich contextual problems.
- 4. Topics be introduced always with a reallife example and then answering What, how, why and when.
- 5. The teacher is able to show different ways to solve the same problem and encourage the students to come up with their own creative ways to solve them.
- 6. In a perfect world, teacher would always be able to demonstrate how every concept can be applied to the real world and when that's possible, it helps improve the students' understanding. When a concept cannot be applied in that manner, we can still share how it might be applied within mathematics.

### 8. SUGGESTED LEARNING RESOURCES:

SlNo.	Author	Title of Books	Publication/Year
1	Dr. Lalitha Balakrishnan & Dr. Gowri Ramachandran	Project Management	Himalaya Publishing, 2019
2	Shailesh Kumar Shivakumar	Complete Guide to Digital Project Management	Apress, 2019
3	Prasanna Chandra	Project planning, analysis, selection, implementation and review	Tata McGraw Hill
4	Gopala Krishnan	Project Management	Mcmillan India Ltd.

### 9. COURSE ASSESSMENT AND EVALUATION CHART

Sl.No	Assessment	Duration	Max marks	Conversion
	CIE Assessment 1 ( Written Test -1)			Average of three written tests
1	At the end of 3 <sup>rd</sup> week	80 minutes	30	30
	CIE Assessment 2 (Written Test -2)			30
2	At the end of 7 <sup>th</sup> week	80 minutes	30	

Sl.No	Assessment	Duration	Max marks	Conversion
	CIE Assessment 3		marks	
	(Written Test -3)			
3	At the end of 13 <sup>th</sup> week	80 minutes	30	
	CIE Assessment 4			Average of three
	(Group Assignment -1)			20
4	At the end of 5 <sup>th</sup> week	60 minutes	20	20
	CIE Assessment 5			
	(Group Assignment -2)			
5	At the end of 9 <sup>th</sup> week	60 minutes	20	
	CIE Assessment 6			
	(Individual Student			
	activity/Assignment) At			
6	the end of 11 <sup>th</sup> week	60 minutes	20	
	Total Continuous Internal Eval	uation (CIE) Ass	essment	50
	Semester End			
8	Examination (SEE)	3 Hrs	100	50
	Assessment (Written Test)			
	Total Mark	KS		100

### Note:

- 1. SEE (Semester End Examination) is conducted for 100 Marks theory course for a time duration of 3 Hrs
- 2. Three CIE (written test), each of 30 marks for a time duration of 80 minutes shall be conducted. Also, three CIE (MCQ or Quiz/Group Assignment/Individual student activity or assignment) each of 20 marks for the time duration of 60 minutes shall be conducted. Any fraction at any stage during evaluation will be rounded off to the next higher digit
- 3. Assessment of assignment and student activity is evaluated through appropriate rubrics by the respective course coordinator. The secured mark in each case is rounded off to the next higher digit.

### 10 DETAILED COURSE CONTENT

Unit No And Name	DETAILED COURSE CONTENT	CONTACT HRS	TOTAL
	1.1 Introduction	3	

Unit No And Name	DETAILED COURSE CONTENT	CONTACT HRS	TOTAL
	1.2 Meaning of Project		
	1.3 Definition and No Change Mode		
1	1.4 Features of a Project		C
1. Introduction	1.5 Types of Projects		6
	1.6 Benefits of Project Management		
	1.7 Obstacles in Project Management		
	1.8 Project Management A Profession		
	1.9 Project Manager and His Role		
	1.10 Project Consultants		
	1.11 What is Operation?	3	
	1.12 Difference between Project and Operation		
	1.13 What is Process in Project Management and		
	Process Groups?	_	
	1.14 What is Scope? Difference between Project Group Objectives and		
-	1.15 Project Scope		
2. Project	2.1 Essentials of Project Administration	3	18
Administrat	2.2 Project Team		
ion	2.3 Project Design		
	2.4 Work Breakdown Structure (WBS)		
	2.5 Project Execution Plan (PEP)	6	
	2.6 Contracting Plan		
	2.7 Work Packing Plan		
	2.8 Organisation Plan	3	
	2.9 Systems and Procedure Plan		
	2.10 Project Procedure Manual		
	2.11 Project Diary	3	
	2.12 Project Execution System		
	2.13 Project Direction		
	2.14 Communication in a Project	3	-
	2.15 Project Co-ordination		

	2.16 Pre-requisites for Successful Project Implementation		
3. Project	3.1 Introduction	6	12
Lifecycle	3.2 Phases of Project Life Cycle		
	3.3 Project Management Life Cycle General		
	3.4 Project Planning		
	3.5 Project Execution		
	3.6 Project Closure		
	3.7 Project Risks	3	
	3.8 Types of Risks: Illustrations		
	3.9 Risk Assessment Techniques with Illustrations		
	3.10 Project Cost Risk Analysis	3	
	3.11 Estimating Time and Cost Overrun Risks		
	3.12 Organisation/Procedural/Systemic Reasons for Project Cost Overruns		
	3.13 Time Overruns		
4. Project Planning,	4.1 Introduction	6	18
Scheduling	4.2 Nature of Project Planning		
and Monitoring	4.3 Need for Project Planning		
	4.4 Functions of Project Planning		
	4.5 Steps in Project Planning		
	4.6 Project Planning Structure		
	4.7 Project Objectives and Policies		
	4.8 Tools of Project Planning		
	4.9 Project Scheduling	6	
	4.10 Time Monitoring Efforts		
	4.11 Bounding Schedules		
	4.12 Scheduling to Match Availability of Manpower		
	4.13 Scheduling to Match Release of Funds		
	4.14 Problems in Scheduling Real-life Projects		
	4.15 Introduction	3	

	4.16 Situation Analysis and Problem Definition		
	4.17 Setting Goals and Objectives		
	4.18 Generating Structures and Strategies		
	4.19 Implementation		
	4.20 What is Project Evaluation?	3	
	4.21 Why is Project Evaluation Important?		
	4.22 What are the Challenges in Monitoring and Evaluation?		
5. Project	5.1 Introduction	6	18
Control, Review and	5.2 Projected Control Purposes		
Audit	5.3 Problems of Project Control		
	5.4 Gantt Charts		
	5.5 Milestone Charts		
	5.6 Critical Path Method (CPM)	6	
	5.7 Construction of a Network		
	5.8 Network Technique in Project Scheduling		
	5.9 Crashing Project Duration through Network		
	5.10 Project Review	3	
	5.11 Initial Review		
	5.12 Post Audit		
	5.13 Performance Evaluation		
	5.14 Abandonment Analysis		
	5.15 Objectives of Project Audit	3	
	5.16 Functions of Project Auditor		
	5.17 Project Audit Programme		
	5.18 Difficulties in Establishing Audit Purpose and Scope		
6. Digital Project	6.1 Digital Technology trends in Project management	1	6
Management	6.2 Cloud Technology, IoT, AR and VR applications in Project management, Smart Cities	1	

6.3 Data Science and Analytics in Project Management	1
6.4 Case Studies	3

### **Case Studies:**

**Please note:** The Tutors can either use the following Case studies and activities or Design on their own, with the overall Learning Outcomes being met.

### **Case Study I: Residential House - Project Execution Plan**

- 1. Dr. Sunil Kulkarni wants to build a house on his 9000 square feet (90x100) vacant plot in Bengaluru. His requirements were given below.
  - i) He lives with his wife, parents and two college going children.
  - ii) He likes open space around his house and likes to do gardening during free time
  - iii) His wife teaches Yoga and about 30 middle aged and old people attend the daily sessions.
  - iv) He has a budget limitation of INR 230,00,000 for this project and wants to present to his wife on their 20<sup>th</sup> wedding anniversary which is 18 months away.
  - v) His parents can not climb stairs and hence prefer a ground floor room
  - vi) All the rooms should have attached bathrooms

How-ever the Civil contractor who took the work, overshot the time and money available and hence Dr Sunil was unhappy with the Architect firm who recommended the Contractor.

### Task:

- Split the class into groups of three
- Ask them to prepare 2D drawings with Plan, Elevation, Sections and perspectives.
- Prepare the detailed WBS, a Project execution plan and Project communication plan for contractors
- Estimate the quantities
- Discuss on the possible reasons for delay and methods with which performance to both time and budget could have been achieved
- Present it in a seminar, with each group getting 5-10 minutes to present their idea.

### Case Study 2a:

The Columbus Hospital proposed in Hubli is a 200 bed speciality private hospital for treatment of Cancer. The hospital will come up on a 12 acre plot between Hubli-Dharwad. A leading construction company has come forward to complete the hospital works from concept to commissioning in 9 months. The promoters are willing to spend a premium to complete the hospital in 9 month time and are not particular about type of construction, ie, RCC, Steel frame etc. The key requirements are as follows:

- vi) 200 bed hospital of which 40 are for critical care (ICU), 40 for pre and post Operative care
- vii) 4 Operation Theatres 2 Major (Minimum 800 SFT each) and 2 minor (minimum 400 sft each)
- viii) One full fledged Diagnostic laboratory (1500 Sft)
- ix) One 24x7 pharmacy (360 Sft min)
- x) Doctors rooms, Nurses enclosures, Change rooms
- xi) Office with billing counters (min 2000 sft) for all administrative staff
- xii) Wheel chair parking bays, Stretcher parking bays in all floors
- xiii) One Cafetaria with 50 person capacity
- xiv) One conference room with Multimedia equipment (300 sft min)
- xv) Parking for ambulances, 4 wheelers, two wheelers
- xvi) Reception and enquiry counter
- xvii) All amenities should be accessible for disabled persons
- xviii) Incinerator, Waste storage and disposal area
- xix) Generator and fuel storage area

### Discuss

- vii) The various alternative approaches available to complete the hospital.
- viii) Look into National Building Code and BIS standards for arriving at approximate (+/- 10%) super built-up area required, amenities to be planned
- ix) The various phases of the project according to Project lifecycle and durations
- x) Prepare the detailed WBS, Project Organisation required and Project Dairy template
- xi) Prepare a Project Plan with risks involved and the risk management plan.
- xii) Estimate the cost of time overrun if the project is delayed by 114 calendar days due to issues with approvals

### Case Study 2b:

For case study 2 above, prepare an Implementation Plan using a spreadsheet software.

### Discuss

- i) What happens if a pandemic affects the project in its 7<sup>th</sup>Month. How do you mitigate the possible issues in implementation?
- ii) What happens if during the fourth month of projects the client decides to reduce funds for the month by 50%?

### Case Study 2c:

For case study 2 above, prepare a Critical Path method Chart (CPM) showing all main activities in the WBS with milestones.

### Discuss

- xix) What happens if the client decides to complete the ground floor roof 15 days earlier?
- xx) What happens if the client reduces the inflow of project funds by 50% for the month 4?
- xxi) Write an Audit report for the project at the end of 6<sup>th</sup> month

### Case Study 3:

This will be done as a student activity and has two components.

- xiv) Research on 3D printing in any industry and prepare a three page article
- xv) Study usage of Drones in different Industries and evaluate the Cost benefits of using the same for any one scenario.

### **Model Question Paper**

### I A Test (CIE)

Program	nme: Se	Semester: I Max Marks: 30					
Course:	Ma						
Course	Code: Duration: 1 H	<b>Duration: 1 Hr 20 minutes</b>					
Name o	f the course coordinator: Te	st: I/I	I/III				
Note: Ar	nswer one full question from each section. One full question carrie	s 10 m	ıarks.				
Qn.No	Question	CL	CO	PO	Marks		
	Section-1	•		•	•		
1.a)							
b)							
c)							

2.a) b) c) Section-2 3.a) b) c) 4.a) b) c) Section-3 5.a) b) c) 6.a) b)				
C)         Section-2           3.a)                     b)                     c)                     4.a)                     b)                     c)                     5.a)                     b)                     c)                     6.a)                     b)	2.a)			
Section-2  3.a) b) c) 4.a) b) c) Section-3  5.a) b) c) 6.a) b)	b)			
3.a) b) c) 4.a) b) c) Section-3 5.a) b) c) c) 6.a)	c)			
b) c)	<u> </u>	Section-2		
c) 4.a) b) c) Section-3  5.a) b) c) 6.a) b)	3.a)			
4.a) b) c) Section-3  5.a) b) c) c) 6.a)	b)			
b) c) Section-3  5.a) b) c) c) 6.a) b) c)	c)			
c)         Section-3           5.a)	4.a)			
Section-3  5.a)  b)  c) 6.a)  b)	b)			
5.a)         b)         c)         6.a)         b)	c)			
b) c) 6.a) b)		Section-3		
c) 6.a) b)	5.a)			
6.a) b)	b)			
b)	c)			
	6.a)			
	b)			
	c)			

## Model Question Paper Semester End Examination

Programme:	Semester: I
Course:	Max Marks: 100
Course Code:	Duration: 3 Hrs

Course Code:		Duration	n: 3 Hrs	
	<b>Instruction to the Candidate:</b> Answer one full question from each section. One full	l question carri	es 20 ma	ırks.
Qn.No	Question	CL	СО	Marks
I	Section-1	I		
1.a)				
b)				
2.a)				
b)				
I	Section-2	l	1	.1
3.a)				
b)				
4.a)				
b)				
•	Section- 3	1		
5.a)				
b)				
6.a)				
b)				
·	Section-4	·		
7.a)				
b)				
8.a)				
b)				
	Section-5			
9.a)				
b)				
10.a)				
b)				

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20SC01T	Semester	I/II
Course Title	ENGINEERING MATHEMATICS	Course Group	Core
No. of Credits	4	Type of Course	Lecture
Course Cotogowy	Theory	Total Contact Hours	4Hrs Per Week
Course Category	Theory	Total Contact Hours	52Hrs Per Semester
Prerequisites	10 Level Mathematics	Teaching Scheme	(L:T:P) = 4:0:0
CIE Marks	50	SEE Marks	50

### **RATIONALE**

Engineering Mathematics specification provides students with access to important mathematical ideas to develop the mathematical knowledge and skills that they will draw on in their personal and work lives. The course enables students to develop mathematical conceptualization, inquiry, reasoning, and communication skills and the ability to use mathematics to formulate and solve problems in everyday life, as well as in mathematical contexts. At this level, the mathematics curriculum further integrates the three content areas taught in the higher grades into three main learning areas: Algebra; Measurement of angles and Trigonometry and Calculus.

### 1. COURSE SKILL SET

Student will be able to:

- 1. Solve system of linear equations arise in different engineering fields
- 2. Incorporate the knowledge of calculus to support their concurrent and subsequent engineering studies
- 3. Adept at solving quantitative problems
- 4. Ability to understand both concrete and abstract problems
- 5. Proficient in communicating mathematical ideas
- 6. Detail-oriented

### 2. COURSE OUT COMES

At the end of the course, student will be able to

CO1	Determine the inverse of a square matrix using matrix algebra. Apply the concepts of matrices and determinants to solve system of linear equations and find eigen values associated with the square matrix.
CO2	Find the equation of straight line in different forms. Determine the parallelism and perpendicularity of lines.
CO3	Calculate trigonometric ratios of allied angles and compound angles. Transform sum or difference of trigonometric ratios into product and vice versa.

CO4	Differentiate various continuous functions and apply the concept in real life situations.
CO5	Integrate various continuous functions and apply the concept in evaluating the area and volume through definite integrals.

### 3. SUGGESTED SPECIFICATION TABLE WITH HOURS & MARKS

		<b></b>	DISTRIBUTION(THEORY)				
UNIT NO	IINIT TITLE		R LEVEL	U LEVEL	A LEVEL	TOTAL	
1	Matrices and Determinants	10	8	20	12	40	
2	Straight lines	10	8	20	12	40	
3	Trigonometry	10	8	20	12	40	
4	Differential Calculus and applications	11	8	20	12	40	
5	Integral Calculus and applications	11	8	20	12	40	
	Total	52	40	100	60	200	

**Legends:** R = Remember; U = Understand; A = Apply and above levels (Bloom's revised taxonomy)

### 4. DETAILS OF COURSE CONTENT

The following topics/subtopics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets.

UNIT	Unit skill set	Topics/Subtopics	Hours
NO	(In cognitive domain)		L-T-P
UNIT-1 MATRICES AND DETERMINANTS	Use algebraic skills which are essential for the study of systems of linear equations, matrix algebra and eigen values	<ul> <li>1.1 Matrix and types</li> <li>1.2 Algebra of Matrices (addition, subtraction, scalar multiplication and multiplication)</li> <li>1.3 Evaluation of determinants of a square matrix of order 2 and 3. Singular matrices</li> <li>1.4 Cramer's rule for solving system of linear equations involving 2 and 3 variables</li> <li>1.5 Adjoint and Inverse of the nonsingular matrices of order 2 and 3</li> <li>1.6 Characteristic equation and Eigen values of a square matrix of order 2</li> </ul>	10-0-0

		1	1
UNIT-2 STRAIGHT LINES	<ul> <li>Able to find the equation of a straight line in different forms</li> <li>Determine whether the lines are parallel or perpendicular</li> </ul>	<ul> <li>2.1 Slope of a straight line</li> <li>2.2 Intercepts of a straight line</li> <li>2.3 Intercept form of a straight line</li> <li>2.4 Slope-intercept form of a straight line</li> <li>2.5 Slope-point form of a straight line</li> <li>2.6 Two-point form of a straight line</li> <li>2.7 General form of a straight line</li> <li>2.8 Angle between two lines and conditions for lines to be parallel and perpendicular</li> <li>2.9 Equation of a straight line parallel to the given line</li> <li>2.10 Equation of a straight line perpendicular to the given line</li> </ul>	10-0-0
UNIT-3 TRIGONOMETRY	<ul> <li>Use basic trigonometric skills in finding the trigonometric ratios of allied and compound angles</li> <li>Able to find all the measurable dimensions of a triangle</li> <li>Able to differentiate</li> </ul>	<ul> <li>3.1 Concept of angles, their measurement, Radian measure and related conversions.</li> <li>3.2 Signs of trigonometric ratios in different quadrants (ASTC rule)</li> <li>3.3 Trigonometric ratios of allied angles (definition and the table of trigonometric ratios of standard allied angles say 90°±Θ, 180°±Θ, 270°±Θ and 360°±Θ)</li> <li>3.4 Trigonometric ratios of compound angles (without proof)</li> <li>3.5 Trigonometric ratios of multiple angles</li> <li>3.6 Transformation formulae</li> <li>4.1 Derivatives of continuous functions in an</li> </ul>	10-0-0
UNIT-4 DIFFERENTIAL CALCULUS AND APPLICATIONS	<ul> <li>Able to differentiate algebraic, exponential, trigonometric, logarithmic and composite functions</li> <li>Able to find higher order derivatives</li> <li>Understand and work with derivatives as rates of change in mathematical models</li> <li>Find local maxima and minima of a function</li> </ul>	<ul> <li>4.1 Derivatives of continuous functions in an interval (List of formulae)</li> <li>4.2 Rules of differentiation</li> <li>4.3 Successive differentiation (up to second order)</li> <li>4.4 Applications of differentiation</li> </ul>	11-0-0
UNIT-5 INTEGRAL CALCULUS AND APPLICATIONS	<ul> <li>Understand the basic rules of integration and Evaluate integrals with basic integrands.</li> <li>Identify the methods to evaluate integrands</li> <li>Apply the skills to evaluate integrals representing areas and volumes</li> </ul>	<ul> <li>5.1 List of standard integrals and Basic rules of integration</li> <li>5.2 Evaluation of integrals of simple function and their combination</li> <li>5.3 Methods of integration</li> <li>5.4 Concept of definite integrals</li> <li>5.5 Applications of definite integrals</li> </ul>	11-0-0

### 5. MAPPING OF CO WITH PO

CO	Course Outcome	PO	UNIT	CL	Theory	TOT
CO	Course Outcome	Mapped	Linked	R/U/A	in Hrs	AL
CO1	Determine the inverse of a square matrix using matrix algebra. Apply the concepts of matrices and determinants to solve system of linear equations and find eigen values associated with the square matrix.		1	R/U/A	10	40
CO2	Find the equation of straight line in different forms. Determine the parallelism and perpendicularity of lines.	1,7	2	R/U/A	10	40
CO3	Calculate trigonometric ratios of allied angles and compound angles.  Transform sum (difference) of trigonometric ratios into product and vice versa.	1, 7	3	R/U/A	10	40
CO4	Differentiate various continuous functions and apply the concept in real life situations.	1, 3, 7	4	R/U/A	11	40
CO5	Integrate various continuous functions and apply the concept in evaluating the area and volume through definite integrals.	1, 3, 7	5	R/U/A	11	40
					52	200

Course	CO's	Programme Outcomes (PO's)						
		1	2	3	4	5	6	7
	CO1	3	1	0	0	0	0	3
	CO2	3	1	0	0	0	0	3
ENGINEERING MATHEMATICS	CO3	3	1	0	0	0	0	3
	CO4	3	1	3	0	0	0	3
	CO5	3	1	3	0	0	0	3

Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0- Not Mapped

### 7. INSTRUCTIONAL STRATEGY

These are sample Strategies, which teacher can use to accelerate the attainment of the various course outcomes

- 1. Explicit instruction will be provided in intervention classes or by using different differentiation strategies in the main classroom.
- 2. Lecturer method (L) does not mean only traditional lecture method, but different type of teaching method and media that are employed to develop the outcomes.
- 3. Observing the way their more proficient peers use prior knowledge to solve current challenges and persevere in problem solving will help struggling students to improve their approach to engaging with rich contextual problems.
- 4. Ten minutes a day in homeroom, at the end of class, or as a station in a series of math activities will help students build speed and confidence.
- 5. Topics will be introduced in a multiple representation.
- 6. The teacher is able to show different ways to solve the same problem and encourage the students to come up with their own creative ways to solve them.
- 7. In a perfect world, teacher would always be able to demonstrate how every concept can be applied to the real world and when that's possible, it helps improve the students' understanding. When a concept cannot be applied in that manner, we can still share how it might be applied within mathematics.

### 8. SUGGESTED LEARNING RESOURCES:

Sl. No.	Author	Title of Books	Publication/Year
1	B.S. Grewal	Higher Engineering Mathematics	Khanna Publishers, New Delhi, 40th Edition,2007
2	G. B. Thomas, R. L. Finney	Calculus and Analytic Geometry	Addison Wesley, 9th Edition, 1995
3	S.S. Sabharwal, Sunita Jain, Eagle Parkashan	Applied Mathematics, Vol. I & II	Jalandhar.
4	Comprehensive Mathematics	Comprehensive Mathematics Vol. I & II	Laxmi Publications, Delhi
5	ReenaGarg&Chandrik a Prasad	Advanced Engineering Mathematics	Khanna Publishing House, New Delhi

### 9. COURSE ASSESSMENT AND EVALUATION CHART

Sl.No.	Assessment	Duration	Max marks	Conversion
1	CIE Assessment 1 (Written Test -1) At the end of 3 <sup>d</sup> week	80 minutes	30	Average of three written tests
2	CIE Assessment 2 (Written Test -2) At the end of 7 week	80 minutes	30	30
3	CIE Assessment 3 (Written Test -3) At the end of 13 week	80 minutes	30	
4	CIE Assessment 4 (MCQ/Quiz) At the end of 5 week	60 minutes	20	
5	CIE Assessment 5 (Open book Test) At the end of 9 week	60 minutes	20	Average of three
6	CIE Assessment 6 (Student activity/Assignment) At the end of 11 week	60 minutes	20	20
	Total Continuous Internal E	50		
8	Semester End Examination (SEE) Assessment (Written Test)	3 Hours	100	50
	Total 1	Marks		100

### Note:

- 1. SEE (Semester End Examination) is conducted for 100 Marks theory courses for a time duration of 3 Hours.
- 2. Three CIE (written test), each of 30 marks for a time duration of 80 minutes shall be conducted. Also, three CIE (MCQ or Quiz/Open book test/student activity or assignment) each of 20 marks for the time duration of 60 minutes shall be conducted. Any fraction at any stage during evaluation will be rounded off to the next higher digit
- 3. Assessment of assignment and student activity is evaluated through appropriate rubrics by the respective course coordinator. The secured mark in each case is rounded off to the next higher digit.

### 10 DETAILED COURSE CONTENT

UNIT NO AND NAME	DETAILED COURSE CONTENT	со	PO	CONTACT HRS	TOTAL	
	Definition and types of matrices	1	1,7	1		
	Algebra of Matrices (addition, subtraction and scalar multiplication) problems	1	1,7	1		
AN	Multiplication of Matrices(problems)	1	1,7	1		
1 MATRICES AND DETERMINANTS	Evaluation of 2x2 ,3x3 determinants and Singular matrices and problems in finding unknown variable	1	1,7	1	10	
	Cramer's rule to solve system of linear equation with 2 and 3 variables	1	1,7	1		
	Cramer's rule to solve system of linear equation with 2 and 3 variables.problems	1	1,7	1		
	Minors, Cofactors of elements of square matrices of order 2 and 3	1	1,7	1		
	Adjoint of a square matrix(2x2 and 3x3),Inverse of a non singular square matrix	1	1,7	1		
	Adjoint of a square matrix(2x2 and 3x3),Inverse of a non singular square matrix and problems	1	1,7	1		
	Characteristic equation and eigen values of a 2x2 matirx and problems	1	1,7	1		
	Slope of the straight line(provided with inclination and two points on the line as well) and problems	2	1,7	1		
	Intercepts of a straight line and problems	2	1,7	1		
	Intercept form of a straight line and problems	2	1,7	1		
E	Slope-intercept form of a straight line and problems	2	1,7	1		
	Slope-point form of the straight line and problems	2	1,7	1	10	
2 HTLINES	Two-point form of a straight line and problems	2	1,7	1		
2 STRAIGH	General form of a straight line.problems on finding slope and intercepts.	2	1,7	1		
	Angle between two straight lines and conditions for the lines to be parallel and perpendicular and problems	2	1,7	1		
	Equation of a line parellel to the given line and problems	2	1,7	1		
	Equation of a line perpendicular to the given line.problems	2	1,7	1		

'RY	Concept of angles and their measurement. Radian measures and related conversions (degree to radian and vice-versa) and problems	3	1,7	1		
	Signs of trigonometric ratios in different quadrants (ASTC rule)	3	1,7	1		
	Trigonometric ratios of allied angles (definition and the table of trigonometric ratios of standard allied angles say $90^{\circ}\pm\theta$ , $180^{\circ}\pm\theta$ , $270^{\circ}\pm\theta$ and $360^{\circ}\pm\theta$ )	3	1,7	1		
oxdapsilon	Problems on allied angles. (proving identities)	3	1,7	1		
3 TRIGONOMETRY	Problems on allied angles. (Finding values of x in an identity)	3	1,7	1	10	
	Trigonometric ratios of compound angles (without proof)	3	1,7	1		
TR	Trigonometric ratios of multiple angles (sin2A, cos2A, tan2A, sin3A, cos3A and tan3A)	3	1,7	1		
	Problems on multiple angles sin2A, cos2A, tan2A, sin3A, cos3A and tan3A	3	1,7	1		
	Transformation formulae (without proof) as sum to product. (Simple problems)	3	1,7	1		
	Transformation formulae (without proof) as product to sum. (Simple problems)	3	1,7	1		
AND	Definition of a derivative of a function. Listing the derivatives of standard functions. (Algebraic, trigonometric, exponential, logarithmic and inverse trigonometric functions)	4	1,3,7	1		
S Trus	Addition and subtraction rule of differentiation and problems	4	1,3,7	1		
ALCU	Product rule and quotient rule of differentiation and problems	4	1,3,7	1		
4 DIFFERENTIAL CALCULUS AND APPLICATIONS	Product rule and quotient rule of differentiation and problems	4	1,3,7	1	11	
	Composite functions and their derivatives. (CHAIN RULE)	4	1,3,7	1		
	Composite functions and their derivatives. (CHAIN RULE). Problems	4	1,3,7	1		
	Successive differentiation up to second order	4	1,3,7	1		
	Slope of the tangent and normal to the given curve and their equations and problems	4	1,3,7	1		

	Rate measure: velocity and acceleration at a point of time and problems	4	1,3,7	1	
	Local Maxima and Minima of a function	4	1,3,7	1	
	Local Maxima and Minima of a function. Problems	4	1,3,7	1	
5 CALCULUS AND APPLICATIONS	Definition of an indefinite integral. Listing the Integrals of standard functions. (Algebraic, trigonometric, exponential, logarithmic and inverse trigonometric functions)	5	1,3,7	1	
TIC	Rules of Integration. Evaluation of integrals with simple integrands and their combinations	5	1,3,7	1	
APF	Rules of Integration. Evaluation of integrals with simple integrands and their combinations. Problems	5	1,3,7	1	
AND	Evaluation of integrals with simple integrands and their combinations. Problems	5	1,3,7	1	11
ro so	Evaluation of integrals by Substitution method	5	1,3,7	1	
<b>1</b>	Evaluation of integrals by Integration by parts	5	1,3,7	1	
rcnı	Evaluation of integrals by Integration by parts. Problems	5	1,3,7	1	
[Ā	Definition of definite integrals and their evaluation	5	1,3,7	1	
INTEGRAL	Evaluation of Definite integrals. Problems	5	1,3,7	1	
	Area enclosed by the curves by integral method	5	1,3,7	1	
	Volume generated by the curve rotated about an axis by integral method	5	1,3,7	1	

5

### First Semester Examination, Model Question Paper – 2020 **Engineering Mathematics**

**Subject Code: 20SC01T Duration: 3Hours** Max. Marks:100

**Instruction:** Answer one full question from each section. One full question carries 20 marks.

### SECTION - 1

1

a If the matrix 
$$\begin{bmatrix} 2 & 4 & 6 \\ 2 & x & 2 \\ 6 & 8 & 14 \end{bmatrix}$$
 is singular then find  $x$ .

Find the A<sup>2</sup> for the matrix  $\begin{vmatrix} 1 & 3 & 4 \\ -1 & 2 & 1 \\ 0 & 3 & 3 \end{vmatrix}$ .

Solve 2x - y = 3 and x + 2y = 4 by using determinant method. 5

Find the inverse of the matrix  $\begin{bmatrix} 2 & 3 & 1 \\ -1 & 2 & 1 \\ 5 & 4 & 3 \end{bmatrix}$ . 6

2

**a** If 
$$A = \begin{bmatrix} 2 & -1 \\ 4 & 0 \\ 1 & 3 \end{bmatrix}$$
 and  $B = \begin{bmatrix} 1 & -3 & 4 \\ -1 & -1 & 1 \\ 0 & 4 & 2 \end{bmatrix}$  then find  $(AB)^{T}$ .

Verify whether AB=BA for the matrices  $A = \begin{bmatrix} 1 & 0 & 5 \\ -1 & 2 & 1 \\ 5 & 4 & 3 \end{bmatrix}$  and b

5  $B = \begin{bmatrix} 3 & -1 & 4 \\ 0 & -1 & 1 \\ 2 & 4 & 2 \end{bmatrix}.$ 

Find the Adjoint of the matrix  $A = \begin{bmatrix} 1 & 3 & 4 \\ -1 & 2 & 1 \\ 0 & 3 & 3 \end{bmatrix}$ . 5

Find the charcteristic equation and eigen values for the matrix  $\begin{bmatrix} 1 & 2 \\ 3 & 1 \end{bmatrix}$ . d 6

### SECTION - 2

3

a If the straight line is passing through the points (1, 2) and (3, 5) then find the 4 slope of the line.

Write the standard intercept form of the straight line and hence find the b equation of the straight line whose x and y intercepts are 2 and 3 respectively.

5

Write the standard slope-intercept form of a straight line. Find the equation C of the straight line passing through the point (3, 5) and slope 4 units.

5

Find the equation of the straight line parallel to the line passing through the d points (1, 3) and (4, 6).

6

4

i) If a line inclined at 45° with x-axis find its slope. ii) Write а 2+2 the x and y intercept of the line 2x+3y=10.

Find the equation of the straight line whose angle of inclination is 45<sup>0</sup> and b passingthrough the origin.

5

Find the equation of the straight line perpendicular to the line 2x+6y=3 and C with the y intercept 2 units.

5

Find the acute angle between the lines 7x-4y=0 and 3x-11y+5=0. d

6

### **SECTION - 3**

5 а Express 75° in radian measure and  $3\pi/2$  in degree.

4

Prove that  $cos(A + B)cos(A - B) = cos^2 A - sin^2 B$ . b

5

Show that  $\cos 2\theta = 2\cos^2 \theta - 1$ . C

5

Find the value of  $\sin 120^{\circ} \cdot \cos 330^{\circ} - \sin 240^{\circ} \cdot \cos 390^{\circ}$  without using d calculator.

6

### Find the value of sin 15°

**b** Simplify 
$$\frac{\cos(360^{\circ} - A)\tan(360^{\circ} + A)}{\cot(270^{\circ} - A)\sin(90^{\circ} + A)}$$
.

**c** Prove that  $\sin 3\theta = \sin 3\theta - 4\sin^3 \theta$ .

**d** Prove that  $\sin 20^\circ \cdot \sin 40^\circ \cdot \sin 80^\circ = \frac{\sqrt{3}}{8}$ .

### SECTION - 4

- 7 a Find the derivative of  $y = x^2 + e^{2x} + \cos 2x 2\log x$  with respect to x.
  - **b** Find dy/dx of  $y = \frac{\sec x + \tan x}{\sec x \tan x}$ .
  - **c** Find dy/dx of  $y = \tan^{-1} \left( \frac{1+x}{1-x} \right)$ .
  - d If the  $s = 2x^3 + 3x + 4$  repersents the displacement of the particle in motion at time x, then find the velocity of the particle at x = 2 secs and acceleration at x = 3 secs.
- 8 a Find  $\frac{dy}{dx}$  of  $y = 3x^4 + 4\log x + 2e^{3x} + \tan^{-1} x$ .
  - **b** If  $y = e^{2x} \sin 3x$  then find  $\frac{dy}{dx}$ .
  - **c** Find  $\frac{d^2 y}{dx^2}$  if  $y = 3\sin x + 4\cos x$  at x = 1.
  - **d** Find the equation of tangent and normal to the curve  $y = x^2$  at the point (1, 1).

### **SECTION - 5**

**9 a** Evaluate 
$$\int (x-1)(x+1)dx$$
.

**b** Evaluate 
$$\int_{0}^{p/2} \sin^2 x \ dx$$

**c** Evaluate 
$$\int x \sin x dx$$
.

**d** Find the area bounded by the curve 
$$y = 4x - x^2 - 3$$
, x-axis and ordinates  $x = 1$  and  $x = 3$ .

10 a Evaluate 
$$\int_{0}^{2} e^{x} dx$$
.

**b** Evaluate 
$$\int \frac{4\cos(\log x)}{x} dx$$
.

c Evaluate 
$$\int xe^x dx$$
.

**d** Find the volume of the solid generated by revolving the curve  $y = \sqrt{x^2 + 5x}$  between x = 1 and x = 2.

\*\*\*\*\*\*\*\*

# Government of Karnataka Department of Collegiate and Technical Education Board of Technical Examinations, Bangalore

Course Code	20EG01P	Semester	I/II
Course Title	COMMUNICATION SKILLS	Course Group	Core
No. of Credits	4	Type of Course	Tutorial + Practice
Course Category	Workplace Skills / Humanities & Social	Total Contact Hours	6Hrs Per Week
	Sciences		78Hrs Per Semester
Prerequisites	Nil	Teaching Scheme	(L:T:P)= 0:1:2
CIE Marks	60	SEE Marks	40

### **Preamble**

Today, Communication is a very important skill for the success of every millennial student. Millennials affinity to use digital media for communication, changing career and working landscapes, and greater competition in colleges and workplaces makes enhancing student communication skills beyond language a must. Rote learning a few tips or tricks the night before an interview or performance review won't do the job if students are trying to make an impression in highly collaborative workplaces of the future. Expectations from students aspiring to be part of such future workplaces are that they have not just good verbal and non-verbal communication skills but also a good understanding of how to use modern tools for effective communication.

### Scope

To enable students to communicate clearly and effectively, by improving their verbal and non-verbal communication skills, as well as enhancing interpersonal skills and knowledge of appropriate tools for specific communication strategies.

### **Course Objectives**

The objectives of communication skills course are:

- Build better communication skills: oral and written expressions and body language
- Enable critical thinking
- Empower with active listening skills
- Enable team work/collaboration

### **Instructional Strategy**

To achieve course objectives, it is important to provide the blended mode of instruction for each of the concepts. This blended mode of instruction enables and empowers students with:

- Understanding of Concept (Theory):
  - o Through definitions, discussions, explanation, conclusions.
  - Through demonstrations: Show films or other workplace clips that model various conversation skills. This provides greater clarity of the concept by

- **Enabling observation skills**
- Helping in expression of gesture
- building confidence
- **Application of Concept (Learning by doing):** It is imperative that to become a good communicator, the skills have to be built by applying the concept in the hypothetically created real life situations. Students are encouraged to participate in each of these activities during lab session to help build the effective communication skills.
  - Use of technology tools like audio books, apps like voice thread or paper telephone, etc.
    - To help in workplace conversions.
    - To increase active listening, pronunciation
    - To help in voice modulation
  - Group discussion
    - Reinforce active listening
    - Enable group debate to imbibe healthy communication strategies
    - Sharpen the skills of "Asking clarifying questions"
    - Sharpen Feedback / Response skills
    - Time management skills
  - Group presentations/peer reviews
    - Enable team work
    - Assess concept understanding
    - Sharpen both oral and written communication skills
  - Group activities:
    - foster critical thinking
    - enable reflective learning
  - Tools usage:
    - Understand the difference between a Dictionary and a Thesaurus
    - Understand "When" and "How" to use these tools for communication

### **Course Outcomes**

After completion of this course, the student shall be able to:

- Communicate
  - Identify audience (colleagues, management, customers/vendors) and use the right methodologies for communication using the right terminology, names, grades and other nomenclature pertaining to the trade, tools and specific equipment.
- Write
  - o in at least one language correctly
  - basic level notes and observations
  - o job cards, work sheets, basic report writing and responding to emails, simple presentations, job applications, resume
- Read
  - Technical manuals, task sheets/job orders, policies and regulations pertinent to the job, including OEM guidelines.
  - o all instructions given in memos, manuals, documents or those put up as posters across the premises
  - safety precautions mentioned in equipment manuals and panels to understand the potential risks associated
- Question
  - Ask right questions
  - Use different ways of asking questions
    - Clarifying/Open ended (What, Why, When, Who, Where, How)
    - Close ended

- Present
  - With right Posture & Gesture
  - With greater concept/content clarity
  - With high confidence
  - o With voice modulation to capture the attention of audience
- Use technology tools
  - Office productivity
    - Word : Report writing
    - PowerPoint : Creating effective presentations
    - Excel: Data handling/Charts

### **Course Content**

The following are the various units to be taught and assessed in order to ensure the student is able to demonstrate the Course Outcomes mentioned in the **Course Outcome** section.

### **Pre-assessment:**

Teachers are required to administer pre-assessment before starting the actual instruction. This helps in gathering information about students' like their attitude, beliefs, interests, and learning abilities.

### **Pre assessment expectations:**

- To assess current language skill (Pronunciation, usage, sentence formation)
- To assess their ability to comprehend and respond to the instruction
- To assess their interest towards accepting ideas and learning
- To assess their current communication skills: asking questions, listening, communicating with confidence

# UNIT 1: English - Introduction Learning outcome:

Learn English pronunciation, functional grammar concepts& Reading. To gain confidence in spoken English. This section also covers phonemic awareness, grammar rules to set a strong base for application mode of communication.

Phonemic awareness	Going over 42 sounds	Examining the understanding of sounds Spelling patterns (Consonant and Vowel blending: CVC words) Pronunciation    List of words given above (Commonly used words)   Diction (speech)	0:2:2
Functional Grammar Concepts	Revision of Grammar concepts	Parts of speech Sentence structure Examples of right sentences Gender, Singular, Plural Usage of voice (active and passive) and tenses	2:0:0 0:1:0 0:1:0 0:2:0
Comprehens ion activities	Reading conversations (check the unitwise activity table)	Written test for each comprehension	0:0:2

#### **UNIT 2: Communication**

#### **Lesson outcome:**

At the end of the session:

- Students should be able to
  - Understand the communication process, influence of voice/tone, logical organization of thought, comprehension, listening skills.
  - Understand the basic building blocks of communication and strategies for working with each of these blocks.
  - o Learn about carrying self, etiquettes of communication.
  - o Build positive attitude about self and towards handling communication.
  - $\circ\quad$  Learn the process for effective communication, problem solving techniques, to be confident communicator.

	What is		1: 2:0
	communication?		
	Why communication?		
INTRODUCTI	How do we		
ON:	communicate?		
	Communication		
	Theory and Process	How communication happens?	0:2:2
		<ul> <li>Pictorial representation of</li> </ul>	
		communication framework	

Barriers to communication	<ul> <li>Elements of communication: sender, receiver, message</li> <li>Refer to activity in Unit activity section.</li> <li>Language         <ul> <li>Lack of linguistic ability</li> <li>Grammar</li> </ul> </li> <li>Context         <ul> <li>Psychology</li> <li>Physiology</li> </ul> </li> <li>Systematic</li> </ul>	0:2:2 (video clip play, content tutorial, role play)
	<ul> <li>inefficient or inappropriate information systems</li> <li>Lack of communication channel</li> <li>lack of understanding of the roles and responsibilities</li> <li>Attitude</li> <li>Perceptions</li> <li>Preconceived notions</li> </ul>	

Building	People	People:	0:4:4
blocks of	Message	<ul> <li>Empathising with sender's or</li> </ul>	
communicati	Context	receiver's perception	
on	Listening	<ul> <li>Intent &amp; Impact on the</li> </ul>	
		sender/receiver	
		<ul> <li>Think – Feel – Do model</li> </ul>	
		Message:	
		Message channels:	
		<ul> <li>Inperson, email, memo, report</li> </ul>	
		Be aware of Mental Filters	
		o Level of	
		understanding/knowledge	
		<ul> <li>Personal concerns</li> </ul>	
		<ul> <li>Pre conceived notions</li> </ul>	
		Organize message:	
		<ul><li>Critical thinking: organize your</li></ul>	
		thoughts?	
		Use following strategy:	
		• Who	
		■ What	
		■ When	
		■ Why	
		■ How	
		<ul> <li>Bundle Primary and Secondary information</li> </ul>	
		36: 16:1 1	
		message  o Tone of voice	
		Examples of Types of messages:	
		D 1	
		<ul><li>Persuade</li><li>Cyclical</li></ul>	
		Avoiding Miscommunication:	
		_	
		Evaluate (Checking for)      And a system displayed the intent of the system.	
		understanding of the intent of the	
		message with the receiver – by	
		asking clarifying questions?	
		Context:	
		Define context	
		Importance of context	
		Tune into context	
		• Timing	
		• Location	
		<ul> <li>Relationship</li> </ul>	

Listening:
Importance of listening
Barrier to listening:
Mental filters
Multitasking
Information overload
Strategies for listening:
Recall
Acknowledge
Summarize
Listen with eyes for connecting to
non-verbal connection
Empathize
Pay attention
Ask clarifying questions
Effective Listening Behaviors:
Maintaining relaxed body posture
Leaning slightly forward if sitting
Facing person squarely at eye level
Maintaining an open posture
Maintaining appropriate distance
Offering simple acknowledgements
Reflecting meaning (paraphrase)
Reflecting emotions
Using eye contact
Providing non-distracting environment
Behaviors that hinder effective listening
Acting distracted
Autobiographical (Telling your own
story without acknowledging theirs first)
• No response
Invalidating response, put downs
• Interrupting
• Criticizing
• Judging
Giving advice/solutions
Changing the subject  Proposition of the sector also and a decount.
Reassuring without acknowledgment

#### **UNIT 3: Verbal Communication**

#### **Lesson outcome:**

At the end of this session, Students should be able to:

- Understand and define the communication framework structure for each of the verbal communication(in person/telephonic/video conference).
- Understand and apply the verbal communication techniques.
- Use technical jargons in communication.
- Use right body language during verbal communication
- Understand and practice the Active Listening techniques
- Confidently articulate or present the content

Different types	In person	Use ABC's : Accuracy, Brevity, Clarity	0:2:4
of verbal	in person	o Introduction	0.2.1
communication:	Telephonic	o Main body of the	
		content	
	Video conference	o Summary	
	The conjection	Use voice/tone effectively	
		Reinforcement of Listening	
		skills: Active and Empathetic	
		listening skills	
		Body language	
		Eye contact     Redy posture	
		<ul><li>Body posture</li><li>Gesture</li></ul>	
		o Facial expression	
Listening Skills	Effective Listening	Space  Effective Listening Behaviours	
Listening Skins	behaviors	Effective Listening Behaviours:	
	Dellaviors	Maintaining relaxed body posture  Leaving a lightly forward if sixting.	
		• Leaning slightly forward if sitting	
		Facing person squarely at eye level	
		Maintaining an open posture	
		Maintaining appropriate distance	
		Offering simple acknowledgements  Poffering simple acknowledgements  Offering simple acknowledgements	
		Reflecting meaning (paraphrase)  Pediatria and the second of the se	
		Reflecting emotions  Hair a second of the second of t	
		• Using eye contact	
		Providing non-distracting	
		environment	
	Behaviours that	Behaviours that hinder effective	
	hinder effective		
	listening	listening	
	nstening	Acting distracted     Autobiographical (Talling your own	
		Autobiographical (Telling your own     start with out a glypoul adding theirs	
		story without acknowledging theirs	
		first)	
		No response     Invalidating response put downs	
		<ul><li>Invalidating response, put downs</li><li>Interrupting</li></ul>	
		• Criticizing	
		<ul><li> Judging</li><li> Giving advice/solutions</li></ul>	
		Changing the subject	
Using technical	Assignment based	Reassuring without acknowledgment	
Jargons:	project encouraging		
Jai Boiisi	pupil to use the		
	technical terms in		

t	the written and	
ı	verbal	
0	communication.	
7	This requires	
ι	understanding of	
t	the core concepts	
(	from subject	
t	eacher) and	
i	ntegrating the	
	concept with	
	communication	
0	concepts to gain the	
r	real time application	
l.	knowledge.	

#### **UNIT4: Non-Verbal Communication:**

#### Lesson outcome:

At the end of this unit, students should be able to:

- Understand the importance of Body language and its impact.
- Use the strategies for effective body language.
- Understand the relevance of different elements of emails and how to use them.
- Develop the confidence in presenting written content in logical and organized manner with a definitive email framework.
- Write different email formats confidently: Job application, Request email, apology email, email responses/feedback.
- Confidently write Resume/Curriculum-vitae, Reports, Formal letters and portfolio.
- Confidently communicate using technical jargons and with increased vocabulary.

Body		Body language tips:	0:3:4
Language	Strategies	Keep appropriate distance	
0 0	o o	Take care of your appearance	
		Maintain eye contact	
		Smile genuinely	
		Do's and Don'ts:	
		dos:	
		• smile	
		stand up confident and straight	
		use appropriate hand gestures	
		Make eye contact with audience	
		Hold neat note cards while	
		presenting content	
		Don'ts	
		point at anyone	
		<ul> <li>point at anyone</li> <li>rock backwards and forwards</li> </ul>	
		<ul> <li>pace across front of room</li> </ul>	
		<ul> <li>pace across from or room</li> <li>read off slides</li> </ul>	
		read off notes	
		read off flotes	
		Different types of emails: Job application,	
		request letter, letter writing and quick notes	0:2:4
		request letter, letter writing and quick notes	
		Structure of email text:	
		<ul> <li>Introduction – Beginning of the</li> </ul>	
		letter and this plays crucial role as it	
		provides first impression to the	
	Myitton	reader.	
Art of	Written	<ul><li>Who: author (name +</li></ul>	
Professiona	communication	position and organisation)	
l writing:		o what: purpose - controlling	
i wiiting.	Emails:	idea (what author does or	
	Linuis.	feels)	
	Structured	<ul> <li>Development: Expand on the</li> </ul>	
	framework for	Controlling Idea/purpose of the	
	writing formal	email by answering relevant WH	
	emails to	questions	
	emphasize on	o what, when, where, who,	
	professional	whom, which, whose, why,	
	communication	and how	
	in English	Conclusion: Positive words	
		o Verb: thank, appreciate,	
		hope, wish	

<ul> <li>Phrases: be glad about, look forward to</li> </ul>	
Email writing samples and practice content in the activity section.	
Additional essential writing skills – Framework will be provided and assignments will be advised:	
<ul> <li>Resume writing /CurriculumVitae</li> <li>Report Writing</li> <li>Portfolio writing</li> <li>Formal letters</li> </ul>	

# UNIT5: English - Reading Skills, Grammar & Vocabulary Lesson Outcome:

At the end of the session, student should be able to:

- Read sentences with punctuation.
- Understand the techniques of reading complex words.
- Understand and apply the reading techniques for efficient reading.
- Understand the usage of communication tools like Thesaurus and Dictionary that aids in improving vocabulary and reading.
- Understand and apply the functional grammar aspects in day today communication.

(	Comprehension activities	Passage comprehension	
		Conversation comprehension	0:2: 2
1	Techniques for smart		
r	reading		
		Strategies for smart reading:	
		<ul> <li>Skimming and scanning through</li> </ul>	
		the text, inferring the meaning	
		<ul> <li>Questioning, summarizing</li> </ul>	
	List of Commonly	Set of words to accelerate the English	0:1: 2
	confused words and how	language learning and usage.	
t	o use/avoid them	Strategies to use these words effectively	
	Sentences:		
	entences.		
	<ul> <li>Declarative</li> </ul>	Techniques of categorizing sentences	0.1.2
	o Declarative sentence	Techniques of categorizing sentences,	0:1: 2
	sentence	understanding how to build with	0:1: 2
			0:1: 2

Reading	o Interrogati	This involves more of hands on	
skills	ve sentence	activities.	
	<ul><li>Exclamator</li><li>y sentence</li></ul>		
	y sentence		
Functional			0:1: 2
Grammar	Punctuation, Content	Comprehension remains as a main	
	organization and Comprehension	activity to accelerate the learning of spoken and written English language	
	comprehension	spoken and written English language	
	Techniques:	Increases vocabulary, builds confidence	
	• Learning new	and helps in becoming a good communicator.	
	words from comprehension by	communicator.	
	way of repetition	Activities are done, tips are provided to	
	and usage of these	efficiently implement these strategies.	
	words in		
Vocabulary	communication		
	Listing technical		
	jargons and		
	repeatedly using in the communication		
	with peers and		
	teachers		
	<ul> <li>Chunking and</li> </ul>		
	reading words		
	Tools		0:1:0
	• Understand the		
	difference between a Dictionary and a		
	Thesaurus		
	<ul> <li>Understand</li> </ul>		
	"When" and "How"		
	to use these tools		
	for communication		

#### **Unit 6 - Communication Tools**

#### **Lesson Outcome:**

At the end of the session, student should be able to:

- Use Email technology efficiently for communication
- Present content in the PPT format efficiently
- Understand different platforms available for web conferencing and efficiently work with them.
- Create reports and data management.

	Evolution of	Traditional vs. modern communication tools	1:0:0
Introduction	communication tools	Advantages and Disadvantages	
	Email using Gmail	How to use the tools effectively?	0:1:1
		Formatting, layout	
One-to-One		Including attachment	
		Working with "To, CC, BCC" and Subject	
		fields effectively	
		Using signature	
	Presentation using	Creating, Editing, Saving slides	0:1:2
One-to-	PowerPoint	Using Animation	
Many		Formatting options	
	Webinar / Web	Hosting online meeting using online	0:1:1
	Presentation (zoom,	meeting tools	
	Google meet, Skype)	Inviting people	
		Sharing screen	
Other	Reports using MS	Open, close, Edit and Save usage with	0:1: 2
	Word	documents	
		Layout and strategies for creating report	
		Sample report creation demo with follow on	
		assignment	
		Core subject project report submission	
		assignment	
	Data & Graphs using	Open, close, save and edit the excel	0:1: 2
	MS Excel	document	
		Creating data	
		Using basic maths operation in Excel for	
		working with data	
		Creating simple graphs	
		Assignment: For example, creating statistics	
		of subject wise activities completed for 6	
		months in the credit course	
			4:34:40

# **Course Class Activity List (Unit-wise)**

The following are the various activities that faculty could conduct for each unit are presented below;

Unit No.	Unit Title	Unit Activities
UNIT 1: Activities:	English – Introduction	1. 42 sounds revision:

- 1. s, a, t, i, p, n
- 2. ck, e, h, r, m, d
- 3. g, o, u, l, f, b
- 4. ai, j, oa, ie, ee, or
- 5. z, w, ng, v, oo, oo
- 6. y, x, ch, sh, th, th
- 7. qu, ou, oi, ue, er, ar
- This helps in reducing the native language impact
- Helps in understanding Short and Long vowel words
- Helps in spelling
- Helps in pronunciation
- 2. Reading commonly used words loud from the list (list will be provided in the workbook):
  - This helps in getting familiarity with the word pronunciation and helps in reading.
- 3. Blending words activity:
  - Write simple three letter words (CVC/CVCC/CVCV)
     pattern words: Can, Cap, Snap, cape (list will be
     provided in the workbook)
  - Show how to blend with the sound.
  - Starting with 3 letter words and continuing to 6 to 8 letter words. *Note: Remember before going through big words, it is always important to assess and ensure the student is aware of all the 42 sounds and are comfortable making small words.*

#### Parts of Speech:

building sentence using parts of speech: Demonstration by teacher: (Will be explained in the book as an example)

Jumbled parts of speech: Student should pick the right order to build meaningful sentence:

(More samples will be provided in the workbook)

- College go to youeveryday.
- Makes spider web the a

Gender, Singular and Plurals:

- Match the following activity for singular and plural
- Fill in the blanks activity for genders

participant's ear, and this message has to be passed on in a circle until it reaches back the sender. Making a not of process of message conveyed and how it was perceived.			Reading & Comprehension: Conversation
Unit 2  Communication  Oral:  Introduce yourself?  Visual:  Video clip on communication etiquette  Pictures (in addendum section): do's and don'ts of communication  Group of students, one participant whispers in another participant's ear, and this message has to be passed on in a circle until it reaches back the sender. Making a not of process of message conveyed and how it was perceived.			workbook along with few more conversation samples)  • Questions based on this conversation will be
<ul> <li>Discuss and conclude the communication framework importance</li> <li>Discuss/reiterate how to make communication framework strong.</li> <li>Role play to assess the understanding of building blocks of communication: (can be tapered to the core skills of diploma courses, following are just few of the examples)         <ul> <li>Announcing the result of students in the class or</li> <li>Announcing the job placement of students</li> </ul> </li> </ul>	Unit 2	Communication	provided in the workbook  Oral:  Introduce yourself?  Visual:  Video clip on communication etiquette  Pictures (in addendum section): do's and don'ts of communication  Group of students, one participant whispers in another participant's ear, and this message has to be passed on in a circle until it reaches back the sender. Making a note of process of message conveyed and how it was perceived.  Identify the communication gap if any.  Discuss and conclude the communication framework importance  Discuss/reiterate how to make communication framework strong.  Role play to assess the understanding of building blocks of communication: (can be tapered to the core skills of diploma courses, following are just few of the examples)  a. Announcing the result of students in the class or  b. Announcing the job placement of students (people, context, message, form of message)  c. Discussing the guidelines of examination (listening skills)  d. Listening to the weather forecast without seeing and making note of the listening ability (play video of weather forecast) – Assess based on how much the student is

		<ul> <li>2. Run National geography/Discovery Video clip/subject related technical video clip on YouTube: Check:         <ul> <li>if the student has not understood what a speaker expressed</li> <li>about work or safety related issues</li> <li>seeking clarification or advice appropriately from colleague, customer, management or vendor</li> </ul> </li> </ul>
Unit 3	Verbal communication	1. Voice/tone modulation: Showcase video Discussion:  What was right?  What was wrong?  How it should have been better?
		2. Picture description activity (memory test): Class split into groups A, B C,D: (two or four groups of at least 5 people each): Teacher shows different picture to each group for three minutes. Now each group has to remember what was on the picture and discuss with each other, write down the elements on a piece of sheet and share it with the teacher. Group that remembers more will be the winner.
		Teacher to observe the body language of a student in the group, listening skills of a student, presentation skill, comprehension skill, content delivery skill, confidence level, team work. And reiterate the concepts, dos and don'ts, and discuss what could have been done better.  (details of pictures will be given in the workbook)
		3. Telephonic conversation: Role play by a teacher: Call Airtel/Vodafone department and asking for the phone number portability process.
		After teacher demonstrates, teacher divides the class in to small groups of three people.  • Each group will be given a different telephone conversation assignment (samples will be provided in workbook).

		Two people in the group pretend to converse over the phone, and the third person makes a note of right and wrong approaches during the communication.
Unit 4:	Non-verbal communication	Body language
		Simon Says:
		Instructions and set up :
		1. Series of instructions to the group that are to be copied/reproduced. Start slowly and increase the pace
		2. State the following actions as YOU do them:
		<ul> <li>Put your hand to your nose</li> <li>Clap your hands</li> <li>Stand up</li> <li>Turn around</li> <li>Touch your shoulder</li> <li>Sit down</li> <li>Stamp your foot</li> <li>Cross your arms</li> <li>Put your hand to your forehead - <u>BUT WHILE SAYING THIS PUT YOUR HAND TO YOUR NOSE</u></li> <li>Observe the number of group members who copy what you did rather than what you said.</li> <li>Outcome of this activity:</li> <li>Discuss how body language can reinforce/influence verbal</li> </ul>
		communication and drive the importance of body language and how to work on it  • Email communication & Using technical jargons:
		Sample letter writing as assignment to students. (list will be provided in the text book – Request, apology, job application and relevant email formats that are useful for students post diploma course)
		There will be at least one assignment that utilizes technical jargons in email communication.

UNIT 5:	English - Reading Skills, Grammar & Vocabulary	<ul> <li>Reading passage (Provided in workbook)</li> <li>Reading passage from the text book</li> <li>Comprehension: Passage &amp; Conversation (will be provided in workbook)</li> <li>Chunking words and reading activities</li> </ul>
Unit 6:	Communication tools	<ul> <li>Email writing activities: Writing emails using email provider. Theme based email writing</li> <li>Report writing assignment</li> </ul>
		<ul> <li>Writing about a machinery tool/interior designing plan? Related to the diploma stream.</li> <li>Resume writing assignment</li> <li>Data handling: Collecting data about machines/number of students passed out of college for last three years and creating graph about it.</li> <li>Presentation:         <ul> <li>About learning in the communication class</li> <li>Concept presentation</li> </ul> </li> </ul>

## **Course Assessment Strategies**

## **Assessment Methodology**

- a. Observation (role play activities, team activities, demonstration)
- b. Questions & Answer Periodic Assessment

## **Assessment Grading RUBRICS**

Language Basics	
Beginner	Doesn't know / understand
Intermediate	can read and identify commonly used words
Good	Confident, able to communicate well with known people
Advanced	Confident , able to communicate well with anyone using a English
Expert	Can read, understand; Also comprehend & can train others
Reading	
Beginner	Beginning to read, has native language impact
Intermediate	can read, identify words, build simple 3/4/5 letter words easily
Good	Can read, understand, build words, read simple sentences ; Also
	comprehend
Advanced	Can read, understand, build words, read simple sentences ; Also
	comprehend
Expert	Confident, read simple and complex sentences with punctuation,
	comprehend, spell also build words
Inter personal	
communication	
Beginner	is shy, doesn't talk/express

Intermediate	hesitates to communicate – due to lack of confidence / ability, can talk to known people
Good	can talk to unknown people, less confident, does not express, has hard time working as a team
Advanced	can talk to unknown people, confident, can't express, has hard time working as a team
Expert	confident, can talk to anyone, express well, works well in the team
Body language	
Beginner	Is shy, not open to communicate, has hard time making friends
Intermediate	Knows basics of Body language, practices sometimes
Good	Knows basics of Body language, practices most times, has less confidence in presenting content
Advanced	Knows and practices good body language all times, can present content
Expert	Knows and practices good body language all times, is an example, Leads the pack to get better
Listening Skills	
Beginner	Just hears, no attention
Intermediate	Listens, pays attention, does not ask any question
Good	Listens, pays attention, ask questions
Advanced	listens, pays attention, asks questions, cannot empathize
Expert	Listens, pays attention, asks clarifying questions, able to understand the message communicated
Acceptability to Learn	
Low	is not receiving to information
Average	receives information but resists to implement
Good , Above Average	receives information and implements per instructions
Strong	receives information and proactively implements and seeks feedback
Verbal Communication	
Beginner	Does not communicate, shy, low on confidence: has problem expressing in his/her native language or English language
Intermediate	Can communicate in native language, low confidence, shy, yet to try in English language
Good	Can communicate in native language, good confidence, tries to communicate in English language
Advanced	Can communicate in native language, express view points, good confidence, comfortable talking to people in the team, tries to communicate in English language aswell
Expert	Can communicate in native language, express view points, very good confidence, can communicate with anyone without any fear, asks clarifying questions, communicates well in English, or tries hard to communicate in English language as well
Non-Verbal	
Communication	
Beginner	Struggles to understand the non-verbal cues, has to work on body language, has hard time understanding the written communication
	aspects

Intermediate	Can understand the non-verbal cues, has to practice, tries to apply written communication aspects				
Good	Can understand non-verbal cues, practices well, works hard to get hold on written communication skills, exhibits confidence in whatever task is given				
Advanced	Can understand non-verbal cues, can work on written communication aspects, exhibits confidence, practices well, help others to identify non-verbal cues				
Expert	Can understand non-verbal cues, train others, confident, exhibits good non-verbal cues at all times, can train the pack, has good hold on written communication as well.				
Comprehension					
Beginner	Tries to read the passage, has hard time to comprehend				
Intermediate Can read the conversation passage, has hard time understream regular passage					
Good	Can read the conversation passage, regular passage, but stutters in answering questions if there are technical jargons				
Advanced	Can read the conversation passage, comprehend but regular passage comprehension is good				
Expert	Can read the conversation passage, comprehend but regular passage comprehension is good, explain better to others, help others, lead the pack				
<b>Writing Communication</b>					
Beginner	Has trouble forming right sentences for written communication				
Intermediate	Can form sentences, has problem with the layout, gets confused between layout for different form of written communication				
Good	Can form sentences, has fair understanding of the layout to be used f particular type of written communication, but stutters for words and expression				
Advanced	Can form sentences, has good understanding of the layout to be used for particular type of written communication, confident, can express thoughts well				
Expert	Can form sentences, has good understanding of the layout to be used for particular type of written communication, confident, can express thoughts well and train others and lead the pack				

#### **Recommended Learning Resources**

https://www.englishclub.com/grammar/parts-of-speech.htm
Watch Amy Cuddy's TED Talk: Your Body Language Shapes Who You Are
Additional Reading: http://money.cnn.com/2000/05/03/career/q body language/

#### **Pre-assessment:**

#### **Activity** 1:

Make a group, read random words from the list, build sentence for few words from the list.

Create a group of 3 or 5 students. Randomly pick 5 words from the word list write down on the board/show them as a chart if you have created a word chart/make chit of words and ask them to pick one chit and READ the word.

**Main idea:** Testing the pronunciation ability, language ability, confidence in speaking, ability to understand and accept the instruction

#### **Activity 2:**

Simple reading test – Reading passages (Simple passage from the current course book)
Show the reading passage, let each one of them read 2 lines, after first student is done with reading two lines, then the next student must pick up from there and read next two lines. This process has to be followed until the entire class is done with reading or at least ten students are done with reading.

Main idea: Testing listening skills, attentiveness, language ability, pronunciation ability

#### **Activity 3:**

Students getting to know each other. Create a group of 3 or 5 students. Each student gets chance to talk to another student, introduce him/herself to the student, ask question, make a note of the answer against the name of the student who is answering the question on a sheet of paper.

**Main idea:** To assess current communication level, body language when students talk with each other, and confidence.

Comm	only Used Wo	rd List			Yes	То	Girl	This	
When	Today	For	Off		On	Am	Α	Could	
Give	Stop	There	Often		Been	Where	You	Now	
Again	Little	Than	Myself		Of	Way	Be	Fun	
Do	Large	At	Over		Не	Which	Were	Only	
From	Both	Like	Along		It	Write	Or	Much	
Him	Name	Said	Why		More	Goes	One	Tell	
Can	Few	They	Has		My	Great	All	Out	
Go	Home	Look	Bring		Any	Number		That	Fast
But	Big	Know	Part		Their	First	Cat	Is	
Old	Should	Done	By		We	Find	His	Small	
Not	Once	High	As		She	Me	Have	Dog	
Her	Thought		So	Into	Did	In	How	See	
Time	Better	Them	Away		Went	Before	Water	Here	
Long	Many	Does	No		Full	Saw	And	People	
Had	Get	Always	s Other		Some	Never	Use	School	
Word	Please	These	With		Then	Boy	Take	Two	
Very	Ask	Last	An		If	Right	The	Call	
Your	Say	Got	What		Night	After	Will	Might	
Make	Ten	Next	Come		Made	About	Was	May	
Day	I	Those	Would		Up	Far	Are	Walk	
Each	Show	Play	Who						

To assess current communication skill: Activity based

#### **Activity 3:**

Making a group of students and getting to know each other with a predefined expectation for example:

Name:

I have performed on stage:

I'm good at sports:

I can speak more than 3 languages:

I'm always cheerful:

I like my mother tongue:

# **Course Assessment and Evaluation**

# **Continuous Internal Evaluation (CIE)**

Sl.No	Assessment	Schedule	Duration	Max. Test marks
1	Skill Test 1	At the end of 3 <sup>rd</sup> week of the sem	2 Hrs	20
2	Skill Test 2	At the end of 7th week of the sem	2 Hrs	20
3	Skill Test 3	At the end of 13th week of the sem	2 Hrs	20
			Total	60

# **Scheme of Valuation for CIE**

Serial no	Assessment	Marks
1	Portfolio Evaluation of activities / exercises conducted upto the schedule of Skill Test. (Work Book Based)	10
2	Assessment of any one through qualitative assessment (Rubrics)	10
	TOTAL	20

RUBRICS FOR ASSESSMENT OF ACTIVITY (10marks) (Qualitative Assessment)						
Dimension	Dimension Beginner Intermediate Good Advanced Expert					
	2	4	6	8	10	Score
	Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	
	Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	
	Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	
	Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	
Average / Total Marks:						

	Example Only					
Faculty need	RUBRICS d to develop ap		MENT OF ACT brics as per th			ssment
Dimension	Beginner	Intermediate	Good	Advanced	Expert	Student
	2	4	6	8	10	Score
Language Basics	Doesn't know / understand	Can read and identify commonly used words	Confident, able to communicate well with known people	Confident, able to communicate well with anyone using a English	Can read, understand; Also comprehend & can train others	8
Reading	Beginning to read, has native language impact	Can read, identify words, build simple 3/4/5 letter words easily	Can read, understand, build words, read simple sentences; Also comprehend	Can read, understand, build words, read simple sentences; Also comprehend	Confident, read simple and complex sentences with punctuation, comprehend, spell also build words	6
Inter personal communication	Is shy, doesn't talk/express	Hesitates to communicate - due to lack of confidence / ability, can talk to known people	Can talk to unknown people, less confident, does not express, has hard time working as a team	Can talk to unknown people, confident, can't express, has hard time working as a team	Confident, can talk to anyone, express well, works well in the team	8
Body language	Is shy, not open to communicate, has hard time making friends	Knows basics of Body language, practices sometimes	Knows basics of Body language, practices most times, has less confidence in presenting content	Knows and practices good body language all times, can present content	Knows and practices good body language all times, is an example, Leads the pack to get better	8
			Average / 7	Total Marks: (	8+6+8+8)/4	7.5 = 8 marks

# Scheme of Valuation for Semester End Examination (SEE)

Serial no	Assessment	Evidence	Marks	Conversion
1	Portfolio Evaluation- UNIT 1: English - Introduction	Work Book	15	
2	Portfolio Evaluation- UNIT 2: Communication	Work Book	15	
3	Portfolio Evaluation- UNIT 3: Verbal Communication	Work Book	15	
4	Portfolio Evaluation- UNIT-4: Non-Verbal Communication:	Work Book	15	40 Marks
5	Portfolio Evaluation- UNIT-5: English - Reading Skills, Grammar & Vocabulary	Work Book	15	
6	Any one activity through communication tools- By qualitative assessment (Rubrics).	BTE Answer scripts	25	
		TOTAL	100	

#### **Government of Karnataka**

### **Department of Collegiate and Technical Education**

#### **Board of Technical Examinations, Bangalore**

Course Code	20CS01P	Semester	I/II
Course Title	IT SKILLS	Course Group	ES/CS
No. of Credits	4	Type of Course	Lecture + Practice
Course Category	ES	Total Contact Hours	6Hrs Per Week
			78Hrs Per Semester
Prerequisites	Basic Computer Skills	Teaching Scheme	(L:T:P)= 1:0:2
CIE Marks	60	SEE Marks	40

#### 1. RATIONALE

Information Technology is crucial to the majority of the business and has a great influence on innovation and engineering. Every branch of engineering and every organization opt for computers and IT skills for business automation, communication/connectivity, resource planning, work automation and securing information etc. All engineering diploma students must be conversant with the basic IT skills which empower them to learn new technologies, adapt to changes, business development, communication etc.

#### 2. COURSE SKILL SET

The aim of the course is to help the student to attain the following industry identified competency through various teaching –learning experiences.

Perform jobs related to web design and maintenance, business process automation tool management, cyber security and safety and program assistant.

#### 3. COURSE OBJECTIVES

- 1. Demonstrate the basics of coding.
- 2. Design and develop web pages that include static and dynamic content.
- 3. Describe the basic concepts of Cloud and IoT.
- 4. Express the workflow and business automation
- 5. Recognize the best practices of Cyber Safety and security.

# 4. JOB ROLE

SL.NO	LEVEL	JOB ROLES
1	3	Junior software developer - web.
2	3	Junior Creative Designer/Digital Artist

## **5. PREREQUISITES**

STUDENT  Basic Computer skills (Students without basic computer skills sh basic skills)		Basic Computer skills (Students without basic computer skills should be taught basic skills)
	TEACHER	Computer science faculty with required knowledge of IT Skills.

#### **6. COURSE OUT COMES**

On successful completion of the course, the students will be able to demonstrate industry oriented Cos associated with the above mentioned competency:

	COURSE OUTCOME	UNIT	CL	LINKED	TEACHI NG HOURS
CO1	Illustrate the basics of coding and develop simple applications for android phones.	1	U, A	1,4,7	15
CO2	Design and Develop websites.	2	U, A	1,4,7	30
CO3	Identify Cloud Services IoT applications	3	U	1,4,7	12
CO4	Apply workflow and use ERP for a simple project plan	4	U	1,4,7	09
CO5	Implement best practices of cyber safety and security in the workplace.	5	U, A	1,4,7	12
	TOTAL				78

#### 8. INSTRUCTIONAL STRATERGY

# These are sample strategies, which teacher can use to accelerate the attainment of the various course outcomes

- 1. Lecturer method(L) does not mean only traditional lecture method, but different type of teaching method and media visual/graphical content that are employed to develop the outcomes
- 2. Massive Open on-line courses (MOOCS) can be used to teach various topics/sub topics.
- 3. Online coding platform wherever mentioned.
- 4. Hands on coding should be practiced.
- 5. About 15 to 20% of the topics/sub topics which are relatively simpler or descriptive in nature is to be given to the students for self-directed learning

#### 9. DETAILS OF COURSE CONTENT

The following topics/sub topics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets

UNIT	Topics/Sub topics	Unit skill set/Learning outcomes	Hours
NO		(In cognitive domain)	L-T-P
1	UNIT 1 - INTRODUCTION TO B.	ASICS OF CODING	05-0-10
	1.1 Introduction to computer programming	1. Understand computer	
	1.2 Algorithms –With sufficient examples	programming	
	1.3 Flowcharts – With sufficient examples	2. Create and write Algorithm for	
	1.4 Execute simple programs	programmable problems.	
	Note: Below listed or any other suitable	3. Design Flowchart for	
	online/offline coding platforms should be	programmable problems.	
	used to demonstrate and provide coding	4. Develop simple Android	
	experience to students.	application.	
	a. <a href="https://scratch.mit.edu/">https://scratch.mit.edu/</a>		
	b. <a href="https://studio.code.org/projects">https://studio.code.org/projects</a>		
	Suggested programs are listed in Table 1		

	1.5 Introduction to Application		
	development		
	1.6 Simple android application development (No		
	knowledge of programming language is required).		
	Note:		
	i. The purpose of application development		
	is to ignite and promote programming		
	skills.		
	ii. Application development should be		
	done using any App builder platforms		
	such as		
	iii. MITApp Inventor:		
	https://appinventor.mit.edu/		
	iv. Thunkable: <a href="https://thunkable.com/">https://thunkable.com/</a>		
	v. ibuildapp: <u>https://ibuildapp.com/</u>		
	vi. The student should be introduced to the		
	android application development		
	environment for further research and learninghttps://developer.android.com/		
	1.7 Activity: create a simple Android		
	application (Unique for each student)		
	publish on the learning management		
	system.		
2	UNIT 2 - DESIGN AND DEVE	I OP WER PAGES	10-0-20
2	ONLI 2 - DESIGN AND DEVI	LLOI WEDIAGES	10-0-20
2	2.1 Basic web technologies	1. Understand and examine basic	
	<ul><li>Browser</li></ul>	web technologies	
	<ul><li>Web –Server</li></ul>	2. Creating static web pages	
	<ul><li>Client-Server Model</li></ul>	3. Formatting Webpages with	
	• URL	cascading style sheets (CSS)	
	<ul><li>SEO techniques</li></ul>	4. Creating Dynamic web pages	
	<ul> <li>Domain names and domain name system.</li> </ul>	with JavaScript	
	2.2 Creating Web-pages with HTML5 - Static	5. Creating and launching	
	web pages.	dashboard based personal	
	<ul><li>Introduction, Editors</li></ul>	website.	
	<ul> <li>Tags, Attributes, Elements, Headings</li> </ul>		

- Links, Images, List, Tables, Forms
- Formatting, Layout, Iframes.
- 2.3 Formatting web pages with style sheets (CSS3).
  - Introduction to CSS
  - Inline CSS, Internal CSS, Classes and IDs
  - div, Color, Floating, Positioning
  - Margins, Padding, Borders
  - Fonts, Aligning Text, Styling Links
- 2.4 Creating a web page dynamic using JavaScript.
  - Dynamic web page and Introduction to JS
  - Basic syntax
  - Functions
  - Events

Note: Refer https://www.w3schools.com

- **2.6** Creating dashboards in websites.
- 2.6 Activity: Personal website design and launch with a free platform or Create a Blogging website.
  - Online platforms (Learning and executing)
  - https://www.w3schools.com/
  - https://studio.code.org
  - https://www.khanacademy.org

#### Note:

- 1) The student must be introduced to website development platforms worldpress.com.
- 2) The student must be made familiar with launching websites.

Certification available:

• HTML - W3schools

	CSS - W3schools		
	<ul> <li>JavaScript - W3schools</li> </ul>		
	Januari Programme		
3	UNIT 3 -BUSINESS PROCESS	AUTOMATION/ERP	03:0:06
3	3.1 Introduction to business process automation. 3.2 Organization structure and functions composition-Properties and applications  Structure Types Functional Units  Note: Students should be made familiar with organization, types and components of a big enterprise to make him understand the working of organization keeping him as part of org.  3.3 Workflows Introduction Components Use and use cases  Note: Use free and open-source platform to demonstrate and create workflows.  Example: https://airflow.apache.org/ https://taverna.incubator.apache.org/ https://trello.com/ https://trello.com/ https://www.processmaker.com/  3.4 Enterprise resource planning History Evolution Uses of ERP ERP software tools.  Note: The student should be introduced into	1. Identify and examine the needs of business process automation. 2. Understand Organization structure and functions 3. Create and use workflows 4. Use Enterprise resource planning in workplace.	
	Enterprise resource planning software tools		
	Enterprise resource planning software tools		

	to understand importance of ERP.		
	Examples:		
	https://erpnext.com/		
	■www.bitrix24.com		
	https://www.odoo.com/		
	3.5 Activity:		
	<ul><li>Project plan for summer internship -</li></ul>		
	use open source ERP Software		
	<ul> <li>Identify different components of</li> </ul>		
	nearby organization with recourse		
	plan and workflow design.		
	Identify types of ERP software		
	available with their market share.		
4	UNIT 4 - INTRODUCTION TO CLO	UD AND IOT CONCEPTS	04-0-8
	4.1 Fundamentals of cloud	1. Understand Cloud concepts	
	4.2 Cloud service models	2. Identify and use Cloud services	
	<ul><li>IaaS (Infrastructure-as-a-Service)</li></ul>	•	
	<ul><li>PaaS (Platform-as-a-Service)</li></ul>	3. UnderstandIoT concepts	
	<ul><li>SaaS (Software-as-a-Service)</li></ul>	4. Identify IoT applications	
	4.3 Cloud deployment types		
	■ Public,		
	<ul><li>Private,</li></ul>		
	■ Hybrid		
	<ul><li>Community Cloud</li></ul>		
	4.4 Cloud services:		
	■ Google Drive - file storage and		
	synchronization service developed by Google;		
	■ Google docs- bring your documents to life		
	with smart editing and styling tools to help		
	you easily format text and paragraphs;		
	Google Co-lab (Usage of Jupyter Notebook):		
	Colab notebooks allow you to combine		
	executable code and rich text in a single		
	document, along with images, HTML, LaTeX,		
	and more.		

Google App Engine: Google App Engine is a Platform as a Service and cloud computing platform for developing and hosting web applications in Google-managed data centers. Applications are sandboxed and run across multiple servers.

Note: Above cloud services are not compulsory for all branches; teacher can recommend other cloud service based on need of engineering branch.

- 4.5 Working of IoT and IoT components (Only brief introduction and demonstration through videos)
- 4.6 Explain concept of Internet of Things with examples
  - Smart home
  - Smart city
  - Smart farming

#### Note:

- a. Teacher can also select specific area of work where Things (autonomous computing devices) could be interconnected over TCP/IP to establish IoT.
- b. The students should be introduced to the IoT environment for further research and study.

#### **Example:**

- https://www.raspberrypi.org/
- https://www.arduino.cc/

#### 4.7 Activity:

Create your cloud service account and demonstrate using cloud services.

Identify cloud service provider with respect

to service models and deployment types.		
Identify areas where Internet of Things could		
bring positive changes.		
UNIT 5 - CYBERSECURIT	Y AND SAFETY	4-0-8
5.1 Introduction to Cyber security and cyber safety.  Brief awareness on cyber safety measures  Identification of basic security issues in mobile phones and personal computers  Installation of Antivirus software  Firewall concepts  Browser settings  Importance of privacy and Password policy (Best practices).  5.2 Common threats - Demonstration  Phishing  DoS attack  Man in the middle attack  Eavesdropping  Spamming  5.3 Activity  Identification of basic security issues in computers of your college and fixing the same.  Visit nearby government organization.  Identify basic cybersecurity issues and fixing the same  Demonstrate the importance of cybersecurity, password policy, and cyber safety.	<ol> <li>Identify need for Cyber security and cyber safety</li> <li>Identify basic security issues in mobile phones and personal computers</li> <li>Examine Importance of privacy, Password policy</li> <li>Implement best practices of cyber safety and security in work place</li> </ol>	

### 10. SUGGESTED PRACTICAL SKILL EXERCISES

#### **TABLE-I**

Sl. No.	Practical Out Comes/Practical exercises	Unit No.	PO	СО
1	Write an algorithm for programmable problems	1	1,4,7	1

	Example for Reference:			
	Add/subtract two numbers			
	Find the largest/smallest of 3 numbers			
	<ul> <li>Calculate and print sum of 'N' numbers</li> </ul>			
	Design a flowchart for programmable problems			
	Example for Reference:			
2	Add/subtract two numbers	1	1,4,7	1
	Find the largest/smallest of 3 numbers			
	Calculate and print sum of 'N' numbers			
3	Design and create simple game using MIT-scratch/Code.org	1	1,4,7	1
4	Design and create simple android application (MIT App Inventor)	1	1,4,7	1
-	Design and create webpage for displaying your poem (Title,	2	1,4,7	2
5	header, paragraph, formatting tags)			
	Design and create webpage for your wish list (What you want to			
6	do). Also list challenges and opportunities along with images to	2	1,4,7	2
	present your dreams (List ordered and unordered, Image, table)			
7	Design and create webpage using HTML and CSS about an	2	1,4,7	2
,	awesome animal (Use necessary CSS tags)		1,4,/	
8	Design and create web page for a travel book/recipe book with	2	1,47	2
O	more than 3 pages, table to list places/recipes (iframe, hyperlink)	2		2
	Design and create web page with JavaScript to design a simple	2	1,4,7	2
9	calculator to perform the following operations: sum, product,			
	difference and quotient			
10	Design and create a personal webpage with dashboard	2	1,4,7	2
11	Design and create web page about advantages of business process		1,4,7	2,3
11	automation with respect to your branch of engineering	2,3	1,1,7	2,0
12	Create a workflow for education loan approval in bank/diploma	3	1,4,7	3
12	admission process (Use any tool)	3	1,1,/	
13	Demonstrate ERP with ERPNext Demo for manufacturing, retail	3	1,4,7	3
10	and service sector (Use any other ERP tools)		1,1,/	
	Create user account and demonstrate use of Google drive, Google		4	
14	docs, Google Co-lab (Usage of Jupyter Notebook)	4	1,4,7	4
15	1.1 Demonstrate Internet of Things using with examples	1	1 / 7	1
15	a. Smart home	4	1,4,7	4

	b. Smart city			
	c. Smart farming			
	Note: Teacher can also select specific area of work where Things			
	(autonomous computing devices) could be interconnected over			
	TCP/IP to establish IoT.			
16	Installation of Antivirus software	5	1,4,7	5
17	Demonstration and hands on browser settings	5	1,4,7	5
18	Demonstration and hands on privacy settings and password policy	5	1,4,7	5
	Demonstration of common security threats (using videos)			
19	a. Phishing			
	b. DoS attack	5	1 4 7	5
	c. Man in the middle attack	5	1,4,7	5
	d. Spamming			
	e. Virus			

The suggested practical activities (TABLE-I) in this section are demonstrated for the attainment of the competency. These practical activities can also be used for the student assessment in portfolio mode for awarding CIE marks. The lecturer can enhance the competency level of the students by sketching more practical exercises.

#### **NOTES:**

- 1. It is compulsory to prepare log book/record of exercises. It is also required to get each exercise recorded in logbook, checked and duly dated signed by the teacher
- 2. Student activities are compulsory and are also required to be performed and noted in logbook.
- 3. Student activity is compulsory and part of skill assessment. The activity enable student to explore the course, help student to demonstrate creativity & critical thinking.
- 4. Student activity report is compulsory part to be submitted at the time of practical ESE
- 5. Term work report is compulsory part to be submitted at the time of practical ESE.
- 6. Student activity and student activity reports must be uploaded to Learning management system.
- 7. For CIE, students are to be assessed for Skills/competencies achieved.

#### 11. MAPPING OF CO WITH PO

COURSE	CO'S	PROGRAMME OUTCOMES (PO'S)						
		1	2	3	4	5	6	7
IT SKILLS	CO1	3	0	0	3	0	0	3
	CO2	3	0	0	3	0	0	3

CO3	3	0	0	3	0	0	0
CO4	3	0	0	3	0	0	3
CO5	3	0	0	3	0	0	0

Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0- Not Mapped

# 12 SUGGESTED LEARNING RESOURCES

	BOOKS							
1	The Art of Programming Through Flowcharts & Algorithms, A. B. Chaudhuri, Firewall							
1	Media publication							
2	HTML5 Black Book, by Publishing company Limited. Kogent Learning Solutions Inc.							
3	"World Wide Web design with HTML", Xavier, Tata McGraw-Hill							
4	Internet of Things – A Hands on Approach, By ArshdeepBahga and Vijay Madisetti							
•	Universities Press, ISBN: 9788173719547							
	URL'S							
1	https://scratch.mit.edu							
2	https://studio.code.org							
3	http://ai2.appinventor.mit.edu							
4	https://www.w3schools.com							
5	https://www.tutorialspoint.com/javascript/index.htm							
6	https://www.geeksforgeeks.org/html-tutorials/							
7	Android							
	https://developer.android.com							
8	https://www.khanacademy.org							
	Tools for Web Development							
	a. https://www.wix.com							
9	b. <a href="https://atom.io/">https://atom.io/</a>							
	c. https://www.openelement.com/							
	d. <a href="https://www.layoutit.com">https://www.layoutit.com</a>							

## 13. SUGGESTED LIST OF PROPOSED STUDENTS ACTIVITY

Note: Refer activities mentioned in DETAILS OF COURSE CONTENT table

## 14. COURSE ASSESSMENT AND EVALUATION CHART

SL.N	ASSESSMENT	DURATIO	MAX	CONVERSION
0		N	MARKS	
		(in		
		minutes)		
1	CIE Assessment 1 ( Written Test -1 TH) -	60	20	Average of
	At the end of 3 <sup>d</sup> week			two written
2	CIE Assessment 2 (Written Test -2 TH) -	60	20	tests
	At the end of 13 week			20
3	CIE Assessment 3 (Skill Test) - At the end of	3 hrs	20	Average of
	5 week			three skill test
4	CIE Assessment 4 (Skill Test) - At the	3 hrs	20	20
	end of 7 week			
5	CIE Assessment 5 (Skill Test) - At the end of	3 hrs	20	
	9 week			
6	CIE Assessment 6 (Student activity)- At the	-	20	20
	end of 11 week			
7	Total Continuous Internal Evaluation	n (CIE) Assess	sment	60
8	Semester End Examination(SEE)	3 hrs	100	40
	Assessment (Practical Test)			
	TOAL MARKS			100

Note: CIE written test is conducted for 20 marks (Two sections). Each section shall have two full questions of same CL, CO. Student shall answer one full question from each section.

## 15. RUBRICS FOR ACTIVITY

	RUBRICS FOR ACTIVITY (Example Only)						
	Appropriate rubrics shall be developed by the concerned faculty						
Dimensio	Poor	Below	Average	Good	Exemplary	Student	
n		Average				Score	
	4	8	12	16	20		

Concept	Does not collect	Collects very	Collect much	Collects some	Collects a great	8
	any information	limited	information;	basic	deal of	
	relating to the	information;	but very	information;	information; all	
	concept	some relate to	limited relate	most refer to	refer to the	
		the concept	to the concept	the concept	concept	
Design	Design is not	Design is poor	Design	Design &	Design	6
	acceptable/very	and not well	Fallowed	convey both	considered all	
	poorly structured	structured.	layout	content and	aspect of	
			samples and	context	concept,	
			well		concept and	
			structured		presentation	
					(UI)	
Creativity	Very little	Creativity in	Creativity in	Creativity in	Creative	8
	creativity in	concept or	concept	concept	concept,	
	design/impleme	design or	/design/impl	/design/imple	content,	
	ntation	implementatio	ementation	mentation	presentation	
		n		which	and	
				complements	implementation	
				each other		
Impleme	Poorly	Partially	Implemented	Product convey	Product is	8
ntation	implemented	implemented	on time with	both content	creative with	
			results	and context	easy-to-use UI,	
			(content)		structure	
			Ave	erage / Total Mar	ks: (8+6+8+8)/4	7.5 = 8

## 16. RUBRICS for Skill Test Evaluation (Both for CIE & SEE)

Sl No	Parameter to be Observed	Marks
		Allotted
1	Design-Written	
	Skill Test 1: Algorithm / Flowchart/Visual Design	30
	Skill Test 2: Web site visual design	
	Skill Test 3: Work flow or Project plan or cyber security	
	plan or Cloud service Concept	
2	Implementation	50
	Skill Test 1: Android application	
	Skill Test 2: Web site / Web pages	
	Skill Test 3: Create or use cloud service account or	
	Cyber safety and security- Antivirus	
	Installation or browser settings	
3	Appeal and Presentation	20
	Total	100

## **17. SYSTEM REQUIREMENTS:**

Sl. No.	Specification	Quantity
1.	Computers with HD Graphics Card	20
2.	Software: GIMP, KRETA, BLENDER, PHOTOSHOP or any other relevant open-source software.	-
3.	Internet Connectivity	-

Note: Above specification is for a batch of 20 students

#### Government of Karnataka

# **Department of Collegiate and Technical Education**Board of Technical Examinations, Bangalore

Course Code	20EC21P	Semester	II
Course Title	Electronics Components and Devices (ECD)	Course Group	Core
No. of Credits	4	Type of Course	Lecturing & Practice
Course Catagory	EC	Total	6Hrs Per Week
Course Category	EC	Contact Hours	78Hrs Per Semester
Prerequisites	Arithmetic, basic of electronics	Teaching Scheme	(L: T:P) = 1:0:2
CIE Marks	60	SEE Marks	40

#### **RATIONALE**

An electronic circuit is a structure that directs and controls electric current to perform various functions including signal amplification, computation, and data transfer. It comprises several semiconductor devices & components such as resistors, transistors, capacitors, inductors, and diodes. These Semiconductor devices supply themselves in integrating into complex and are readily manufacturable into microelectronic circuits. They also find a good scope in the future in forming key components for the majority of electrical and electronic instruments and systems in various fields such as communications, data-processing, consumer electronics & robots and also in industrial control equipment.

#### **COURSE SKILL SET**

The goal of the course is to help the student to attain the following industry-need competencies through various teaching-learning processes.

- Identifying various components and semiconductor devices used in industrial applications.
- Interpretation of datasheets and usage of instruments.
- Basic knowledge of components, devices and simple applications.
- Perform soldering job, build and test analog electronic circuits for simple applications.
- Testing and experimentation under simulated and real environments.

#### **INSTRUCTIONAL STRATEGY**

- 1. This is theory-practice course with 1:2 time-share. Normally, the Lecturer is expected essentially to teach the relevant theoretical parts in one hour prior to the 2-hour lab session so that at the end the class the student attains the specified class-outcome. This requires well planning from Lecturer side to adhere to the schedules.
- 2. The Lecturer is expected to emphasize only the essential concepts/components/circuit/topics in respect of the practicing experiment in one-hour.
- 3. Normally in industrial environments, experiments are conducted under simulated environments before real-environment experimentation owing to the benefits of simulation. Same procedure is adapted here too with a few exceptions.
- 4. Awareness and safety-precautions on use of instruments/laboratory resources is mandatory for all students prior to the experimentation.
- 5. Lecturers shall advise repetition of experiments wherever possible and necessary.
- 6. Pre-reading of the content and experiment-procedure will have a greater impact on effectiveness and taking forward of this course and hence students shall be advised to do so.
- 7. The intent of the activity is to integrate multiple concepts learnt in the course and to create interest in students to face the integrating-challenge; hence, the Lecturer is advised to assign only such the activities.
- 8. Activity project need not be designed by student, teacher may provide or guide to search; however, it has to be simulated, fabricated and tested by the students.

#### LEVELS OF COURSE SKILL - SETS

Sl. No.	LEVEL	SKILL SET/S To be Attained			
1	Level-1	Identification of components and semiconductor devices.  Testing electronics components and semiconductor devices.  Adaption of the best soldering methods/practices for fabrication of circuits.			
2	Level-2	Experimenting to observe characteristics/behaviour/working of semiconductor devices.			

		Building simple application development using components and semiconductor devices.
3	Level-3	Application, circuit simulation, testing and implementation.

## **COURSE OUT COMES**

On successful completion of the course, the students will be able to

CO1	Identify and select the electronic components and devices and instruments.
CO2	Test electronic components and devices.
СО3	Fabricate/construct discrete circuits.
CO4	Select and analyse electronic circuits for characteristics and/or simple applications.
CO5	Experiment the circuit characteristics/simple applications under simulated and real environment.

## Course Outcomes CO, PO, Cognitive-level and Teaching hours map

Course Outcomes	CL	Linked PO
<b>CO1:</b> Identify and select the electronic components and devices	U/A	1,2,4,5
and instruments.	,	, , ,
CO2: Test electronic components and devices.	U/A	1,4,5
CO3: Fabricate/construct discrete circuits.	U/A	1,4,5
<b>CO4:</b> Select and analyse electronic circuits for characteristics	U/A	1,3,4,5
and/or simple applications.	0/11	1,0,1,0
<b>CO5:</b> Experiment the circuit characteristics/simple applications	U/A	1,3,4,5,7
under simulated and real environment.		1,0,1,0,7

## **COURSE CONTENTS**

The following topics/subtopics is to be taught and assessed in order to develop Unit Skill sets for achieving CO to attain identified skill sets

Sl. No	Content	Experiment	Method/ Class Outcome	L: T:P		
No	UNIT 01: BASICS OF ELECTRONIC COMPONENTS  Note: Demonstrations are hand-on and can be supported by videos/animation wherever possible to see that the students understand the concepts. Real experimentation is after simulation.					
1	Passive components (Resistor, Inductors and capacitors): Introduction, role, symbols, units, types/classification, identification, selection and applications.	Identification and finding/assessing values/tolerances (using color codes, labels) of passive components of different packages.	Demonstration, examples and exercises.  Identify and ascertain roles and values of components.	1:0:2		
2	Usage of electronic instruments: Multi-meter and LCR meter.	Verification of the values of passive components using multimeter and LCR meters (for the same components used in Expt.1)	Demonstration and exercises.  Use meters to assess values and to test components.	1:0:2		
3	Active components(Diode, Zener Diode, BJT): Introduction, list, role/function, symbols, types/packages, and applications	Identification of the active components, terminals, packages, and testing them for working, using multimeter.	Demonstration and exercises.  Identify terminals, packages and test the active components.	1:0:2		
4	Active components (MOSFET, SCR, DIAC, TRIAC): Role/function symbols, types and applications.	Identification of the active components, terminals, packages, and testing them for working, using multimeter.	Demonstration and experiment Identify terminals, packages and test the active components.	1:0:2		

5	Packing, Stocking, Handling of components and their electrostatic safety.  Understand the data/ specification sheets of all components  Selection criterion of components. Soldering types and precautions.	Preparation of components, use of soldering iron and lead and flux. Standard Soldering practice to connect the components on base-board /PCB/assembly board (follow soldering standard).	Videos, demonstration and exercises.  Identify specifications from component data-sheets and perform standard soldering.	1:0:2
6	Atomic structure and energy-band diagram of conductors, insulators and semiconductors-comparison and examples.	Soldering practice (continued) and desoldering.	Videos, demonstration and exercises. Compare the features of conductors, insulators and semiconductors.  Solder and de- solder the components on PCBs/Baseboards.	1:0:2
7	Simulators: Concept, advantages and features. Prominent simulation softwares and their features.	Using simulator/editors: Identification, selection and use of sources, components, devices and instruments. Component specifications and properties, schematic preparation. Component foot-print/3D views.	Demonstration.  Use simulator for experimentation.	1:0:2
	Semiconductor- Covalent	Usage of electronic	Videos,	

			junction. Use CROs, RPS and Sig. generators.	
	UNIT 02: BAS	SICS OF SEMICONDUCTO	R DEVICES	
9	PN Junction Diode: Formation of PN junction. Potential barrier. Depletion region.  Forward and Reverse biasing of diode and VI characteristics.  Diode ratings and parameters.	Plot VI characteristics of Diode and ascertain Ri and cut-in voltage using simulator.	Animation/Video/ Visuals to show the working of PN junction/ diode.  Experiment, graph and calculations.  Know the behavior of PN junction for biasing voltages.	1:0:2
10	Zener diode: Working principle, constructional features, Avalanche and Zener breakdown. Reverse VI characteristics and applications.	Plot reverse VI characteristics of Zener ascertain Zener voltage using simulator.	Animation/Video/ Visuals to show the working of Zener Diode. Experiment and graph. Know the behavior of Zener diode for reverse biasing.	
11	Simple problems on diode circuits/selection of different biasing voltages to illustrate FB and RB of diode.	VI characteristics of diode (expt. 9) in real environment. Plot reverse VI characteristics of Zener diode (expt.10) in real environment.	Experiment and comparison with simulated results.  Compare real and simulated results.	
12	AC-to-DC conversion: Need, rectification, half- wave and full-wave rectification. Half-wave rectifier: waveform, efficiency and ripple factor.	Experiment to obtain half-wave rectification under simulated environment: Obtain waveform, ripple factor, efficiency and PIV.	Experiment, waveforms and calculations.  Apply diode- switch to convert AC to DC.	

13	Bridge Rectifier: efficiency and its ripple factor.  Filter components and their role in reducing ripple.	Experiment to obtain full-wave rectification (using bridge) and filtering under simulated environment: Obtain waveform, ripple factor, efficiency and PIV, without and Full wave rectifier with C-filter	Experiment, waveform and calculations.  Apply diode- switches and filter to convert AC to DC more efficiently.	1:0:2
14	Simple problems on half-wave, full-wave rectifier and filter circuits.	Conduct Expt. 13 under real environment.	Experiment, graph/waveforms and calculations. Compare simulated and real-experiment results.	1:0:2
15	Voltage regulation: Concept, need, Zener diode as voltage regulator. Voltage regulator circuit working. Condition and types of regulation.	Show how Zener diode can be used as voltage regulator in simulated and real environments.	Experiment and interpretation of result.  Use of Zener diode for regulated voltage supply.	1:0:2
16	BJT: Types, construction, symbols, functions of base, emitter and collector, Current gain in CE mode.	Obtain output characteristics of BJT in CE configuration under simulated environment. Calculate input resistance, and mark the different regions on output characteristics and calculate current gain.	Experiment, graph and calculations  Understand the behaviour of BJT	1:0:2
17	BJT: Modes of operation- comparison.BJT packages and specifications. Data sheet interpretation.	Experiments 16 under real environment.	Experiment, graph and calculations.  Compare with simulated results.	

18	Operating regions of BJT, BJT as a switch and amplifier. Thermal runaway and need for heat sink.	Experiment BJT as a switch under simulation environment. (This is linked to experiment 21)	Experiment  Use BJT as a  current- controlled electronic switch	1:0:2
19	MOSFET: Field effect, types, construction, working and applications. MOSFET applications. MOSFET as switch. Comparison of BJT and MOSFET.	Show how MOSFET can be used as a switch under simulated environment.  (This is linked to experiment 21)	Experiment  Use MOSFET as a voltage-controlled switch	1:0:2
20	SCR: Construction, Working, Operating modes, definition of triggering and commutation.	Forward VI characteristics of SCR simulation and real environment.	Experiment  Understand the behavior of controlled switch	1:0:2
	UNIT 03	: OPTO ELECTRONIC DE	VICES	
21	Opto-electronic devices: Introduction, principle of photo emission, photoconduction and photovoltaic effects.  LED: Construction features, role, biasing, packages, and ratings.	Switching to blink an LED using BJT or MOSFET. Experiment under simulated and real environment. This is extension of Expt. 18 and 19)	Experiment.  Use electronic switch and LED in application development.	1:0:2
22	LDR: Construction features, role, package, specifications, and application. Explanation of automatic street-light control circuit.	Conduct a simple experiment to automatically control street-light using LDR and SCR in simulated environment.	Experiment.  Use electronic components and devices to solve simple real-world problem	1:0:2

23	Photodiode and Phototransistor: Construction features, role, specifications, packages, and applications.	Conduct experiment 22 in real environment.	Experiment.  Compare real and simulated results.	1:0:2
	UNIT 04	4: SENSORS AND ACTUA	TORS	
24	Sensors: Principle and classification. List passive sensors, their working principle/role, packages, specifications, and applications. Anyone passive-sensor-based simple application in detail.	Experiment any passive- sensor-based simple application to under simulated environments to illustrate use of sensor for solving simple real- world problem.	Experiment.  Use electronic components and/or devices (sensor) to solve simple real-world problem	1:0:2
25	Active sensors: List active sensors, their working principle/role, packages, specifications, and applications. Anyone active-sensor-based simple application in detail.	Experiment 24 under real environment	Experiment,  Use electronic components and/or devices (sensors) to solve simple real-world problem	1:0:2
26	Actuators: Basic principle/role and types/classification. Roles/applications of different actuators. Simple application involving actuator in detail.	Conduct a simple application involving actuator under simulated and/or real environment.	Experiment,  Use electronic components and/or devices (actuator) to solve simple real-world problem	1:0:2

# Mapping of COs, POs, Cognitive-levels and Teaching Hours

CO Course Outcome	PO Ma nn EX pe	Co gani tiv tiv Le ctu	Tu tor ial	TO TA
-------------------	----------------------------	------------------------	------------	----------

CO1	Identify and select the electronic components and devices and instruments	1,2,4, 5	U1	U/A	8	16	24
CO2	Test electronic components and devices.	1,4,5					
CO3	Fabricate/construct discrete circuits.	1,4,5					
CO4	Select and analyse electronic circuits for characteristics and/or simple applications.	1,3,4, 5	U2, U3	U/A	18	36	54
CO5	Experiment the circuit characteristics/simple applications under simulated and real environment.	1,3,4, 5,7	& U4				
					26	52	78

Course			Pr	ogı		0 0 0 0 0 s		omes
		1 2 3 4 5 6					7	
	CO1	3	1	0	2	1	0	0
	CO2	3	0	0	2	1	0	0
Electronic Components and Devices	CO3	3	0	0	2	1	0	0
	CO4	3	0	1	2	1	0	0
	CO5	3	0	1	2	1	0	2

Legends: Level 3- Highly Mapped, Level 2-Moderately Mapped, Level 1-Low Mapped, Level 0- Not Mapped

### SUGGESTED LEARNING RESOURCES

#### **Reference Books:**

- 6. Principles of Electronics, Rohit Mehta & V K Mehta, S. Chand Publishing ISBN: 9788121924504
- 7. Fundamentals of Electrical and Electronics Engineering, B. L. Theraja, S. Chand and Company. REPRINT 2013, ISBN 8121926602
- 8. Electronic Components, Dr. K. Padmanabhan and P. Swaminathan, Lakshmi Publications, 2006.
- 9. Electronic Devices and Circuits, David A. Bell, Oxford University Press, ISBN:

- 10. Sensors Handbook-SabrieSoloman-McGraw Hill publication, Second Edition.
- 11. Handbook of Modern Sensors: Physics, Designs, and Applications, Jacob Fraden, Springer Publications, Third Edition.

#### E-Resources

- 1. <a href="http://www.electronics-tutorials.ws">http://www.electronics-tutorials.ws</a>
- 2. <a href="http://electrical4u.com/">http://electrical4u.com/</a>

## **Student Activities (CIE 20 marks)**

#### Introduction

Group of 4(maximum) students should conduct **UNIQUE** activity. Suggestive activities are as follow, but not limited to these only, any similar activity can be assigned. Each activity can be carried out off-class hours in the laboratory. Choose the activity for which circuits, boards and components are easily available with the guidance of the teacher, and the activity shall have the scope to integrate multiple concepts. Students are advised plan and start the activity in 7<sup>th</sup> week and complete it by 11<sup>th</sup> week.

## a. Example activities

SL. No.	Activities
1.	Fire detector application.
2.	Intruder detector
3.	LED serial-sets
4.	Clapp/sound detector application
5.	Smoke detector application
6.	LED bulb
7.	And all such simple circuits/projects that have scope to integrate multiple concepts learnt and for which circuits/boards/components are easily available.

#### 10.3 Execution details

- 1.Maximum of 4 students in each batch.
- 2. Write qualitative report not exceeding 8 pages; one report per batch.
- 3. Each of the activity can be carried off-class, and shall be presented to the teacher using suitable presentation mode

4. Assessment shall be made based on information collection, integration of concepts involved, execution (simulation/fabrication/testing/results), report, presentation, and role in the team.

## **10.4** Activity Assessment Scheme

Sl. No.	Parameter to be Observed for Activity	Marks
31. NO.	Assessment	Allotted
1	Information collection	4
2	Level of integration of concepts/creativity	4
3	Simulation, testing and result	4
4	Fabrication, testing and result	4
5	Report Presentation Role in the team	4
	Total	20

### **COURSE ASSESSMENT AND EVALUATION CHART**

Sl.No	Assessment	Duration	Max marks	Conversio n		
1.	CIE Assessment 1 (Written Test -1-theory) - At the end of 3 <sup>rd</sup> week	60 minutes	20	Average of two written		
2.	CIE Assessment 2 (Written Test -2-theory) - At the end of <b>13</b> <sup>th</sup> <b>week</b>	60 minutes	20	tests 20		
3.	CIE Assessment 3 (Skill test) - At the end of <b>5</b> <sup>th</sup> <b>week</b>	3 Hours	20	Average of		
4	CIE Assessment 4 (Skill test) - At the end of <b>7</b> <sup>th</sup> <b>week</b>	3 Hours	20	three skill tests 20		
5	CIE Assessment 5 (Skill test) - At the end of <b>9</b> <sup>th</sup> <b>week</b>	3 Hours	20	20		
6	CIE Assessment 6 (Student activity) - At the end of <b>11</b> <sup>th</sup> <b>week</b>	-	20	20		
7.	Total Continuous Internal Evaluation (CIE) Assessment					

8.	Semester End Examination (SEE) Assessment (Practical Test)	3 Hours	100	40
	Marks		Total	100

#### Note:

- 1. CIE written test is conducted for 20 marks (Two sections). Each section shall have two full questions of same CL, CO. Student shall answer one full question (10 marks) from each section.
- 2. CIE Skill test is conducted for 100 marks (3 Hours duration) as per scheme of evaluation and the obtained marks are scaled down to 20 marks

## Scheme of Evaluation for CIE and SEE (Skill test)

## (CONTINOUS INTERNAL EVALUATION & SEMESTER END EXAMINATION)

Sl. No.	Particulars	Marks
	Understanding of the problem, identification and selection	
1	of components/devices/equipment, inputs and expected	10
	outputs.	
2	Experimentation/Execution under simulated environment	20
	Circuit building using soldering and board (Assessment	
3	indicators: correctness, quality, effective use of space and	20
	soldering in conformance with standards)	
4	Experimentation and recording the observed readings in	30
4	real environment	30
5	Results interpretation (Calculation/accuracy/graph/table)	10
6	Viva-voice	10
	Total	100

#### NOTE:

- Both CIE &SEE is conducted for 100 marks (3 Hrsduration).
- Examiner is suggested to assign the question randomly and uniformly so as to avoid resource shortage in the laboratory.
- Reasonable opportunities shall be given to the student to write the circuit. Until and unless correct circuit is written he should not be allowed to rig-up/fabricate the circuit; however, student can be allowed to work under simulation environment. Then he can rig-up/fabricate the circuit for real execution.
- $\bullet \quad \hbox{Normally, the experiment is simulated first, then moving on to real experimentation.}\\$

However, order of simulation and real experimentation can be interchanged in the examination for the purpose of resource and time management during the examination.

## **RUBRICS FOR ACTIVITY**

## Faculty need to develop appropriate rubrics for respective activity

Dimension	Beginning	Developing	Satisfactory	Good	Exemplary	Student
	1	2	3	4	5	Score
	Does not collect	Collects very	Collect much	Collects	Collects a	
	any	limited	information;	some basic	great deal of	
Collection	information	information;	but very	information;	information;	
of data	relating to the	some relate	limited	most refer	all refer to	8
	topic	to the topic	relate to the	to the topic	the topic	
			topic			
Fulfil	Does not	Performs	Performs	Performs	Performs all	
team's	perform any	very little	very little	nearly all	duties of	
roles &	duties assigned	duties but	duties	duties	assigned	6
duties	to the team role	unreliable.			team roles	
	Always relies	Rarely does	Usually does	Normally	Always does	
Cl	on others to do	the assigned	the assigned	does the	the assigned	0
Shares	the work	work; often	work; rarely	assigned	work	8
work		needs	needs	work	without	
equally		reminding	reminding		having to be	
					reminded.	
	Is always	Usually does	Talks good;	Listens, but	Listens and	
Listen to	talking; never	most of the	but never	sometimes	speaks a fair	
other	allows anyone	talking;	show	talk too	amount	
Team	else to speak	rarely	interest in	much		8
mates		allows	listening			
mates		others to	others			
		speak				
			Average ,	 / Total Marks:	(8+6+8+8)/4	7.5=8

## **Model Question Paper FOR CIE (WRITTEN TEST)**

Program	me:	Semeste	er: II									
Course N	Jame :	Max Marks :20										
CourseC		<b>Duration: 60minutes</b>										
Name of	thecoursecoordinator:	Test: I/II										
Note: Answer one full question from each section. One full question carries 10 marks.												
Qn. No	Question	CL	CO	PO	Marks							
	Secti	ion-1										
1.a)												
b)												
c)												
2.a)												
b)												
c)												
	Secti	ion-2										
3.a)												
b)												
c)												
4.a)												
b)												
c)												
	Secti	ion-3										
5.a)												
b)												
c)												
6.a)												
b)												
c)												

# **Equipment List (For a batch of 20 students)**

Sl. No.	NAME OF THE EQUIPMENT	Quantity
01	Dual Channel 0-30V at 2/1A RPS with short-circuit protection	10
02	0-30V at 2/1A RPS with short circuit protection	10
03	Function Generator (0-10MHz)	10
04	Dual Trace Oscilloscope (20MHz)	10
05	Digital multimeters.	20
06	Decade resistance boxes	10
07	Decade capacitance boxes	10
08	Decade inductance boxes	10
09	LCR meter	05

10	Electronic components/Consumables resistors, inductors, capacitors, transformers, hookupwires ,SCR, MOSFET, DIAC,TRIAC, BJT, Photo transistor, Photo diode, JFET, diode, Zener diode, soldering lead etc	10
11	Bread boards, Soldering Gun, Tag Board, 9V battery cells, Bulbs.	10
12	Computer System for Circuit simulation (having Electronics Circuit Simulation Software installed in each computer).	20

# ದ್ವಿತೀಯ ಸೆಮಿಸ್ಟರ್ ಕನ್ನಡ ಬಾರದ / ಕನ್ನಡೇತರ ಡಿಪ್ಲೋಮಾ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ನಿಗಧಿಪಡಿಸಿದ ಪಠ್ಯಕ್ರಮ

# ಬಳಕೆ ಕನ್ನಡ – 1

Course Code	20KA21T	Semester: II	Course Group – AU/KA
Course Title	ಬಳಕೆ ಕನ್ನಡ – I	Category: AU	Lecture Course
No. of Credits	2	Type of Course	CIE Marks : 50
Total Contact Hours	2 Hrs Per Week 26Hrs Per Semester	Teaching Scheme (L:T:P)= 2:0:0	SEE Marks : Nil

# Table of Contents (ಪರಿವಿಡಿ)

Part - 1	Teaching Hour					
Introduction to the Book, Necessity of learning a local language, Tips to learn the language with easy methods. Easy learning of a Kannada Language: A few tips. Hints for correct and polite conservation. Instructions to Teachers for Listening and Speaking Activities.						
Part – II						
Key to Transcription for Correct Pronunciation of Kannada Language, Instructions to Teach to teach Kannada Language	ners <b>02</b>					
Part – III Lessons to teach Kannada Language - Listening and Speaking Acti	vities					
<b>Lesson</b> − 1 Personal Pronouns, Possessive Forms, Interrogative words	02					
<b>Lesson</b> − 2 Possessive forms of nouns, dubitive question and Relative nouns	02					
<b>Lesson – 3</b> Qualitative, Quantitative and Colour Adjectives, Numerals	02					
Lesson – 4 Predictive Forms, Locative Case	02					
Lesson – 5 Dative Cases, and Numerals	02					
Lesson – 6 Ordinal numerals and Plural markers	02					
Lesson – 7 Defective / Negative Verbs and Colour Adjectives	02					
Lesson – 8 Permission, Commands, encouraging and Urging words (Imperative words a sentences)	nd <b>02</b>					
Lesson – 9 Accusative Cases and Potential Forms used in General Communication	02					
Lesson – 10 Helping Verbs "iru and iralla", Corresponding Future and Negation Verbs	02					
Lesson – 11 Do's and Don'ts in Learning of Kannada Language (Any Language in Gener	ral) <b>01</b>					
Lesson – 12 Kannada Vocabulary List : ಸಂಭಾಷಣೆಯಲ್ಲಿ ದಿನೋಪಯೋಗಿ ಕನ್ನಡ ಪದಗಳ	5 - <b>01</b>					
Kannada Words in Conversation Total Teaching Hours	26 Hours					

# ದ್ವಿತೀಯ ಸೆಮಿಸ್ಟರ್ ಕನ್ನಡ ಬಲ್ಲ ಡಿಪ್ಲೋಮಾ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ನಿಗಧಿಪಡಿಸಿದ ಪಠ್ಯಕ್ರಮ

# (ಕನ್ನಡ ಭಾಷೆ, ಸಾಹಿತ್ಯ, ಸಂಸ್ಕೃತಿ ಮತ್ತು ಪರಂಪರೆ ಕುರಿತು)

Course Code	20KA21T	Semester: II	Course Group – AU/KA
Course Title	ಸಾಹಿತ್ಯ ಸಿಂಚನ – ೧	Category: AU	Lecture Course
No. of Credits	2	Type of Course	CIE Marks : 50
Total Contact Hours	2 Hrs per Week 26 Hrs per Semester	Teaching Scheme (L:T:P)= 2:0:0	SEE Marks : Nil

# ಸಾಹಿತ್ಯ ಸಿಂಚನ – ೧ (ಕಾರ್ಯಪುಸ್ತಕ) (20KA21T)

	ಪಠ್ಯಪುಸ್ತಕದ ಪರಿವಿಡಿ	ಬೋಧನಾ ಅವಧಿ
1.	ಕರ್ನಾಟಕದ ಸಂಕ್ಷಿಪ್ತ ಇತಿಹಾಸ ಮತ್ತು ಸಾಹಿತ್ಯದ ಬೆಳವಣಿಗೆ	01 ಗಂಟೆ
2.	ಕನ್ನಡ ಸಾಹಿತ್ಯದ ಸಂಕ್ಷಿಪ್ತ ಚರಿತ್ರೆ	01 ಗಂಟೆ
3.	ಹಳೆಗನ್ನಡ ಸಾಹಿತ್ಯ - ಪಂಪ ಪೂರ್ವ ಯುಗ	
	ಕನ್ನಡ ಸಾಹಿತ್ಯದ ರಚನೆಗೆ ಪ್ರಮುಖ ಪ್ರೇರಣೆಗಳು ಮತ್ತು ಪ್ರಭಾವಗಳು	03 ಗಂಟೆ
	ಕನ್ನಡ ಸಾಹಿತ್ಯ ಪರಂಪರೆ ಮತ್ತು ರಾಜಾಶ್ರಯ	
	ಕವಿರಾಜಮಾರ್ಗ ಮತ್ತು ವಡ್ಕಾರಾಧನೆ	
4.	ಪಂಪ / ಚಂಪೂ ಯುಗದ ಕನ್ನಡ ಸಾಹಿತ್ಯ ಮತ್ತು ಪರಂಪರೆ	
	ಆದಿಕವಿ ಪಂಪ, ರನ್ನ, ಪೊನ್ನ, ಜನ್ನ. ಒಂದನೇ ನಾಗವರ್ಮ ಮತ್ತು ನಾಗಚಂದ್ರ	04 ಗಂಟೆ
	10 ಮತ್ತು 11ನೇ ಶತಮಾನದ ಸಮಕಾಲೀನ ಪ್ರಮುಖ ಕವಿಗಳು	
<b>5</b> .	ನಡುಗನ್ನಡ ಸಾಹಿತ್ಯ - ವಚನ ಸಾಹಿತ್ಯ / ಬಸವ ಯುಗ	
	ವಚನ ಸಾಹಿತ್ಯದ ಬೆಳವಣಿಗೆಗೆ ಕಾರಣಗಳು ಮತ್ತು ಅದರ ಮಹತ್ವ	04 ಗಂಟೆ
	ಪ್ರಮುಖ ವಚನಕಾರರು, ವಚನ ಸಾಹಿತ್ಯದಲ್ಲಿ ವೈಚಾರಿಕತೆ ಮತ್ತು ಕಾಯಕ ತತ್ತ್ವ	
6.	ಕುಮಾರವ್ಯಾಸ ಯುಗ ಮತ್ತು ಸಾಹಿತ್ಯದ ಇತರೆ ರೂಪಗಳು	
	<b>ರಗಳೆ</b> – ಹರಿಹರ,	04 ಗಂಟೆ
	<b>ಷಟ್ಪದಿ</b> – ಕುಮಾರವ್ಯಾಸ, ಲಕ್ಷ್ಮೀಶ ಮತ್ತು ರಾಘವಾಂಕ	
	<b>ಸಾಂಗತ್ಯ -</b> ರತ್ನಾಕರವರ್ಣಿ,	
<b>7</b> .	ದಾಸ ಸಾಹಿತ್ಯ / ಕೀರ್ತನೆಗಳು	02 ಗಂಟೆ
	ಪುರಂದರದಾಸರು, ಕನಕದಾಸರು ಮತ್ತು ಇತರೆ ಕೀರ್ತನೆಕಾರಾರು	
8.	ಇತರೆ ಸಾಹಿತ್ಯದ ಪ್ರಕಾರಗಳು	04 ಗಂಟೆ
	<b>ತ್ರಿಪದಿ</b> - ಸರ್ವಜ್ಞ,	
	ಜಾನಪದ ಸಾಹಿತ್ಯ,	
	<b>ತತ್ತ್ವಪದಗಳು -</b> ಶಿಶುನಾಳ ಶರೀಫರು	
9.	<b>ಮಹಿಳಾ ಸಾಹಿತ್ಯ :</b> ಹೆಳವನಕಟ್ಟೆ ಗಿರಿಯಮ್ಮ ಮತ್ತು ಸಂಚಿಹೊನ್ನಮ್ಮ,	02 ಗಂಟೆ
	ಆಧುನಿಕ ಪೂರ್ವ ಕನ್ನಡ ಸಾಹಿತ್ಯ: ಕೆಂಪುನಾರಾಯಣ ಮತ್ತು ಮುದ್ದಣ	
10.	. ಹಳೆಗನ್ನಡ ಮತ್ತು ನಡುಗನ್ನಡ ಸಾಹಿತ್ಯ ಚರಿತ್ರೆಯ ಒಂದು ಅವಲೋಕನ	01 ಗಂಟೆ
	ಒಟ್ಟು ಬೋಧನಾ ಅವಧಿ 26 ಗಂಟೆಗಳು	26 ಗಂಟೆ

# ಬಳಕೆ ಕನ್ನಡ -ı ಮತ್ತು ಸಾಹಿತ್ಯ ಸಿಂಚನ -on ಪಠ್ಯಕ್ರಮಗಳಿಗೆ ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ ಮಾರ್ಗಸೂಚಿಗಳು

# (COURSE ASSESSMENTS AND EVALUATION CHART- CIE ONLY)

Sl.No	Assessment	Туре	Time frame in semester	Duration	Max marks	Conversion
1.	CIE Assessment 1	Written test-1	- At the end of 3 <sup>rd</sup> week	80 minutes	30	Average of three written
2	CIE Assessment 2	ent 2 Written test-2 - At the end of 7th week		80 minutes	30	tests-1,2,3
3	CIE Assessment 3	Written test-3	- At the end of 13 <sup>th</sup> week	80 minutes	30	101 30 marks
4	CIE Assessment 4   MCO/Ouiz		- At the end of 5 <sup>th</sup> week	60 minutes	20	Average of three
5	CIE Assessment 5	Open book test	- At the end of 9th week	60 minutes	20	Assessment 4,5,6 for 20
6	CIE Assessment 6	Student activity & presentation	60 minutes	20	marks	
	Total Continuous Ir	nternal Evaluation (CIE)	Assessment			50
				Total Marks		50



# Government of Karnataka Department of Collegiate and Technical Education

# C-20 Second Year Diploma Curriculum Electronics and Communications Engineering



# DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

## **Curriculum Structure**

# III Semester Scheme of Studies- Diploma in Electronics and Communications Engineering

	11				Hours per week		è		CI Mar		SEE Marks			ing			
Sl. No.	+	Course Code	Course Name	L	Т	P	Total contact hrs /week	Credits	Max	Min	Max	Min	Total Marks	Min Marks for Passing (including CIE marks)	Assigned Grade	Grade Point	SGPA and CGPA
	Integrated Courses																
1	PC/EC	20EC31P	Analog Electronics	3	1	4	8	6	60	24	40	16	100	40			
2	PC/EC	20EC32P	Logic Design using Verilog	3	1	4	8	6	60	24	40	16	100	40			CGPA
3	PC/EC	20EC33P	Communication Systems	3	1	4	8	6	60	24	40	16	100	40			∞3
4	PC/EC	20EC34P	Electronic Measurements and Testing Techniques	3	1	4	8	6	60	24	40	16	100	40			Both SGPA
					Aud	it Cour	se										Bc
5	AU/KA	20KA31T	ಸಾಹಿತ್ಯ ಸಿಂಚನ-॥/ ಬಳಕೆ ಕನ್ನಡ-॥	2	0	0	2	2	50	20	-	-	50	20			
			Total	14	4	16	34	26	290	116	160	64	450	180			

<sup>\*</sup>PC: Programme Core:: AU-Audit Course:: KA: Kannada:: L: Lecture:: T: Tutorial:: P: Practice



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

## **Curriculum Structure**

# IV Semester Scheme of Studies- Diploma in Electronics and Communications Engineering

	y /		Н	ours per w	eek	ķ		CI Mar		SE Mar			ing				
Sl. No.	T ← T Course Co	Course Code	Course Name	L	Т	Р	Total contact hrs /week	Credits	Max	Min	Max	Min	Total Marks	Min Marks for Passing (including CIE marks)	Assigned Grade	Grade Point	SGPA and CGPA
	Integrated Courses																
1	PC/EC	20EC41P	PCB Design & Fabrication	3	1	4	8	6	60	24	40	16	100	40			
2	PC/EC	20EC42P	Wireless Communication	3	1	4	8	6	60	24	40	16	100	40			CGPA
3	PC/EC	20EC43P	Embedded C Programming	3	1	4	8	6	60	24	40	16	100	40			જ
4	PC/EC	20EC44P	Industrial Automation	3	1	4	8	6	60	24	40	16	100	40			Both SGPA
	1	I			Aud	it Cour	se			l		l					Bo
5	AU/EC	20EC45T	Indian Constitution	2	0	0	2	2	50	20	1	-	50	20			
			Total	14	4	16	34	26	290	116	160	64	450	180			

<sup>\*</sup>PC: Programme Core:: AU-Audit Course:: L: Lecture:: T: Tutorial:: P: Practice

# 3rd SEMESTER



## DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics and Communication	Semester	III
Course Code	20EC31P	Type of Course	Programme Core
Course Name	Analog Electronics	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

### 1. Rationale

Analog electronics is a branch of electronics that deals with a continuously variable signal. It is widely used in radio and audio equipment along with other applications where signals are derived from analog sensors before being converted into digital signals for subsequent storage and processing. Analog Electronics offers a very elegant design with many components and would effectively act as an impetus to the digital world.

## **2. Course Outcomes/Skill Sets:** On successful completion of the course, the students will be able to:

CO-01	Identify the components in a given analog electronic circuit and list their characteristics and uses.
CO-02	Study the given analog circuit and using the data sheets/specification sheets, list alternative electronic components for the given circuit.
CO-03	Construct an analog electronic circuit for a given application and demonstrate the working of that circuit either in Real or Simulated environment.
CO-04	Test a given circuit for a desired result/outcome, identify the problem and troubleshoot to obtain the desired result/outcome.

## 3. Course Content

			Lecture	Tutorial	Practice
			(Knowledge Criteria)	(Activity	(Performance Criteria)
Week	CO	PO		Criteria)	
			3 hours/week	1 hour/week	4 hours/week (2
			,		hours/batch twice in a
					week)
1	1,3,4	1,3,	Power Supplies		1. Build 5V/12V Regulated
		4,6,			Power Supply.
		7	1.Need, Types – Unregulated,		
			Regulated – Linear, Switched, Battery,		2a) Identify the
			Selection Criteria of different power	Refer Table 1	components in a SMPS.
			supplies		
			2. RPS & UPS – Online & Offline – Block		2b) Identify front panel
					control & indicators of
			Diagram and its working principle		UPS
			3. SMPS – Block diagram and its		
			working principle		
2	1,3,4	1,3,	Wave Shaping Circuits.		1. Generate the following
		4,6,		Refer Table 1	waveforms from
		7	1. RC Integrator & RC Differentiator.		sinusoidal waveform.
			2. Clippers - Series, Shunt & Biased.		a. Trapezoidal waveform.

					b. Positive Cycle.
			3. Clampers – Positive Voltage & Negative Voltage, Voltage Multipliers – doubler, Tripler.		2. Construct and verify voltage doubler and tripler circuit to multiply the input voltage.
3	1,2,3,4	1,3, 4,6, 7	Special Purpose Devices. 1. Features & Applications of Tunnel Diode, Varactor Diode.		1. Identify & test all special purpose diodes and interpret their data sheets.
			2.Features & Applications of Gunn diode & PIN diode, Solar cell	Refer Table 1	2. Simulate/Analyse Schottky diode/PIN
			3. Features & Applications of Schottky diode & UJT.		diode/Gunn Diode/Varactor Diode application circuits.
4	1,2,3,4	2,3, 4,6, 7	Transistor Amplifiers.  1. Introduction, DC load line, Operating point, Need for biasing, Stabilization, stability factor.  2. Types of biasing-voltage divider bias for BJT.  3. Classification of Amplifiers-based on use, frequency, coupling methods & mode of operations (advantages, disadvantages)	Refer Table 1	1a. Demonstrate Numbering System of Semiconductor Devices. 1b. Identify Transistors in different packages and interpret their datasheets.  2a.Construct/Simulate a AND/OR Gate using transistors 2b. Design and construct voltage divider biasing circuit to fix an operating point and test the voltages
5	3,4	1,3, 4,6	<ol> <li>Common Emitter Transistor         Amplifier-Working, Voltage gain, phase reversal.     </li> <li>RC Coupled transistor amplifier-frequency response.</li> <li>Power amplifiers- classification, principle &amp; performance criteria of power amplifiers.</li> </ol>	Refer Table 1	1.Construct voltage divider biased single- stage RC coupled CE amplifier and plot frequency response  2. Simulate the RC coupled amplifier using BJT. Verify the same using FET.
6	3,4	1,2, 5,6, 7	1. Working of Class A–Series-fed amplifier and transformer-coupled amplifier. Expression for output power and maximum power efficiency  2. Class B- Push pull Amplifier and complementary-symmetry push-pull amplifier. Expression for output power and maximum power efficiency.  3. Working of Class AB and Class C amplifiers. Stages of practical power	Refer Table 1	<ol> <li>Demonstrate and document the working of a power amplifier using video or simulator.</li> <li>Construct and Demonstrate/Simulate the working of push pull amplifier. Verify the same using FET.</li> </ol>
			amplifier, Concept and expression for voltage gain of multistage amplifiers.		

		1		1	
7	1,2,3	1,4, 6	1. Op-amp: Block diagram, Symbol, Basic differential amplifier- Working principle.	Refer Table 1	1. Identify Op-amp IC, its pins and Interpret its data sheet.
			2. Modes of operation-Single ended, Common mode & Differential mode, Ideal and practical characteristics.		2. Conduct an experiment to find the practical characteristics of Op-amp
			3.0p-amp parameters: Input offset voltage, input offset current, power supply rejection ratio, CMRR, Input and output impedance, gain, gainbandwidth product, slew-rate		and compare them with ideal characteristics.
8	3,4	1,4, 6	1. Open-loop configuration: Comparator-inverting, non-inverting, applications, disadvantages.		1. Construct and test an op-amp circuit to obtain Inverting & Non inverting output.
			2. Closed-loop configuration: virtual ground, applications - inverting, non-inverting amplifier.	Refer Table 1	2. Construct a circuit to obtain the Sum/Difference of all
			3. Voltage follower, summing & difference amplifiers.		input voltages.
9	3,4	1,3, 4,6	<ol> <li>Construct and verify Op-amp as Differentiator, Integrator.</li> <li>Op-amp as Schmitt trigger and</li> </ol>		1. Construct a circuit to obtain triangular wave and spike from square wave.
			precision rectifier, Gain of Multistage Op-Amp Circuits.	Refer Table 1	Build an op-amp circuit to generate clock pulses
			3. Sinusoidal Oscillators, Types of Oscillations, LC Tank circuit and stability.		and verify its working.
10	3,4	1,3, 4,6	1. Concept of feedback and types, Barkhausen criteria.		1. Construct/Simulate Hartley oscillator using BJT. Verify the same using
			2. Types of Oscillators, Working of Hartley oscillator using BJT/Op-amp and its applications.	Refer Table 1	op-amp.  2. Construct, test and Troubleshoot Colpitts
			3. Working of Colpitts and crystal oscillator using BJT/Op-amp and their applications		oscillator using BJT/op-amp.
11	3,4	1,3, 4,6	1. Working of RC phase-shift and Wein-bridge oscillators using Op-amp and their applications.		1. Design and implement /Simulate RC phase shift oscillator for generating a frequency of 1khz using
			2. Filters: Classification, Applications & Advantages of Active over Passive Filters.	Refer Table 1	BJT. Verify the same using op-amp.
			3. Filter Terminology, frequency response of 1st order Butterworth LPF, HPF (No Derivation).		2. Conduct an experiment to plot the frequency response of LPF & HPF.

Total in	hours		multivibrator.	13	52
			3. IC 555 timer as monostable		555 timer as monostable multivibrator.
			multivibrator.	iciei iabic i	2. Verify the working of IC
			2. IC 555 timer as Astable	Refer Table 1	multivibrator.
		4	Configuration. Interpret Datasheets.		555 timer as astable
13	1,3,4	1,3,	1. IC 555 Timer: Internal diagram, Pin		1. Verify the working of IC
			3. Phase Locked Loop (PLL): voltage to frequency converter, PLL operation with mention of its applications		2. Verify the working of PLL using a simulator.
			2. Instrumentation amplifier-Need for instrumentation amplifier, Working of instrumentation amplifier circuit.	Refer fable 1	Potential Signals (using simulator or video to be displayed)
12	3,4	1,3, 4,7	1. Frequency response of 1st order Butterworth BPF and Band Elimination Filter, BEF (No Derivation).	Refer Table 1	1. Build an Instrumentation Amplifier Circuit to detect and Amplify Analog/Bio-

Note: 1) In Practice sessions Video demonstration should be followed by MCQs/Quiz/Subjective questions and the evaluation has to be documented.

2) In Practice sessions, all discrete circuits should be simulated using suitable software before its construction and verification.

**TABLE 1: Suggested activities for tutorials** 

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week. No.	Suggested activities for tutorials
01	<ol> <li>Gather knowledge and give a presentation on the type of power supply used in mobile charger, desktop computer and laptop with its specifications and Justify.</li> <li>Build a Notch Filter to reject 50 Hz noise in power supplies and demonstrate it in the class.</li> <li>Identify the type of UPS used in the lab, its specifications, analyze its load carrying capacity related to its power factor and prepare a report on it.</li> </ol>
02	<ol> <li>Design and build a circuit that can store maximum voltage of the input signal (Peak Detector) and demonstrate it in the class.</li> <li>Prepare a report on any one application of peak detector in daily life, also compare the nature of output of a rectifier and a peak detector.</li> <li>Prepare a video of a circuit which increases the input voltage 4 times. (Quadrupler).</li> </ol>

03	1. Give a presentation on the use of opto isolator to detect DC or control AC signals and data.
	2. Demonstrate the use of PIN diode as a switch in domestic applications.
	3. Build a power supply switching circuit using optocouplers.
04	1. Prepare a report on applications of each type of amplifier and present it.
	2. Demonstrate any one real life application of an amplifier.
05	1. Prepare a report and explain a specific application of emitter follower in daily life. (Ex: as switching circuit, isolator circuit, voltage buffer, impedance matching circuit).
	2. Prepare a presentation on comparison of power amplifiers.
06	1. Prepare a video/report on any one real life application of a power amplifier.
	2. Build and demonstrate radio player amplifier circuit.
	3. Give a presentation on low noise amplifiers.
07	1. Explain the criteria for selecting an op-amp for a given application.
	2. Identify at least 5 electronic circuits using op-amp and present the details of its working.
08	1. Prepare a report on comparison of transistor amplifier and op-amp.
00	2. Demonstrate the operation of auto cut for manual stabilisers using 741 IC.
09	1. Explain how an op-amp can be used in applications such as A/D converters, wave shaping circuits etc.
	2. Prepare a report on Schmitt trigger applications such as switch debouncing, noise removal etc.
10	1. Demonstrate the operation of a variable audio frequency oscillator using op-amp 741.
10	2. Explain the working of FM radio jammer.
11	1. Discuss the problems to design and analyse 1st order butterworth filters.
11	2. Demonstrate how LEDs can be made to blink on the beats of music.
12	Prepare a report on different applications of instrumentation amplifier
12	2. Explain the operation of Frequency Shift Keying (FSK) generator using PLL 565.

- 1. Study the latest technological changes in this course and present the impact of these changes on industry.
  - 2. Demonstrate the use of IC 555 timer in traffic light controller.
  - 3. List the real life applications of IC 555 timer and explain any one application.

#### LINKS FOR REFERENCE.

- 1. <a href="https://www.teamwavelength.com/power-supply-basics/">https://www.teamwavelength.com/power-supply-basics/</a>
- 2. https://www.tutorialspoint.com/electronic circuits/electronic circuits smps.htm
- 3. www.electronicshub.org
- 4. <a href="https://images.app.goo.gl/xb2JnuqBKyaLgwi6A">https://images.app.goo.gl/xb2JnuqBKyaLgwi6A</a> (Tutorial 6)
- 5. <a href="https://youtu.be/mgoCeOCjiBI">https://youtu.be/mgoCeOCjiBI</a> (Experiment 7)
- 6. <a href="https://www.circuitstoday.com/">https://www.circuitstoday.com/</a>
- 7. https://elec-club-iitb.github.io/blog/2016/09/get-electrified-2/
- 8. <a href="https://bestengineeringprojects.com/frequency-shift-keying-fsk-generator-using-pll-565/">https://bestengineeringprojects.com/frequency-shift-keying-fsk-generator-using-pll-565/</a>
- 9. <a href="https://images.app.goo.gl/cbkCDCHJngANwiyF6">https://images.app.goo.gl/cbkCDCHJngANwiyF6</a>
- 10.<u>http://www.allaboutcircuits.com</u>
- 11.<u>http://www.allaboutcircuits.com/videos</u>

#### E-WEBSITES FOR REFERENCE

- 1. Electronic Tutorials
- 2. Spark fun-Learning section
- 3. All about circuits
- 4. Electronics theory
- 5. Electronics Lab
- 6. Instructables

#### 4. CIE and SEE Assessment Methodologies

Sl.	Assessment	Test Week	Duration	Max	Conversion
No			In minutes	marks	
1.	CIE-1 Written Test	5	80	30	Average of three tests
2.	CIE-2 Written Test	9	80	30	30
3	CIE-3 Written Test	13	80	30	
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two skill
5	CIE-5 Skill Test-Practice	12	180	100	tests 20
6	CIE-6 Portfolio continuous evaluation of Activity through Rubrics	1-13		10	10
		tal CIE Marks	60		
	Semester End Examination	n (Practice)	180	100	40
				Total Marks	100

## 5. Format for CIE (1, 2, 3) Written Test

Course Name			Test	I/II/III	Sem	III/IV
Course Co	de		Duratio	80 Min	Marks	30
			n			
Note: Ans	wer	any one full question from each section	n. Each ful	l question ca	rries 10 mark	S.
Section		Assessment Questions		Cognitive	Course	Marks
				Levels	Outcome	
I	1					
	2					
II	3					
	4					
III	5					
	6					

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

## 5.(a)Format for CIE-(4,5) Skill Test -Practice.

SL.	COs	Particulars/Dimension	Marks
No.			
1	1,2	Identify and test the given Electronic components - 10 Marks	20
		Interpret datasheet of any one Electronic component -10 Marks	
2	3	Test the working of electronic circuit using simulation.	20
3	3 ,4	a) Writing the circuit diagram, tabular column, formula - 10 Marks b) Build the circuit - 10 Marks c) Test, troubleshoot and demonstrate working of the circuit - 10 Marks d) Result - 10 Marks	40
4	1,2,3, 4	PortFolio evaluation of Practice sessions through rubrics	20
	•	Total Marks	100

## 6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl. No.	Dimension	Beginner	Intermedia te	Good	Advanced	Expert	Students Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
_	Average Marks= (8+6+2+2)/4=4.5						

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

## 7. Reference:

Sl. No.	Description		
1	1. Electronic Devices and Circuits, S. Salivahanan, N. Sereshkumar, McGraw Hill Education (India		
	Private Limited, ISBN - 9781259051357		
2	Op-amps and linear integrated circuits, Ramakanth A. Gayakwad, ISBN- 9780132808682		
3	Principles of Electronics, Rohit Mehta and V K Mehta, S. Chand and Company Publishing, ISBN		
	9788121924504		
4	Electronic Devices and Circuits, David A. Bell, Oxford University Press, ISBN 9780195693409		
5	Fundamentals of Electrical and Electronics Engineering, B. L. Theraja, S. Chand and Compan		
	Publishing. REPRINT 2013, ISBN-8121926602.		

## 8. SEE Scheme of Evaluation

SL.	COs	Particulars/Dimension	Marks
No.			
1	1.2	Identify and test the given Electronic components - 10 Marks	20
		Interpret datasheet of any one Electronic component -10 Marks	
2	3	Test the working of electronic circuit using simulation.	20
3	3 ,4	Conduct an experiment on analog circuit	
		a) Writing the circuit diagram, tabular column, formula - 10 Marks	40
		b) Build the circuit -10 Marks	
		c) Test, troubleshoot and demonstrate working of the circuit - 10 Marks	
		d) Result - 10 Marks	
4	1,2,3,	Viva-Voce	20
	4		
		Total Marks	100

# 9. Equipment/software list with Specification for a batch of 20 students

Sl. No.	Particulars	Specification	Quantity
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	MATLAB /PSPICE/Electronic Workbench Software for simulation		
3	Regulated Power Supply (Single)	1A/2A 0-30V	10
4	Regulated Power Supply (Dual)	1A/2A 0-30V	10

5	DC Voltage supply	(+/-5v, +/-12V, +/-15V	10
6	Digital multimeters		10
7	Function/Signal generator		10
8	Dual trace oscilloscope	Upto 20-30MHz	10
9	Electronic consumables (Diode, Transistor(npn and pnp), Resistors, Inductors, Capacitors, Special purpose diodes,etc)		Consumables as required
10	Step down transformers	6-0-6v 12-0-12v	10 each
11	OP-amps	741 IC	20
12	IC 555		10
13	Single strand wire/ patch cards	Different lengths	
14	Probes		10
15	Breadboard/Analog trainer kit		10



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics and Communication	Semester	III
Course Code	20EC32P	Type of Course	Program Core
Course Name	Logic Design using Verilog	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

### 1. Rationale

Digital Electronics is a field of electronics involving the study of digital signals and engineering of devices that use or produce them. It is very important in today's life because if digital circuits are used instead of analog circuits the signals can be transmitted without degradation due to noise. Also in a digital system information stored is easier than that of analog systems. The functionality of digital circuits can be changed easily with the help of software without changing the actual circuit. Verilog, a Hardware Description Language, is used for describing digital electronic circuits and systems. It is used for verification of digital circuits through simulation, for timing analysis, for test analysis and for logic synthesis.

### **2. Course Outcomes:** On successful completion of the course, the students will be able to:

CO-01	List the types of Verilog modeling and the use of each model for specific application		
CO-02	Design and construct a sequential circuit for a given application and test the circuit to obtain the		
	desired result/output.		
CO-03	Compare and contrast combinational and sequential circuits and simulate a given circuit using		
	Verilog descriptions to test to obtain the desired result/output		
CO-04	List the various types of A to D, D to A converters along with memory and for a given application select the appropriate converters and/or memory types to be used to obtain the given result/output.		

## 3. Course Content

	CO	PO	Lecture	Tutorial	Practice
Week			(Knowledge Criteria)	(Activity Criteria)	(Performance Criteria)
			3 hours/week	1 hour/week	4 hours/week (2 hours/batch twice in a week)
1	1	1,4,5, 6,7	<ol> <li>VLSI - Introduction, Importance &amp; Need.</li> <li>HDL- Introduction, Importance, Need &amp; Types.</li> <li>Introduction to Verilog HDL, Types of modeling- Switch level, Structural, Data flow and Behavioral.</li> </ol>	Refer Table 1	1. Familiarization of Xilinx software.  2. Familiarization of FPGA/CPLD KIT.
			3. Basic Concepts- Lexical conventions, comments, keywords, identifiers, strings.		

2	1	1,2,4	<ol> <li>Data types -Value Set, Wires, Nets, Registers, Vectors, Integers, Real, Time, Parameters, Arrays, Strings.</li> <li>Operators- Arithmetic, Logical, Relational, Bit-wise.</li> <li>Reduction, Shift, Concatenation, Replication, Conditional operators. Operator Precedence.</li> </ol>	Refer Table 1	1. Demonstrate and Practice simple examples using different data types.  2. Compute the output for expressions having different operators using simple programs.
3	1,3	1,2,3,	<ol> <li>Program structure- Module declaration, port declaration, port connection.</li> <li>Gate level modeling for basic gates.</li> <li>Gate level Verilog description for half adder, full adder.</li> </ol>	Refer Table 1	Write the verilog code, simulate and download to FPGA/CPLD kit for the following  1. 2 input basic gates using gate level modelling.  2. Full adder and full subtractor using gate level modelling.
4	1,3	1,2,3, 4,6	<ol> <li>Data flow modeling- Continuous assignment, Module instantiations, net declaration, delays, expressions.</li> <li>Data flow Verilog description of multiplexer and demultiplexer.</li> <li>Data flow Verilog description for 4-bit comparator</li> </ol>	Refer Table 1	Write the verilog code, simulate and download to FPGA/CPLD kit for the following.  1. 4:1 Mux and 1:4 Demux using data flow modeling.  2. Comparator using data flow modeling.
5	1,3	2,3,4,	<ol> <li>System tasks-display, strobe, monitor, reset, stop, finish.</li> <li>Compiler directives- include, define.</li> <li>Behavioral modeling- Always and Initial statements.</li> <li>Procedural Assignments-Blocking and non-blocking assignments.</li> <li>Timing Control-Delay, Event</li> <li>Conditional statements-if, if-else, Case, Loops- While, For, Repeat, Forever.</li> </ol>	Refer Table 1	1a.Write and execute simple programs to illustrate conditional statements. 1b. Write and execute simple programs to illustrate loops.  2. Write the verilog code, simulate and download to FPGA/CPLD kit for a 4-bit ALU with any 2 arithmetic and logical operations.
6	1,3	1,2,3, 4,6	<ol> <li>Behavioral Verilog description for BCD to seven segment decoder for common anode display using ifelse, Case.</li> <li>Traffic light controller using Behavioral description.</li> <li>Test bench- Need, Importance, testbench for half adder.</li> </ol>	Refer Table 1	<ol> <li>Write the verilog code, simulate and download to FPGA/CPLD kit for a BCD to seven segment decoder using case statement.</li> <li>Write and simulate a Test bench for half adder.</li> </ol>

1.23		1	1	T		
truth table, working, timing diagram.  2. JK, JK-MS flip flops-Logic circuit, truth table, working, timing diagram.  3. D. T flip flops-Logic circuit, truth table, working, timing diagram.  8 2.3 1,2.3, 1. Verilog description of SR flip flops using data flow modeling.  2. Verilog description of JK flip flop using behavioral modeling.  9 2.3 1,2.3, 1. Verilog description of SR flip flop using behavioral modeling.  9 2.4 1,2.3, 1. Verilog description of SR flip flop using behavioral modeling.  9 2.5 1,2.3, 1. Verilog description of SR flip flop using behavioral modeling.  9 2.5 1,2.3, 1. Verilog description of SR flip flop using behavioral modeling.  9 2.5 1,2.3, 1. Verilog description of simple (3 or 4 bit) SISO using flip-flops.  10 3 1,3.4, 1. Verilog description of simple (3 or 4 bit) SISO using flip-flops.  10 2. Verilog description of any one shift register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3. 1,3.4, 6,7 2,4 6,7 3,4 6,7 4,5 6,7 4,5 6,7 4,5 6,7 4,5 6,7 4,5 6,7 4,5 6,7 4,5 6,7 4,5 6,7 5,5 6,5 6,7 5,5 6,5 6,7 6,7 6,7 6,7 6,7 6,7 6,7 6,7 6,7 6,7	7	2	1,2,3, 4,6,7			1 Construct and test clocked
2				truth table, working, timing		SR Flip flop using NAND
truth table, working, timing diagram.  3. D, T flip flops-Logic circuit, truth table, working, timing diagram. Relevance of Asynchronous inputs to flip-flops.  8					Refer Table 1	garee in angivar eramer inv
Section   Sect				truth table, working, timing		I
table, working, timing diagram. Relevance of Asynchronous inputs to flip-flops.  2.3 1,2,3, 4 1. Verilog description of SR flip flops using data flow modeling. 2. Verilog description of JK flip flop using behavioral modeling. 3. Registers- Classification of registers, realization of simple (3 or 4 bit) SISO using flip-flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit). 3. Verilog description of JPO, PISO and PIPO using flip flops. 2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit). 3. Verilog description of any one shift register using any modeling.  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers. 2. Ring and Johnson counter(4-bit).  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers. 2. Ring and Johnson counter(4-bit).  2. Working and realization of synchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3 bit/4 bit) counters using flip-flops.  3. Realization of partial mod (mod n) counters synchronous. 3. Realization of higher-mod counters using lower-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using lower-mod counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1, 2,3, 1, ADC specifications, types, working of Flash ADC.  Refer Table 1 1. Construct/Simulate and verify the working of Flash ADC.				diagram.		
Refer Table 1  2,3 1,2,3 1, Verilog description of SR flip flops using data flow modeling.  2. Verilog description of JK flip flop using behavioral modeling.  3. Registers- Classification of registers, realization of simple (3 or 4 bit) SISO using flip-flops.  9 2,3 1,2,3 4,6,7 PIPO using flip flops.  2. Concept of universal shift-registers, resilization of sIPO, PISO and PIPO using flip flops.  2. Concept of universal shift-registers using any modeling.  3. Verilog description of any one shift register using any modeling.  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers. 2. Ring and plants of asynchronous (3 bit/4 bit) counters using flip-flops.  10 3 1,3,4, 1. Counters - definition, 6,7 classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  11 2. April 1,2,6, 1. Realization of flipfer-mod counters-asynchronous, synchronous, synchron						observe the timing diagram.
1.2.3				Relevance of Asynchronous inputs		
Section   Properties   Proper		2.2	122	* *		Marit a the assertion and a
2. Verilog description of JK flip flop using behavioral modeling.  3. Registers - Classification of registers, realization of simple (3 or 4 bit) SISO using flip-flops.  9. 2,3 1,2,3, 4,6,7 PIPO using flip flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 6,7 Cassification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3 bit/4 bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  11 3,4 1,2,6, 7 Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  2. Verilog description of any one counter using flip-flops.  2. Verilog description of any one counter using any modeling.  2. Verilog description of any one counter using any modeling.  3. Data converters - Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 1. ADC specifications, types, working of Flash ADC.  2. Verilog description to Jerus the verilog of Flash ADC.  3. Refer Table 1 following using digital trainer kit  1. SIST, JK flip flops using behavioral modeling.  2. Construct and verify the working of R-2R DAC.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.  1. Construct/Simulate and verify the working of Flash ADC.	8	2,3				simulate and download to
3. Registers- Classification of registers, realization of simple (3 or 4 bit) SISO using flip-flops.  9 2,3 1,2,3, 4,6,7 PIPO using flip flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 6,7 Classification, modulus. Working and realization of synchronous (3 bit/4 bit) counters using flip-flops.  10 3 1,3,4, 6,7 Classification, modulus. Working and realization of synchronous (3 bit/4 bit) counters using flip-flops.  11 3,4 1,2,6, 1. Realization of partial mod (mod n) counters-asynchronous, synchronous.  3. Realization of higher-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. Refer Table 1  3. I, I, Realization of higher-mod counters using lower-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Flash ADC.  12 4 1,2,3, 1. ADC specifications, types, working of Flash ADC.  13 I, SR, JK flip flops using behavioral modeling and flow modeling.  1. SR, JK flip flops using behavioral modeling.  2. Construct and verify the working of Flash ADC.  1. SR, JK flip flops using behavioral modeling.  2. Lority of the following using suitable IC in digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers.  2. Refer Table 1  3. SR, JK flip flops using behavioral modeling  2. Construct and verify the working of Hash					Refer Table 1	
3. Registers- Classification of registers, realization of simple (3 or 4 bit) SISO using flip-flops.  9 2,3 1,2,3, 4,6,7 1. Realization of SIPO, PISO and PIPO using flip flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 6,7 6,7 6,7 6,7 6,7 6,7 6,7 6,7 6,7 6,				using behavioral modeling.		1. SR, JK flip flops using data
9 2,3 1,2,3, 4,6,7 1. Realization of SIPO, PISO and PIPO using flip flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 1. Counters - definition, classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3 bit/4 bit) counters and their comparison.  3. Realization of partial mod (mod n) counters, and their comparison.  3. Realization of higher-mod counters using glower-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. A 1,2,6, 7				9		flow modeling
4,6,7 PIPO using flip flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 6,7 Classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3 bit/4-bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  3. Realization of partial mod (mod n) counters asynchronous.  3. Realization of bigher-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 1. ADC specifications, types, working of Flash ADC.  Refer Table 1 trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) and PIPO(4-bit) counter using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral moderity the working of the following using suitable IC in digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral moderity the working of Refer Table 1  1. SISO, SIPO, PISO and PIPO(4-bit) counter using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral deverify the working of Refer Table 1  1. SISO, SIPO, PISO and PIPO(4-bit) counter using using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral modeling.  2. Working and realization of any one counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.				_ = -		
4,6,7 PIPO using flip flops.  2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 6,7 Classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3 bit/4-bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  3. Realization of partial mod (mod n) counters asynchronous.  3. Realization of bigher-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 1. ADC specifications, types, working of Flash ADC.  Refer Table 1 trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) and PIPO(4-bit) counter using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral moderity the working of the following using suitable IC in digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral moderity the working of Refer Table 1  1. SISO, SIPO, PISO and PIPO(4-bit) counter using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral deverify the working of Refer Table 1  1. SISO, SIPO, PISO and PIPO(4-bit) counter using using digital trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) counter using behavioral modeling.  2. Working and realization of any one counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.	9	2,3	1,2,3,	1. Realization of SIPO, PISO and		Construct and verify the
2. Concept of universal shift-register. Ring counter and Johnson's counter (3 bit).  3. Verilog description of any one shift register using any modeling.  1. Counters - definition, classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3-bit/4-bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  3. Realization of higher-mod counters. Concept of up/down counters. Concept of up/down counters.  2. Verilog description of any one counter using any modeling.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12. 4 1,2,3, 4,6,7 working of Flash ADC.  Refer Table 1 trainer kit  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers.  2. Ring and Johnson counter(1. Sing and Johnson counter(2. Ring and Johnson counter(4-bit).  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers.  2. Ring and Johnson counter(4-bit).  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers.  2. Ring and Johnson counter(4-bit).  Construct and verify the working of the following using digital trainer kit  1. 3 bit ripple counter using IC 7476. 2. 4 bit counter as a frequency divider.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.  1. Construct/Simulate and verify the working of Flash			4,6,7	PIPO using flip flops.		
counter (3 bit).  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers. 2. Ring and Johnson counter(4-bit).  10 3 1,3,4, 6,7 classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3-bit/4-bit) counters and their comparison.  2. Working and realization of synchronous (3-bit/4-bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  11 3,4 1,2,6, 7 1. Realization of higher-mod counters using lower-mod counters. Concept of up/ down counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 4,6,7  1. All C specifications, types, working of Flash ADC.  1. SISO, SIPO, PISO and PIPO(4-bit) shift registers. 2. Ring and Johnson counter (2- Ring and Johnson counter (4-bit).  Construct (4-bit).  1. 3 bit ripple counter using IC 7476. 2. 4 bit counter using because of the working of Refer Table 1  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of Flash ADC.  1. Construct/Simulate and verify the working of Flash					Refer Table 1	
3. Verilog description of any one shift register using any modeling.  10 3 1,3,4, 6,7						1. SISO, SIPO, PISO and
shift register using any modeling.  1,3,4, 6,7   1, Counters - definition, classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2, Working and realization of synchronous (3-bit/4-bit) counters and their comparison.  3, Realization of partial mod (mod n) counters-asynchronous, synchronous.  1, 2,6, 7   1, Realization of higher-mod counters. Concept of up/ down counters.  2, Verilog description of any one counter using any modeling.  2, Verilog description of any one counter using any modeling.  3, Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  1, Construct and verify the working of the following using digital trainer kit  1, 3 bit ripple counter using IC 7476.  2, 4 bit counter as a frequency divider.  1, Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2, Construct/Simulate and verify the working of R-2R DAC.  2, Construct/Simulate and verify the working of Flash ADC.  2, Construct/Simulate and verify the working of Flash ADC.						PIPO(4-bit) shift registers.
10 3 1,3,4, 6,7 classification, modulus. Working and realization of asynchronous (3 bit/4 bit) counters using flip-flops.  2. Working and realization of synchronous (3-bit/4-bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  11 3,4 7,2,6, 7 counters using lower-mod counters. Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 4,6,7 working of Flash ADC.  Construct and verify the working of the following using digital trainer kit  1. 3 bit ripple counter using lic 7476. 2. 4 bit counter as a frequency divider.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of Flash ADC.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of Flash						1
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2. Working and realization of synchronous (3-bit/ 4-bit) counters and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  11 3,4 1,2,6, 7				bit/4 bit) counters using flip-flops.	Dofor Table 1	1 2 hit ripple counter using
and their comparison.  3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.  12 4 1,2,3, 4,6,7 working of Flash ADC.  Refer Table 1 1. Construct/Simulate and verify the working of Flash				_	Refer Table I	IC 7476.
3. Realization of partial mod (mod n) counters-asynchronous, synchronous.  11 3,4 1,2,6, 7 1. Realization of higher-mod counters. Concept of up/ down counters.  Concept of up/ down counters.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 4,6,7 Working of Flash ADC.  3. Realization of partial mod (mod n) counters.  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.						
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synchronous.  1. Realization of higher-mod counters using lower-mod counters. Concept of up/ down counters. Concept of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  2. Verilog description of any one counter using any modeling.  3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  4 1,2,3, 1. ADC specifications. types, 4,6,7 working of Flash ADC.  Refer Table 1  1. Write the verilog code, simulate and download to FPGA/CPLD kit for an up/down counter using behavioral modeling.  2. Construct/Simulate and verify the working of R-2R DAC.  1. Construct/Simulate and verify the working of Flash				<u> </u>		
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counter using any modeling.  2. Construct/Simulate and verify the working of R-2R and ADC, DAC specifications, types, working of Weighted resistor type.  2. Construct/Simulate and verify the working of R-2R DAC.  12 4 1,2,3, 4,6,7 working of Flash ADC.  Refer Table 1 verify the working of Flash						up/down counter using
3. Data converters- Need for DAC and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 4,6,7 working of Flash ADC.  3. Data converters- Need for DAC verify the working of R-2R DAC.  14 1,2,3, 4,6,7 Refer Table 1 verify the working of Flash					Refer Table 1	
and ADC, DAC specifications, types, working of Weighted resistor type.  12 4 1,2,3, 4,6,7 working of Flash ADC.  Refer Table 1 DAC.  1. Construct/Simulate and verify the working of Flash				3. Data converters- Need for DAC		
12 4 1,2,3, 1. ADC specifications. types, 4,6,7 working of Flash ADC.  1. Construct/Simulate and verify the working of Flash				and ADC, DAC specifications, types,		
4,6,7 working of Flash ADC. Refer Table 1 verify the working of Flash						
	12	4			Dofor T-l-1 4	
			4,6,/	working of Fiash ADC.	keier labie 1	

Total in	hours		39	13	52
			3. PLA-Architecture, Implementation of a Boolean expressions using PLA.		
			2. PAL- Architecture, Implementation of a Boolean expressions using PAL.	Refer Table 1	2. Implementation of Boolean expressions using PLA.
13	4	1,2,3, 4,7	1. Introduction to PLDs- PAL, PLA, CPLD, FPGA, ASIC. IC Design Verification – Types & Stages.		1. Implementation of Boolean expressions using PAL.
			<ul><li>2. Working of Successive approximation and dual slope ADCs.</li><li>3. Memory devices- Introduction, classification based on different criteria, read and write operations.</li></ul>		2. Illustrate the storing and retrieving of data in RAM using suitable IC.

Note: 1) In Practice sessions Video demonstration should be followed by MCQs/Quiz/Subjective questions and the evaluation has to be documented.

2) In Practice sessions, all circuits should be simulated using suitable software before its construction and verification.

**TABLE 1: Suggested activities for tutorials** 

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week No.	Suggested activities for tutorials
01	Explain the typical design flow for VLSI IC Circuits.      Give a presentation on comparison of different types of HDLs.      Give the state of
	3. Give a presentation on comparison of different types of modeling in Verilog.
02	1. Prepare a report on declaration and initialization of variables of different data types in Verilog.
	2. Prepare a report on hierarchy of operators.
03	<ol> <li>Explain basic components of a module? Which components are mandatory?</li> <li>Prepare a report on Hierarchical names for variables.</li> </ol>
	3. Write and explain a Verilog code for 4:1 mux and 1:4 demux using gate level modeling.

04	1. Write and explain the Verilog code for full adder using data flow modeling.
01	2 Write and explain the Verilog code for 8:1 mux using data flow modeling.
	1.Give a presentation on the differences between tasks and functions
05	2. Illustrate the use of system tasks with examples.
	3. Illustrate the use of gate delays to model timing for a simple logic equation.
06	1. Compare if-else and case statements with the help of examples.
00	2. Compare all loops with the help of examples.
	3. Write and explain the verilog code for full subtractor and 1:8 demux using behavioral modeling.
	4. Explain the Verilog Test bench with an example to verify the HDL designs.
07	1. Prepare a report on differences between Combinational and Sequential circuits with examples.
	2. Give a presentation on application of flip flop as bounce elimination switch.
	3. Demonstrate the working of flip flop as a one bit memory element.
08	1. Prepare a report on flip flop ICs and their features.
00	2. Give a presentation on eliminating race -around condition in JK flip flop.
	3. Compare the advantages and disadvantages of all flip flops.
09	1. Prepare a report on shift register ICs and their features.
09	2. Give a presentation on applications of shift registers in real life.
	3. Demonstrate the working of IC 7495 as shift register.
10	1. Prepare a report on differences between asynchronous and synchronous counters.
10	2. Give a presentation on how counters can be used in a simple car parking system.
	3. Give a presentation on implementation of footfall counter for various purposes
11	1. Prepare a report & explain the specifications of DAC and ADC ICs.
11	2. Give a presentation on any application of DAC in real life.
	3. Give a presentation on any application of ADC in real life.

12	<ol> <li>Prepare a report &amp; explain the types of RAM and ROM.</li> <li>Give a presentation on usage of RAM and ROM in different digital devices.</li> </ol>
13	1. Study the latest technological changes in this course and present the impact of these changes on industry.
	2. Prepare a report on CPLD, FPGA and ASIC and its applications.
	3. Give a presentation on importance or scope of Design Verification in Integrated circuit designs.

#### LINKS.

- 1. <a href="https://verilogguide.readthedocs.io/en/latest/verilog/testbench.html">https://verilogguide.readthedocs.io/en/latest/verilog/testbench.html</a>
- 2. <a href="https://youtu.be/XES0QUi8ttY">https://youtu.be/XES0QUi8ttY</a> (week 11, exp 2)
- 3. <a href="https://www.youtube.com/watch?v=krmXg-WTbIU">https://www.youtube.com/watch?v=krmXg-WTbIU</a> (week 12, exp 1)
- 4. <a href="http://www.asicguru.com/verilog/tutorial/system-tasks-and-functions/68/">http://www.asicguru.com/verilog/tutorial/system-tasks-and-functions/68/</a>.
- 5. <a href="https://youtu.be/vHlg\_QLGIQ">https://youtu.be/vHlg\_QLGIQ</a> (week 7,exp 3)
- 6. <a href="https://youtu.be/AtX5x53FcLI">https://youtu.be/AtX5x53FcLI</a> (week 9,exp 3)
- 7. <a href="https://youtu.be/Bx 4rsUAGoM">https://youtu.be/Bx 4rsUAGoM</a>
- 8. <a href="https://www.irisys.net/people-counting">https://www.irisys.net/people-counting</a>.

#### 4. CIE and SEE Assessment Methodologies

Sl.	Assessment	Test Week	Duration	Max marks	Conversion
No			In minutes		
1.	CIE-1 Written Test	5	80	30	Average of three
2.	CIE-2 Written Test	9	80	30	tests
3	CIE-3 Written Test	13	80	30	30
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two skill
5	CIE-5 Skill Test-Practice	12	180	100	tests
					20
6	CIE-6 Portfolio continuous	1-13		10	10
	evaluation of Activity through				
	Rubrics				
		60			
	Semester End Examination	180	100	40	
	·	·	•	Total Marks	100

#### 5. Format for CIE (1,2,3) Written Test

Course Nar	Course Name Logic Design Using Verilog		I/II/III	Sem	III/IV
Course Cod	e <b>20EC32P</b>	Duration	80 Min	Marks	30
Note: Ansv	<b>Note:</b> Answer any one full question from each section. Each full question carries 10 marks.				
Section	Assessment Questions		Cognitive	Course	Marks
			Levels	Outcome	
I	1				

	2		
II	3		
	4		
III	5		
	6		

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

5. (a) Format for CIE-4 Skill Test -Practice.

SL. No.	COs	Particulars/Dimension	Marks
1	1	List the types of Verilog modelling and the use of each model for specific application.	20
2	3	Write two Verilog programs on combinational circuits for a given application -40 Marks Simulation - 20 Marks Download to FPGA kit - 10 Marks	70
3	1,3	PortFolio evaluation of Practice sessions through rubrics	10
		Total Marks	100

5. (b) Format for CIE-5 Skill Test - Practice.

SL. No.	COs	Particulars/Dimension	Marks
1	2	Write a Sequential circuit for a given application -20 Marks Conduction using DTK -20 Marks Output -10 Marks	50
2	3	Write a Verilog program on Sequential circuits for a given application - 10 Marks Simulation Output -5 Marks -5 Marks	20
3	4	Identify various types of A to D, D to A converters/ memory for a giver application & select the appropriate converters/ memory types needed to obtain the required output.	
4	2,3,4	Portfolio evaluation of Practice sessions through rubrics.	10
		Total Marks	100

#### 6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl. No.	Dimension	Beginner	Intermedia te	Good	Advanced	Expert	Students Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
	Average Marks= (8+6+2+2)/4=4.5						

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description
1	Fundamentals of Digital Logic with Verilog Design by Stephen Brown and Zvonko Vranesic
2	Verilog HDL by Samir Palnikar
3	Introduction to Verilog-Peter M Nyasulu
4	Verilog Tutorial-Deepak Kumar Tala

## 8. SEE Scheme of Evaluation

SL. No.	COs	Particulars/Dimension	Marks
1	1	List the types of Verilog modelling and the use of each model for specific application	10
2	3	Write a Sequential circuit for a given application -10 Marks Conduction using DTK -10 Marks Output -10 Marks	30
3	2	Write a Verilog program for a given application - 10 Marks Simulation - 10 Marks Download to FPGA kit 10 Marks	30
4	4	Identify various types of A to D, D to A converters and memory and for a given application & select the appropriate converters and/or memory types needed to obtain the given output.	10
5	1,2, 3,4	Viva-Voce	20
	1	Total Marks	100

# 9. Equipment/software list with Specification for a batch of 20 students

Sl.	Particulars	Specification	Quantity
No.			
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	Xilinx software		
3	Digital trainer kits		20
4	Verilog kits		20
5	Dual trace oscilloscope	20-30MHz	10
6	Digital multimeters		05
7	Patch cards	different length	250
8	Digital IC Tester		02
9	ICs 7400,7402,7404,7408,7432,7486,7442, 7445,7446,7474,7476,7427,7489,7490, 7494,7495,74141,74148,74153,74157, 74155,74193,74194,DAC0808,ADC- 0800,741		10 each



#### **Government of Karnataka**

#### DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics and Communication	Semester	III
Course Code	20EC33P	Type of Course	Programme Core
Course Name	Communication Systems	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

#### 1. Rationale

The communication system describes the information exchange between any two points. The process of transmission and reception of information is called communication. Without communication the world ceases to exist. Information or Data can be transmitted and received across any part of the world by adapting suitable techniques, process and medium, hence making the world reachable and smaller through Technology.

#### **2. Course Outcomes :** On successful completion of the course, the students will be able to :

CO-01	Identify all the components of a communication system, list their role and characteristics in the system.
CO-02	Propagate a signal through a transmission medium to obtain a desired output for given conditions in the communication system.
CO-03	Construct an analog/digital communication system for a given application and demonstrate its working either in a Real or Simulated environment.

#### 3. Course Content

Week	со	DO.	Lecture (Knowledge Criteria)	Tutorial (Activity Criteria)	Practice (Performance Criteria)
		PO	3 hours/week	1 hour/week	4 hours/week (2 hours/batch twice in a week)
1	1	1,4,5	Network theorems 1. Superposition theorem- statement and explanation with an example. 2. Maximum Power Transfer theorem-statement and explanation with an example. 3. Thevenin's theorem and Norton's theorem-statements and explanation with an example each.	Refer Table 1	1. Construct and verify maximum power transfer theorem.  2. Construct and verify Thevenin's theorem.

			T n	1	1
2	1,3	1,2,4	Resonance 1. Series resonance - circuit diagram, phasor diagram, resonance plot and characteristics.  2. Condition for series resonance, expression for frequency of resonance. Parallel resonance- circuit diagram, phasor diagram.  3. Parallel resonance-resonance plot and characteristics, Condition for resonance, expression for frequency of resonance.	Refer Table 1	<ol> <li>Construct a series/parallel resonant circuit and plot its frequency response.</li> <li>Construct a series/parallel resonant circuit and find its bandwidth and Q factor.</li> </ol>
3	1,3	1,2,4	Filters  1. Classification of filters, cut-off frequency, pass band and stop band.  2. Ideal characteristics curve of passive LPF, HPF, BPF and BRF.  3. Circuit diagram & formula for cut-off frequency of T and Π configurations of LPF and HPF.	Refer Table 1	<ol> <li>Construct and test the passive low-pass T-type filter circuit for a given cut-off frequency.</li> <li>Construct and test the passive high pass Π -type filter circuit for a given cut-off frequency.</li> </ol>
4	1,3	1,2,4	<ul> <li>Attenuators</li> <li>1. Classification and applications of attenuators. Definition of Bel, Decibel and Neper.</li> <li>2. Symmetrical T type attenuator- Circuit diagram, expression for attenuation.</li> <li>3.Symmetrical Π type attenuator- Circuit diagram, expression for attenuation</li> </ul>	Refer Table 1	<ol> <li>Construct and test         <ul> <li>T type attenuator</li> <li>circuit for the given</li> <li>attenuation &amp; Ro.</li> </ul> </li> <li>Construct and test         <ul> <li>T type attenuator</li> <li>circuit for the given</li> <li>attenuation &amp; Ro.</li> </ul> </li> </ol>
5	1,2,	2,3,4	Transmission Media 1. Need, different types of transmission media(guided, unguided), Transmission lines- Electrical model, Primary constants - R, L, G and C, Secondary constants - Characteristic Impedance and Propagation Constant.  2. Optical fiber -principle of operation, Numerical aperture, Angle of acceptance, Classification, fiber losses.  3Basic components of Fiber optic system, splices, connecters, couplers and switches.	Refer Table 1	1. Demonstrate PC to PC communication using Fiber Optic Digital Link.  2. Demonstrate installation, testing, repair and power budgeting of fiber optic cable (using simulator/video)
6	1,2,	1,4,5	Antennas  1. Concept of electric and magnetic fields in a dipole, antenna terminologypolarization, radiation pattern, antenna gain, directive gain, directivity, power gain, antenna resistance.	Refer Table 1	1. Video demonstration and documentation on the working of the dipole antenna and observe its radiation pattern.

			<ol> <li>Antenna efficiency, beam width, bandwidth, isotropic radiators.</li> <li>Effects of ground on antennas, effect of antenna height, Antenna types, examples and applications.</li> <li>Working of Dish Antenna, Feed mechanisms-Cassegrain and Horn feed.</li> </ol>		2. Video demonstration and documentation of antenna types with examples and applications.
7	2,3	1,4,5 ,6	Wave Propagation  1. Wave Propagation: Fundamentals of Electromagnetic Waves, electromagnetic spectrum.  2. Modes of wave propagation-ground wave propagation and sky wave propagation and space wave propagation, comparison.  Analog modulation  3. Block diagram of communication system, Need for modulation and types of analog modulation techniques.	Refer Table 1	1. Video demonstration and documentation on the fundamentals of electromagnetic waves and electromagnetic spectrum.  2. Video demonstration and documentation on the need for modulation and demodulation techniques.
8	3	1,2,3 ,4,6	<ol> <li>AM Transmitter and Receiver -block diagram &amp; waveforms.</li> <li>Expressions for modulating signal, Carrier signal, modulated signal, modulation index and power.</li> <li>Frequency Transmitter and Receiver-block diagram, waveform, Expressions for frequency deviation, modulation index.</li> </ol>	Refer Table 1	1. Construct and verify amplitude modulation and demodulation using kit.  2. Construct and verify frequency modulation and demodulation using kit.
9	1,3	1,3,4 ,5,6, 7	Digital communication  1. Block diagram of digital communication system. Definition of information capacity, entropy, bit-rate, baud rate and bandwidth of digital data.  2. Sampling- Sampling theorem for low pass and band pass signals, Nyquist criterion and aliasing effect.  3. Explain Analog pulse modulation techniques-PAM, PPM, PWM using waveforms.	Refer Table 1	<ol> <li>Verify sampling theorem for low pass signals using kit.</li> <li>Conduct an experiment to study the effect of aliasing using kit.</li> </ol>

1,3 1,2,3 ,4,6	3		<ol> <li>Perform an experiment to study Pulse Code Modulation and Demodulation using kit.</li> <li>Generation of Delta modulated signal using kit.</li> </ol>
11 1,3 1,2,4	<ol> <li>Baseband transmission - significance of inter symbol interference (ISI) and eye pattern.</li> <li>Digital modulation techniques-types.</li> <li>Generation and detection of Binary ASK and Binary FSK.</li> <li>Generation and detection of Binary PSK and QPSK.</li> </ol>	Refer Table 1	1. Perform an experiment to generate and detect BASK signal using kit.  2. Perform an experiment to generate and detect BPSK signal using kit.
1,3 1,2,6	Multiplexing 1. FDM & TDM- concept applications 2. PAM/TDM system -Block diagram, transmission bandwidth, synchronization, crosstalk and guard time. 3. Digital multiplexers-Principle, classification and performance factors.	Refer Table 1	1. Demonstrate TDM using Fiber Communication System.  2. Video demonstration and documentation of FDM and TDM.
3 1,2,4	Error detection & correction  1. Errors-types, redundancy, error control schemes.  2. Error control codes- types, Parity check bit coding, error detection methods-LRC.  3. VRC, CRC, Checksum with examples.	Refer Table 1	1. Video demonstration and documentation of error detection and correction.  2. Video demonstration and documentation on LRC, VRC, CRC.
Total in hours	39	13	52

Note: 1. In Practice sessions Video demonstration should be followed by MCQs/Quiz/Subjective questions and the evaluation has to be documented.

2. In Practice sessions, all discrete circuits should be simulated using suitable software before its construction and verification.

#### **TABLE 1: Suggested activities for tutorials**

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week No.	Suggested activities for tutorials
01	<ol> <li>Write a report on implementation fields of all the theorems.</li> <li>Solve problems on all theorems.</li> </ol>
02	<ol> <li>Give a presentation on demonstrations of series and parallel resonance.</li> <li>Solve problems on series and parallel resonance.</li> </ol>
03	<ol> <li>Write a report on the needs of LPF, HPF, BPF, BRF and their comparison.</li> <li>Give a presentation on the working of BPF &amp; BRF.</li> <li>Solve problems on Filters.</li> </ol>
04	<ol> <li>Give a presentation on the relationship between Bel, Decibel and Neper.</li> <li>Give a presentation on the importance of attenuators in communication circuits.</li> </ol>
05	<ol> <li>Prepare a report on properties of light and its significance in optical communication.</li> <li>Visit a nearest telephone exchange, collect and prepare a handwritten brief report on optical fibers for the communication purpose with specifications.</li> <li>Present a report on the FIBERNET broadband and compare it with traditional broadband.</li> </ol>
06	<ol> <li>Give a presentation on miniature antennas.</li> <li>Demonstrate how a mobile phone antenna performance can be improved.</li> <li>Study the technical paper and present it.</li> <li><a href="https://documentcloud.adobe.com/link/review?uri=urn:aaid:scds:US:3d5cab35-a6d0-49e4-b4b3-06c745d34d98">https://documentcloud.adobe.com/link/review?uri=urn:aaid:scds:US:3d5cab35-a6d0-49e4-b4b3-06c745d34d98</a></li> </ol>
07	<ol> <li>Give a presentation on uses of each range of frequency in the Electromagnetic spectrum.</li> <li>Give a presentation on different types of wave propagation.</li> <li>Demonstration and explain different waveforms in Analog modulation.</li> </ol>
08	<ol> <li>Give a presentation on working of superheterodyne receiver.</li> <li>Prepare a report on the merits, demerits and applications of AM and FM.</li> <li>Collect the specifications of FM receivers and explain it.</li> </ol>
09	<ol> <li>Give a presentation on comparison of analog and digital communication.</li> <li>Collect and prepare a report on the functional blocks in the digital communication system such as scramblers, unscramblers, equalizers with applications.</li> <li>Give a presentation on types of sampling.</li> </ol>

10	Give a presentation on advantages, disadvantages and applications of PCM and DPCM.     Give a presentation on advantages, disadvantages and applications of delta modulation and adaptive delta modulation.
11	<ol> <li>Prepare a report on the type of digital modulation technique used for voice signal transmission in telephone systems.</li> <li>Give a presentation on generation and detection of DPSK.</li> <li>Give a presentation on comparison of digital modulation techniques.</li> </ol>
12	<ol> <li>Give a presentation on applications of TDM and FDM.</li> <li>Prepare a report on the type of multiplexing used in mobile communication with specifications of multiplexer.</li> </ol>
13	<ol> <li>Study the latest technological changes in this course and present the impact of these changes on industry.</li> <li>Give a presentation on the merits, demerits and applications of all error detection methods.</li> </ol>

#### Links.

- 1. <a href="https://www.gopracticals.com/electrical/basic-electrical/verify-thevenin-theorem/">https://www.gopracticals.com/electrical/basic-electrical/verify-thevenin-theorem/</a>
- 2. <a href="https://youtu.be/0k7DJGuOulQ">https://youtu.be/0k7DJGuOulQ</a>
- 3. <a href="https://youtu.be/B u3sGbpM8M">https://youtu.be/B u3sGbpM8M</a>
- $\frac{4. \quad https://documentcloud.adobe.com/link/review?uri=urn:aaid:scds:US:3d5cab35-a6d0-49e4-b4b3-06c745d34d98}{2.5 \quad https://documentcloud.adobe.com/link/review.uri=urn:aaid:scds:US:3d5cab35-a6d0-49e4-b4b3-06c745d34d98}{2.5 \quad https://documentcloud.adobe.com/link/review.uri=urn:aaid:scds:US:3d5cab35-a$
- 5. <u>https://www.wikihow.com/Design-a-Simple-Antenna</u>
- 6. <a href="https://youtu.be/r4NikIMA4dQ">https://youtu.be/r4NikIMA4dQ</a>
- 7. <a href="https://youtu.be/8P6DBAxbQxY">https://youtu.be/8P6DBAxbQxY</a>
- 8. <a href="https://youtu.be/00ZbuhPruJw">https://youtu.be/00ZbuhPruJw</a>

## 4. CIE and SEE Assessment Methodologies

Sl.	Assessment	Test	Duration	Max	Conversion	
No	rissessment	Week	In minutes	marks	Conversion	
1.	CIE-1 Written Test	5	80	30	Average of three	
2.	CIE-2 Written Test	9	80	30	tests	
3	CIE-3 Written Test	13	80	30	30	
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two	
5	CIE-5 Skill Test-Practice	12	180	100	skill tests 20	
6	CIE-6 Portfolio continuous evaluation of Activity through Rubrics	1-13		10	10	
		Tota	al CIE Marks	60		
Seme	ster End Examination (Practice)		180	100	40	
		100				

## 5. Format for CIE (1, 2, 3) Written Test

Course Name		<b>Communication Systems</b>	Test	I/II/III	Sem	III/IV
Course Coo	de	20EC33P	Duration	80 Min	Marks	30
Note: Answ	<b>Note:</b> Answer any one full question from each section. Each full question carries 10 marks.					
				Cognitive	Course	_
Section	Ass	essment Questions		Levels	Outcome	Marks
	1					
I	2					
	3					
II	4					
	5					
III	6					

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

5. (a).Format for CIE-4 Skill Test -Practice

SL. No.	COs	Particulars/Dimension	Marks
1	1	Conduct an experiment on Network Theorems/ Resonance/ Filters/ Attenuators Writing schematic diagram -20 Marks Conduction -30 Marks Result -10 Marks	60
2	2	Conduct an experiment on Transmission media/Antennas	30
3	1,2	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

## 5. (b).Format for CIE-5 Skill Test-Practice

SL. No.	COs	Particulars/Dimension	Marks
1	2	Explain propagation of signals through transmission media to obtain desired output.	20
2	3	Demonstrate an analog/digital modulation / demodulation technique Write schematic diagram (2 Circuits) -30 Marks Conduction using kit -20 Marks Result -20 Marks	70
3	2,3	Portfolio evaluation of Practice sessions through Rubrics	10
	•	Total Marks	100

# **6. Rubrics for Assessment of Activity (Qualitative Assessment)**

Sl.	Dimension	Beginner	Intermediat	Good	Advanced	Expert	Students
No.			e				Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
	Average Marks= (8+6+2+2)/4=4.5						

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description	
1	Electronic communications	- George Kennady
2	Advanced Electronics Communication System.	- Wayne Tomosi
3	Understanding communication systems	- Texas Instruments
4	Fiber Optic Communication Systems,	- Dr.R.K.Singh, Wiley India
5	Principles of Electronic Communication Systems	- Louis E. Frenzel, Tata McGraw Hill
6	Digital and analog communication systems	- K.Sham Shanmugam, Wiley India

## 8. SEE Scheme of Evaluation

SL. No.	COs	Particulars/Dimension	Marks
1	1	Conduct an experiment on Network Theorems/ Resonance/ Filters/ Attenuators Writing schematic diagram -15 Marks Conduction - 15 Marks Result -10 Marks	40
2	2	Identify the type of Transmission media/Antenna used in a given application	10
3	3	Demonstrate an analog/digital modulation / demodulation technique Write schematic diagram -10 Marks Conduction using kit - 10 Marks Result - 10 Marks	30
4	1,2,3	Viva-voce	20
	<u> I</u>	Total Marks	100

9. Equipment/software list with Specification for a batch of 20 students

Sl. No.	Particulars	Specification	Quantity
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	MATLAB/PSPICE/Electronic Workbench Software Simulator		
3	Amplitude modulation and demodulation trainer kits		05
4	Frequency modulation and demodulation trainer kits		05
5	Generation and detection BASK,BFSK,BPSK trainer kits		05 each
6	Regulated Power supply	(1A/2A, 0-30V)	10
7	Dual trace oscilloscope	up to 20 to 30MHz	10
8	Digital multimeters		10
9	Function/Signal generators		10
10	Step down transformer, Capacitors, Resistors, Inductors, BJT, Opamp IC-741, Regulator IC-7812, Diode		Consumables as required
11	Single strand wire/Patch cards (different lengths)		150
12	Probes		10
13	Analog trainer kit		5
14	DC Voltage supply	(+/-5v, +/-12V, +/-15V)	10
15	Kit to demonstrate Sampling theorem and aliasing effect		05
16	Kit to demonstrate PCM		05
17	Delta Modulation and Detection trainer kit		05
18	Adaptive Delta Modulation and Detection trainer kit		05
19	Optical fiber communications trainer kit to cover all the experiments.		05
20	Computers	Pentium and higher,8GB RAM,512 HDD	20
21	Tool kit		02 set



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics and Communication	Semester	III
Course Code	20EC34P	Type of Course	Programme Core
Course Name	Electronic Measurements and Testing Techniques	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

#### 1. Rationale

The instruments used to measure any Electrical/Electronic quantity are known as measuring instruments. The standards of measurements are very useful for calibration of measuring instruments. They help in minimizing the errors in the measuring systems. Testing Techniques are means of enhancing troubleshooting and the ability to learn skills. It keeps electronic equipment in working condition and ensures safety. The damage of the equipment can be significantly reduced.

#### **2. Course Outcomes :** At the end of the Course, the student will be able to:

CO-01	List the types of measurement and common errors that occur while using electronic measuring systems and demonstrate use of statistical analysis to validate specific output from measuring and testing equipment.
CO-02	Explain the standards used for calibration and demonstrate calibration of a measuring and/or
	testing equipment to ensure it provides reliable output.
CO-03	Select an appropriate sensor or transducer for a given application and demonstrate its use to
	measure and record the readings for a given project.
CO-04	Test a given lab equipment, identify the reasons for error, troubleshoot or calibrate to ensure
	the equipment provides the correct reading

#### 3. Course Content

We	C	PO	Lecture (Knowledge Criteria)	Tutorial (Activity Criteria)	Practice (Performance Criteria)
ek	0		3 hours/week	1 hour/week	4 hours/ (2 hours/batch twice in a week)
1.	1	1,4 ,6	1. Necessity of measurements-direct and indirect methods, Static characteristics of an instrument.		1. Find the static characteristics of analog voltmeter/ multimeter.
			2. Dynamic characteristics of an instrument. Generalized electronic measurement system-Block diagram.	Refer Table 1	2. Find the dynamic characteristics of analog voltmeter/multimeter.
			3. Errors–classification of errors, sources of errors.		voitilieter/illuitililieter.
2	1, 2, 4	1,4 ,5, 7	1. Statistical analysis- arithmetic mean, deviation, average deviation, standard deviation, probability of errors and limiting errors.	Refer Table 1	1. Identify the errors and do the calibration for setting up an analog multimeter before
			2. Problems on Statistical analysis.		performing measurement.

	1	1		1	
			3. Calibration, Error check, understand specification sheet of digital multimeter.		2. Troubleshoot and service the Digital trainer kits.
3.	1, 2, 4	1,4 ,5	1. Standards-primary, secondary, working and IEEE standards.		1. Build a Wheatstone bridge to find unknown resistance.
			2. Bridges- Comparison of AC and DC bridges. Applications of AC and DC bridges.	Refer Table 1	2. Construct a circuit to
			3. Wheatstone bridge-Explanation and applications.		measure AC voltage by voltage divider method.
4	2, 3	1,2 ,3, 4,6	Electrical Transducers- necessity, selection, classification- active and passive, analog and digital, primary and secondary.	Refer Table 1	1. Video demonstration and documentation on multi-function meter used for measuring any electrical parameter.
			<ul><li>2. Strain gauge-principle, gauge factor, features of bonded, unbonded, foil type strain gauges.</li><li>3. Load Cell, capacitive transducer-principle &amp; features.</li></ul>	NOTOR TRUBE I	2. Calibrate a load cell to measure the weight of any object. Use suitable components and/or programming to
	2,	3,4	1. Hall effect transducers, LVDT,		accomplish the task.  1. Build a temperature
5	3	,5, 7	thermistor.	Refer Table 1	sensor circuit using a thermistor.
			Thermocouple, piezoelectric transducers, position sensors.      Proximity sensors, digital optical encoders & PIR sensors.	Refer Table 1	2. Build a simple application using position/proximity
6	1, 2	1,4	1. PMMC meters- principle, DC ammeters and multi range ammeters.		sensor.  1. Construct a circuit to verify KVL and measure voltages using analog
			2. DC voltmeters using PMMC, multi range voltmeters, loading effect and voltmeter sensitivity.	Refer Table 1	voltages using analog voltmeter.  2. Construct a circuit to
			3. Electrodynamometer -principle, ammeter, voltmeter.		verify KCL and measure currents using analog ammeter.
7	1, 2	1,4 ,5, 7	1. Electronic voltmeter- Chopper amplifier type voltmeter.		1. Study of Regulated DC power supply and measurement of standard voltages at
			2. AC voltmeter- full-wave rectifier, Peak responding and true RMS voltmeters.	Refer Table 1	various stages of RPS.  2. Identify and rectify the
			3. Ohmmeters- series and shunt type, concept of calibration of meters.		various faults in the Regulated DC power supply.

	1	1 2			1 Video demonstration
8	1,	1,2 ,4, 5	<ol> <li>Digital instruments –Introduction,</li> <li>Ramp type DVM.</li> <li>Automatization in digital meters-</li> </ol>	Refer Table 1	1. Video demonstration and documentation on testing life cycle of electrical loads using Electronic Counter.
			automatic polarity indication, automatic decimal point positioning, automatic ranging and zeroing.		2. Troubleshoot and perform minor repair practices on Decade
			3. Electronic counters-block diagram.		Boxes (Rotary switches, connectors, components connectivity etc).
9	1, 2, 4	1,4 ,5, 6	1. Digital frequency meter, Time interval measurement.		1. Calibrate LCR meter and perform measurement of Resistance, capacitance,
			2. Digital LCR meter, digital multimeter.	Refer Table 1	and inductance and verify with actual value.
			3. Microprocessor based instruments, IEEE 488 GPIB instruments.		2. Troubleshoot and rectify any analog circuit using simulation software (Multisim)
10	1, 2, 4	1,4 ,5,	1. Cathode Ray Oscilloscope-block diagram, working of CRT.		1. Study the front panel controls of CRO and do
		Ü	2. Dual trace CRO, CRO probes, applications of CRO.	Refer Table 1	its calibration  2. Demonstrate the use of CRO to measure phase difference between two
			3. DSO-block diagram, features, Sampling oscilloscope.		waveforms and obtain the lissajous patterns.
11	1, 2, 4	1,4 ,7	1. Function generator- block diagram and applications.		1. Demonstrate the analysis of different waveforms (amplitude, phase, frequency) from a function generator using
			2. Standard RF signal generator, sweep frequency generator.	Refer Table 1	CRO.
			3. Harmonic distortion, harmonic analyzing instruments.		2. Demonstration and documentation on the working of a spectrum analyser. (Video/simulator)
12	1, 4	1,5 ,7	1. Electrical grounding and shielding- concept, interference, shielding of cabinets.		1. Do it yourself (DIY) a probe and use the probe
			2. Precautions to prevent instrument damage, general precautions for instrument safety.	Refer Table 1	to test the circuit continuity in PCB.
			3. Testing and troubleshooting- introduction, generalized troubleshooting.		

13	1, 2, 4	1,4 ,5, 7	<ol> <li>Precautions to be taken to achieve personnel safety during servicing.</li> <li>Testing Techniques, electronic repair tools.</li> <li>Explain Basic steps of electronic equipment service and maintenance.</li> <li>Study of basic procedure of service and maintenance</li> <li>Circuit tracing techniques</li> </ol>	Refer Table 1	1. Do it yourself (DIY) an antistatic wrist strap useful to handle electronic component.
Total in hours			39	13	52

Note: 1) In Practice sessions Video demonstration should be followed by MCQs/Quiz/Subjective questions and the evaluation has to be documented.

2) In Practice sessions, all discrete circuits should be simulated using suitable software before its construction and verification.

**TABLE 1: Suggested activities for tutorials** 

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week No.	Suggested activities for tutorials
01	<ol> <li>Give a presentation on ways of reducing systematic and random errors.</li> <li>List the basic tools (electronic repair tools) required for servicing electronic equipment and their purpose.</li> </ol>
02	<ol> <li>Present the specification sheets of voltmeter/ammeter/ohmmeter.</li> <li>Demonstrate the procedure to calibrate DC power supply.</li> </ol>
03	Identify the faults in Digital ICs and Troubleshoot using digital IC tester/ Logic Probe     Prepare a report on IEEE standards.
04	Give a presentation on applications of strain gauge and explain any one.     Prepare a report on advantages and disadvantages of capacitive transducers.
05	<ol> <li>Write a report on various kinds of transducers used in Biomedical Instrumentation.</li> <li>Demonstrate the application of any sensor.</li> </ol>

06	<ol> <li>Give a presentation on KVL and KCL and use of voltmeter and ammeter in taking readings.</li> <li>Solve problems on extending range in ammeter and voltmeter.</li> </ol>
07	Give a presentation on comparison of commercially available electronic voltmeters.      Prepare a report on calibration of meters.
08	Discuss pros and cons of Digital instruments.     Prepare a report on comparison of analog and digital instruments.
09	<ol> <li>Give a presentation on performance testing on digital multimeters.</li> <li>Collect and present service manuals of measuring instruments.</li> <li>Present the applications of IEEE 488 GPIB instruments.</li> </ol>
10	<ol> <li>Give a presentation on applications of CRO.</li> <li>Give a presentation on the Technical specification of CRO.</li> <li>Collect and present the specifications of DSO.</li> </ol>
11	1.Suggest cost-quality effective of any 4 measuring instruments by preparing comparative statements containing function, specification, make, market-price, and warranty     2. Collect and present the specifications of signal generator.
12	<ol> <li>Prepare a report on the grounding and shielding of any lab equipment (ex. oscilloscope). Also present the consequences if not done so.</li> <li>Do minor repair practices on Decade Boxes (Rotary switches, connectors, components connectivity etc.,)</li> </ol>
13	<ol> <li>Study the latest technological changes in this course and present the impact of these changes on industry.</li> <li>Discuss about Trouble shooting chart.</li> </ol>

#### **LINKS**

- 1. <a href="https://www.webassign.net/labsgraceperiod/ncsuplseem2/lab\_1/manual.html">https://www.webassign.net/labsgraceperiod/ncsuplseem2/lab\_1/manual.html</a>
- 2. <a href="https://youtu.be/i4sI dBWH50">https://youtu.be/i4sI dBWH50</a>
- 3. <a href="https://blog.matric.com/pcb-testing-methods">https://blog.matric.com/pcb-testing-methods</a>
- 4. <a href="https://www.youtube.com/watch?v=AUTcWsR6pwU">https://www.youtube.com/watch?v=AUTcWsR6pwU</a>
- 5. <a href="https://www.youtube.com/watch?v=x4B6">https://www.youtube.com/watch?v=x4B6</a> 1C4gEQ
- 6. <a href="https://www.youtube.com/watch?v=-0Pre73mp7A">https://www.youtube.com/watch?v=-0Pre73mp7A</a>
- 7. <a href="https://www.youtube.com/watch?v=lgvCMd5nMw4">https://www.youtube.com/watch?v=lgvCMd5nMw4</a>
- 8. <a href="https://www.youtube.com/watch?v=Evw5AqUYJcg">https://www.youtube.com/watch?v=Evw5AqUYJcg</a>
- 9. <a href="https://www.youtube.com/watch?v=yasajLJUYvg">https://www.youtube.com/watch?v=yasajLJUYvg</a>

# 4. CIE and SEE Assessment Methodologies

Sl. No	Assessment	Test Week	<b>Duration</b> In minutes	Max marks	Conversion
1.	CIE-1 Written Test	5	80	30	Average of three
2.	CIE-2 Written Test	9	80	30	tests
3	CIE-3 Written Test	13	80	30	30
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two
5	CIE-5 Skill Test-Practice	12	180	100	skill tests 20
6	CIE-6 Portfolio continuous evaluation of Activity through Rubrics	1-13		10	10
		60			
Seme	ester End Examination (Practice)		180	100	40
		100			

## 5. Format for CIE (1, 2, 3) Written Test

Course Name		Electronics Measurement and Testing Techniques	Test	I/II/III	Sem	III/IV
Course Code		20EC34P	Duratio	80 Min	Marks	30
Note: Answ	wer a	ny one full question from each section. Ea	n ach full que	l estion carries	10 marks.	
Section	Assessment Questions			Cognitive	Course	Marks
Section	ЛЗЗ	essment Questions	Levels	Outcome	Maiks	
T	1					
1	2					
II	3					
II	4					
III	5	5				
	6					

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional Questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

#### 5. (a) Format for CIE-4 Skill Test - Practice

SL. No.	COs	Particulars/Dimension	Marks
1	1	Conduct an experiment on characteristics/ Calibration/ Bridge Writing circuit -20 Marks Conduction -20 Marks Result -10 Marks	50
2	3	Conduct an experiment on Sensor/Transducer	40
3	1,3	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

5. (b) Format for CIE-5 Skill Test - Practice

SL. No.	COs	Particulars/Dimension	Marks
1	2	Conduct experiment on CRO/ Measurement of L C R	40
2	4	Conduct an experiment on Troubleshooting RPS/ Repair of Decade Boxes	50
3	2,4	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl.	Dimensio	Beginner	Intermediat	Good	Advanced	Expert	Students
No.	n		e				Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
	Average Marks= (8+6+2+2)/4=4.5						5

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description
1	Electronic Measurements and Instrumentation -2nd Revised Edition, R. K. Rajput, ISBN: 81- 219- 2917-2 234.
2	Electronic Measurements and Instrumentation-3rd Edition, Sanjay Talbar & Akhilesh Upadhayaya, ISBN :81-874-3335-3
3	Electronic Instrumentation -3rdEdition, Kalsi H. S., ISBN: 00-707-0206-3
4	Modern Electronic Instrumentation and Measurement Techniques-2nd Edition, Albert Helfrick & William Cooper, ISBN:81-203-0752-6

## 8. SEE Scheme of Evaluation

SL. No.	COs	Particulars/Dimension	Marks
1	1,2	Identify errors, calibrate and perform measurement using analog multimeter/Wheatstone bridge/LCR meter/CRO	30
2	3	Identify the Sensor/Transducer used in different applications.	10
3	4	Conduct an experiment on Troubleshooting and repair of DTK/ RPS/ Decade Boxes	40
4	1,2,3,4	Viva-Voce	20
		Total Marks	100

9. Equipment/software list with Specification for a batch of 20 students

Sl. No.	Particulars	Specification	Quantity
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	MATLAB/Multisim/PSPICE/Electronic Workbench Simulation Software		
3	Dual trace oscilloscope	20-30MHz	20
4	LCR meter		10
5	Multi function meter		5
6	Resistors, Capacitors, Inductors ,Thermistor		Consumables as required
7	Digital multimeter		10
8	Analog multimeter		10
9	Function generator		5
10	Position, and Proximity sensors		10 each
11	Transducer		5
12	Load cell		5
13	Tool kit		2 sets
14	Soldering set		10 sets

# ಮೂರನೇ ಸೆಮಿಸ್ಟರ್

# ಕನ್ನಡ ಬಲ್ಲ ಡಿಪ್ಲೋಮಾ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ನಿಗದಿಪಡಿಸಿದ ಪಠ್ಯಕ್ರಮ

(ಕನ್ನಡ ಭಾಷೆ, ಸಾಹಿತ್ಯ, ಸಂಸ್ಕೃತಿ ಮತ್ತು ಪರಂಪರೆ ಕುರಿತು)

Course Code	20KA31T	Semester : III	Course Group - AU
Course Title	ಸಾಹಿತ್ಯ ಸಿಂಚನ - 2	Category : Audit	Lecture Course
No. of Credits	2	Type of Course	CIE Marks : 50
Total Contact Hours	02 Hrs Per Week 26 Hrs Per Semester	Prerequisites Teaching Scheme (L:T:P)= 2:0:0	SEE Marks : Nil

# ಸಾಹಿತ್ಯ ಸಿಂಚನ – 2 ಪಠ್ಯಕ್ರಮ - 20KA31T

#### 26 ಗಂಟೆಗಳು

ಪಠ್ಯಕ್ರಮದ ಪರಿವಿಡಿ	ಬೋಧನಾ ಅವಧಿ
<ol> <li>ಹೊಸಗನ್ನಡ ಸಾಹಿತ್ಯ ಚರಿತ್ರೆಯ ಪ್ರಭಾವಗಳು ಮತ್ತು ಪ್ರೇರಣೆಗಳು</li> </ol>	01 ಗಂಟೆ
2. ಹೊಸಗನ್ನಡ ಕಾವ್ಯದ ಪ್ರಕಾರಗಳು -	02 ಗಂಟೆ
<ul> <li>ನವೋದಯ ಸಾಹಿತ್ಯ - ಲಕ್ಷಣಗಳು ಮತ್ತು ಪ್ರೇರಣೆ, ಪ್ರಮುಖ ಕವಿಗಳು ಮತ್ತು ಸಾಹಿತ್ಯದ ಕೊಡುಗೆಗಳು.</li> <li>ನವ್ಯ ಸಾಹಿತ್ಯ - ಲಕ್ಷಣಗಳು ಮತ್ತು ಪ್ರೇರಣೆ, ಪ್ರಮುಖ ಕವಿಗಳು ಮತ್ತು ಸಾಹಿತ್ಯದ ಕೊಡುಗೆಗಳು.</li> </ul>	03 ಗಂಟೆ 03 ಗಂಟೆ
<ul> <li>ಬಂಡಾಯ ಮತ್ತು ಪ್ರಗತಿಪರ ಸಾಹಿತ್ಯ - ಲಕ್ಷಣಗಳು ಮತ್ತು ಪ್ರೇರಣೆ, ಪ್ರಮುಖ ಕವಿಗಳು ಮತ್ತು ಸಾಹಿತ್ಯದ ಕೊಡುಗೆಗಳು.</li> <li>ದಲಿತ ಸಾಹಿತ್ಯ, ಮಹಿಳಾ ಸಾಹಿತ್ಯ, ವಿಜ್ಞಾನ ಸಾಹಿತ್ಯ ಮತ್ತು ಇತ್ತೀಚಿನ ಪ್ರಚಲಿತ ಕನ್ನಡ ಸಾಹಿತ್ಯ - ಲಕ್ಷಣಗಳು ಮತ್ತು ಪ್ರೇರಣೆ, ಪ್ರಮುಖ ಕವಿಗಳು ಮತ್ತು ಸಾಹಿತ್ಯದ ಕೊಡುಗೆಗಳು.</li> </ul>	03 ಗಂಟೆ 03 ಗಂಟೆ
3. ವೈಚಾರಿಕತೆ ಕುರಿತಾದ ಲೇಖನ - <b>ಜಿ ಎಸ್. ಶಿವರುದ್ರಪ್ಪ</b>	01 ಗಂಟೆ
4. ಕಥೆ - <b>ನೇಮಿಚಂದ್ರ</b>	01 ಗಂಟೆ
5. ಪ್ರವಾಸ ಕಥನ - <b>ಹಿ.ಚಿ.ಬೋರಲಿಂಗಯ್ಯ</b> ರವರ (ಕುಪ್ಪಳ್ಳಿ ಡೈರಿ ಪುಸ್ತಕದಿಂದ)	01 ಗಂಟೆ
6. ಪರಿಸರ, ವಿಜ್ಞಾನ ಮತ್ತು ತಂತ್ರಜ್ಞಾನ ಕುರಿತಾದ ಲೇಖನಗಳು	01 ಗಂಟೆ
7. ಪ್ರಬಂಧ - <b>ಗೊರೂರು ರಾಮಸ್ವಾಮಿ ಅಯ್ಯಂಗಾರ</b>	01 ಗಂಟೆ
8. ಪ್ರಚಲಿತ ವಿದ್ಯಮಾನಕ್ಕೆ ಸಂಬಂಧಿಸಿದ ಲೇಖನ - "ಪೇರು ಮಾರುಕಟ್ಟೆ ಮತ್ತು ಹಣಕಾಸು ನಿರ್ವಹಣೆ" ಕುರಿತಂತೆ	01 ಗಂಟೆ
9. ಕರ್ನಾಟಕ ಏಕೀಕರಣ ಚಳುವಳಿ - <b>ಪ್ರೊ. ಜಿ. ವೆಂಕಟಸುಬ್ಬಯ್ಯ</b>	01 ಗಂಟೆ
10. ಕನ್ನಡ ಸಿನಿಮಾರಂಗ ಬೆಳೆದು ಬಂದ ದಾರಿ ಮತ್ತು ನಾಡು-ನುಡಿ ಹಾಗೂ ನಾಡಿನ ಸಂಸ್ಕೃತಿಯ ಮೇಲೆ ಬೀರಿದ ಪ್ರಭಾವಗಳು	01 ಗಂಟೆ
11. ಕನ್ನಡದ ಸಾಮಾಜಿಕ ಉಪಭಾಷೆಗಳು (ಭಾಪಾ ಪ್ರಭೇದಗಳು)	01 ಗಂಟೆ
12. ಆಧುನಿಕ ಕನ್ನಡ ಸಾಹಿತ್ಯ ಚರಿತ್ರೆಯ ಒಂದು ಅವಲೋಕನ	02 ಗಂಟೆ
ಒಟ್ಟು ಬೋಧನಾ ಅವಧಿ 26 ಗಂಟೆಗಳು	26 ಗಂಟೆ

# ಕನ್ನಡ ಬಾರದ / ಕನ್ನಡೇತರ ಡಿಪ್ಲೋಮಾ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಕನ್ನಡ ಕಲಿಸಲು ನಿಗದಿಪಡಿಸಿದ ಪಠ್ಯಕ್ರಮ

Course Code	20KA31T	Semester : III	Course Group - AU
Course Title	ಬಳಕೆ ಕನ್ನಡ – 2	Category : Audit	Lecture Course
No. of Credits	2	Type of Course	CIE Marks : 50
<b>Total Contact Hours</b>	2 Hrs Per Week	Prerequisites Teaching	SEE Marks : Nil
	26Hrs Per Semester	Scheme (L:T:P)= 2:0:0	

# ಬಳಕೆ ಕನ್ನಡ – 2 ಪಠ್ಯಕ್ರಮ - 20KA31T

# Table of Contents (ಪರಿವಿಡಿ)

#### 26 ಗಂಟೆಗಳು

Part - 1	Teaching Hour
Necessity of learning a local language (Continuation).	
Tips to learn the language with easy methods (Continuation).	
Easy learning of a Kannada Language: A few tips (Continuation).	
Hints for correct and polite conversation (Continuation).	01 Hour
Instructions to Teachers for Listening and Speaking Activities (Continuation).	
Instructions to Teachers for Reading and Writing Activities (Continuation).	
Part – II	
Key to Transcription for Correct Pronunciation of Kannada Language (Continuation).	02 Hour
Instructions to Teachers to teach Kannada Language (Continuation).	
Part – III Lessons to teach Kannada Language	
(Speaking, Listening, Reading and Writing Activities with Explanation	1)
Lesson – 1 Personal Pronouns, Possessive Forms, Interrogative words – Part II	02 Hour
Lesson – 2 Permission, Commands, encouraging and Urging words (Imperative words and sentences) – Part II	02 Hour
Lesson - 3 Comparative, Relationship, Identification and Negation Words - Part II	02 Hour
Lesson - 4 Different types of forms of Tense (Use and Usage of Tense in Kannada) - Part II	02 Hour
Lesson - 5 Kannada Helping Verbs in Conversation (Use and Usage of Verbs) - Part II	02 Hour
Lesson - 6 Formation of Past, Future and Present Tense Sentences with Changing Verb Forms	02 Hour
Lesson – 7 Karnataka State and General Information about the State	02 Hour
Lesson – 8 Kannada Language and Literature	02 Hour
Lesson – 9 Do's and Don'ts in Learning a Language	02 Hour
PART - IV Reading and writing Practice of Kannada Language	
Lesson - 10 Kannada Language Script Part - 1	02 Hour
Lesson - 11 Kannada Language Script Part - II (Continuation)	02 Hour
Lesson - 12 Kannada Vocabulary List : ಸಂಭಾಷಣೆಯಲ್ಲಿ ದಿನೋಪಯೋಗಿ ಕನ್ನಡ	01 Hour
ಪದಗಳು - Kannada Words in Conversation (Continuation).	
Total Teaching Hours	26 Hou

# ಸಾಹಿತ್ಯ ಸಿಂಚನ ಭಾಗ - II ಮತ್ತು ಬಳಕೆ ಕನ್ನಡ ಭಾಗ - II ಈ ಎರಡು ಪಠ್ಯಕ್ರಮಗಳಿಗೆ CIE - ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ ಮಾರ್ಗಸೂಚಿಗಳು :

## (Course Assessment and Evaluation Chart - CIE only)

Sl. No	Assessment	Туре	Time frame in semester	<b>Duration</b> In minutes	Max marks	Conversion	
1.	CIE- Assessment - 1	Written Test - 1	At the end of 3 <sup>rd</sup> week	80	30	Average of three written	
2.	CIE- Assessment - 2	Written Test - 2	At the end of 7 <sup>th</sup> week	80	30	tests: 1, 2 & 3 for 30 Marks	
3	CIE- Assessment - 3	Written Test - 3	At the end of 13 <sup>th</sup> week	80	30		
4.	CIE- Assessment - 4	MCQ/Quiz	At the end of 5 <sup>th</sup> week	60	20	Average of three	
5	CIE- Assessment - 5	Open Book Test	At the end of 9 <sup>th</sup> week	60	20	Assessment tests: 4,5 & 6 for 20 Marks	
6	CIE- Assessment - 6	Work book Consolidation & Activities	At the end of 11 <sup>th</sup> week	60 (Work book Submission)	20	IOI ZO MAINS	
	Total CIE - Continuous Internal Evaluation Assessment Marks						
Total Marks						50	

**ಸೂಚನೆ:** 1.CIE - ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ 1, 2 ಮತ್ತು 3 ರ ಕಿರು ಪರೀಕ್ಷೆಗಳನ್ನು ಮತ್ತು ಮೌಲ್ಯಮಾಪನದ 4, 5 ಮತ್ತು 6 ರ ಪರೀಕ್ಷೆಗಳನ್ನು ಪ್ರತ್ಯೇಕ ಬ್ಲೂಬುಕ್ ಪುಸ್ತಕದಲ್ಲಿ ವಿದ್ಯಾರ್ಥಿಗಳು ಬರೆಯಬೇಕು.

<sup>2.</sup>ಸಮಿಸ್ಟರ್ ಅಂತ್ಯದಲ್ಲಿ ವಿದ್ಯಾರ್ಥಿಗಳು, ತರಗತಿ ಕನ್ನಡ ಭಾಪಾ ಶಿಕ್ಷಕರಿಂದ ಮತ್ತು ವಿಭಾಗಾಧಿಕಾರಿ ಗಳಿಂದ ದೃಢೀಕರಣಗೊಂಡ ಕಾರ್ಯಪಠ್ಯಪುಸ್ತಕವನ್ನು (Work Book) ಮೌಲ್ಯಮಾಪನ ಭಾಗ-CIE- Assessment – 6 ರ ಪರೀಕ್ಷೆಯ ನಂತರ ಆಯಾ ವಿಭಾಗಕ್ಕೆ ಸಲ್ಲಿಸಬೇಕು.

# **4**<sup>TH</sup> **SEMESTER**



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics & Communication	Semester	IV
Course Code	20EC41P	Type of Course	Programme Core
Course Name	PCB Design & Fabrication	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

#### 1. Rationale

Printed Circuit Boards (PCBs) are the core component in almost all the electronic gadgets used either for domestic or industrial purposes. PCBs hold almost all electronic components necessary for a device to function. Using a PCB has many advantages such as compact design, ease of testing and repair, low noise and interference, and improved reliability. Apart from electrically connecting, it also gives mechanical support to the electrical components. Using PCBs, a highly complicated circuit can be designed in a very small package which helps in reducing the size of electronic devices.

PCB design can be done either manually or using software. Electronic design automation tools are software tools used for designing the schematic and layout of PCB. Large number of PCBs can be fabricated at the same time after the layout is designed once. With consumers pushing for slimmer and faster devices, and with industries seeking improved functionality, the PCB will continue to develop in the future.

#### **2. Course Outcomes:** On successful completion of the course, the students will be able to:

CO-1	Identify different types of Printed Circuit Board (PCB), list the differences between them and its adequacy for specific application.
CO -2	Draw the schematic and PCB layout for an analog circuit to be used for a given application.
CO-3	Select the right components for a designed circuit, build the circuit and fabricate it using the appropriate tools following all necessary safety protocols.
CO-4	Test the fabricated circuit, identify the problem and troubleshoot to ensure the circuit provides the desired output.

## 3. Course Content

	CO	PO	Lecture	Tutorial (Activity	Practice
Week			(Knowledge Criteria)	Criteria)	(Performance Criteria)
			3 hours/week	1 hour/week	4 hours/week (2 hours/batch twice in a week)
1	1	1	1. Introduction to PCB, need and evolution of PCBs.		Familiarization of any Electronic design
			2. Classes of PCB – First Class (RF, microwave, and analog PCB) & Second Class (digital based PCB)	Refer Table 1	automation (EDA) software -Open source EDA Tool KiCad.
			(digital based PCB) – characteristics.		2. Practice the PCB design steps for a simple analog
			3. Types of PCB - Single sided, double sided and multilayer PCBs, rigid and flexible PCBs.		circuit: Schematic design- Familiarization of schematic editor, schematic creation, annotation, electrical rule check, mapping of components, netlist generation.
2	1,2	1	1. Comparison between single layer, double layer and multilayer PCBs.		1. Practice placement of components.
			2. Importance of grounding in PCBs, impedance matching, reflection, ground bounce, SSN.	Refer Table 1	2. Practice the routing (normal tracks -10 mils and power tracks-50 mils).
			3. Materials used for multilayer PCBs, PCB thickness, aspect ratio.		
3	1,2	1,2	1. Component package types - Through-Hole, Surface- Mount, Fine Pitch, FPGA,		1. Learn how to create symbols for diodes, transistors, connectors, ICs.
			QFT, TFP, BGA, Press Fit.  2. Calculation of track width required for different types of packages.	Refer Table 1	2. Create the footprint for diodes, transistors, connectors,ICs.
			3. Types of Planes in PCB.		
4	2	2,3,4	1. Design for manufacturability (DFM).		1. Design Schematic for Regulated Power supply.
			2. Electromagnetic Interference (EMI), Electromagnetic Compatibility (EMC).	Refer Table 1	2. Design PCB layout for Regulated Power supply.
			3. Thermal issues in PCB		

5	2	2,3,4	<ol> <li>Conduction, convection, radiation in thermal issues.</li> <li>Heat Dissipation in PCB, Heat sinks.</li> <li>RF PCB-introduction.</li> </ol>	Refer Table 1	<ol> <li>Design Schematic for inverting /summing amplifier using op-amp.</li> <li>Design PCB layout for inverting /summing amplifier using op-amp.</li> </ol>
6	2	2,3,4	<ol> <li>High-speed digital basics.</li> <li>General design factor for digital and analog PCBs.</li> <li>Voltage and current considerations in PCBs.</li> </ol>	Refer Table 1	<ol> <li>Design Schematic for astable multivibrator using IC 555.</li> <li>Design PCB layout for astable multivibrator using IC 555.</li> </ol>
7	2	2,3,4	<ol> <li>Transmission lines, significance of transmission line and its effects.</li> <li>Types of Transmission lines.</li> <li>Different types of termination techniques, simple problems.</li> </ol>	Refer Table 1	<ol> <li>Design Schematic for RC coupled amplifier.</li> <li>Design PCB layout for RC coupled amplifier.</li> </ol>
8	2	2,3,4	1. Crosstalk in transmission lines, minimization of crosstalk.  2.ENIG and ENEPIG.  3. Noise budget.	Refer Table 1	1. Design Schematic for a given circuit (Ex: proximity sensor/ LED blinking/+ or -12v power supply using 7812 IC and 7912 IC)  2. Design PCB layout for a given ckt (Ex:proximity sensor/LED blinking circuit/+or - 12v power supply using 7812 and 7912 IC)
9	2,3	3,4,,5	<ol> <li>Preparation of Manufacturing Drawing (MD).</li> <li>Importance of Solder mask, assembly drawing, silkscreen, Gerber file.</li> <li>Board origin, component origin, importance of origin.</li> </ol>	Refer Table 1	1.Familiarisation of copper clad sheet, drilling machine, drill bits, required chemicals .(links)  2. Generate the Gerber file of works done in weeks 4 - 8 and take printouts on glossy paper.

10	2,3,4	4,5	1. Importance of CNC		
			machines. CNC machines for component pick and place, drill file.		
			2. Design for Testing(DFT)	Refer Table 1	Fabrication process.
			3. Design specification standards.		
11	2,3,4	7	1. Steps involved in fabrication of single side PCB.		
			2. Steps involved in fabrication of double sided PCB.	Refer Table 1	Fabrication process.
			3. Steps involved in fabrication of multilayer PCB.		
12	2,3,4	7	1. Steps involved in fabrication of multilayer PCB- continued.	Refer Table 1	Fabrication process.
			2. Soldering techniques.	Refer fable 1	rabification process.
10	0.0.1	_	3. Testing of PCB.		
13	2,3,4	7	1. Importance of RoHS (Restriction of use of Hazardous Substances).		
			2. Waste management of hazardous materials in PCB.	Refer Table 1	Fabrication process.
			3. Environment Management Standards (EMS).		
Total i	n hour	5	39	13	52

#### **TABLE 1: Suggested activities for tutorials.**

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week no.	Suggested activities for tutorials			
01	1. Prepare a report on reference designators for components used in PCB.			
	2. Give a presentation on general guidelines for designing the PCB.			
	3. Prepare a report on comparison of different types of PCBs.			

02	1. Collect information on different electronic design automation (EDA) tools and their comparison
02	(Cadstar, Orcad, Pads).
	2. Collect the information on materials used for multilayer PCB, drill holes, vias, aspect ratio and present it.
03	1. Collect the datasheets of electronic components such as diode, regulator IC, DIP IC and study their mechanical dimension and their projection (first angle/ third angle projection, top view, front view).
	2. Give a presentation on through-hole and surface-mount technology.
04	1. Collect a case study on DFM issues and present it.
04	2. Give a presentation on Electromagnetic Interference in real life and provide solution to solve the problem.
05	1. Collect details of different types of heat sinks used in PCBs.
05	2. Collect any frequency synthesizer circuit and explain it.
0.6	1. Prepare a report on comparison of analog and digital PCBs and present it.
06	2. Give a presentation on the importance of spacing and thickness of the tracks in PCB's.
07	1. Give a presentation on the importance of transmission lines.
07	2. Discuss selection of transmission lines for optimum design.
08	1. Write a report on how to analyse the presence of crosstalk in signals.
	2. Give a presentation on advantages and disadvantages of ENIG and ENEPIG.
00	1. Collect information on different types of solder paste.
09	2. Collect any completed PCB file and explain it.
10	1. Prepare a report on comparison of manual soldering and machine soldering and present it.
10	2.Collect information on design standards used in India for designing PCBs.
	1. Collect different types of manufacturing techniques and explain them.
11	2. What are the different parameters to be considered to decide the cost of manufactured PCB?
	1. Give a presentation on the failures of PCB due to improper soldering.
12	2. Write a report on testing of PCB.

- 1. Study the latest technological changes in this course and present the impact of these changes on industry.
  - 2. Give a presentation on the role of students for protecting environment from hazardous materials.
  - 3. Find different methods for disposing of PCB lab wastes and dispose it.

#### 4. CIE and SEE Assessment Methodologies

Sl.	Assessment	Test Week	Duration	Max	Conversion
No			In minutes	marks	
1.	CIE-1 Written Test	5	80	30	Average of three
2.	CIE-2 Written Test	9	80	30	tests
3	CIE-3 Written Test	13	80	30	30
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two skill
5	CIE-5 Skill Test-Practice	12	180	100	tests
					20
6	CIE-6 Portfolio continuous	1-13		10	10
	evaluation of Activity through				
	Rubrics				
		60			
	Semester End Examination	40			
		100			

#### 5. Format for CIE (1, 2, 3) Written Test

Course Nar	ne	PCB Design & Fabrication	Test	I/II/III	Sem	III/IV	
Course Code		20EC41P	Duration	80 Min	Marks	30	
Note: Ansv	<b>Note:</b> Answer any one full question from each section. Each full question carries 10 marks.						
Section		Assessment Questions		Cognitive Levels	Course Outcome	Marks	
I	1						
	2						
II	3						
	4						
III	5						
	6						

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

#### 5. (a) Format for CIE-4 Skill Test - Practice.

SL. No.	COs	Particulars/Dimension	Marks
1	1	Identification of different types of PCB.	10
2	2	Schematic Design of the given Analog Circuit using EDA tool(KiCad)	40
3	2	Layout Design of the given Analog Circuit using EDA tool (KiCad)	40
4	1,2	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

# 5. (b) Format for CIE-5 Skill Test - Practice.

SL.	COs	Particulars/Dimension		Marks
No.				
1	2	Design of the given Analog Circuit		
		Schematic Design -5 Marks		10
		Layout Design -5 Marks		
2	3	Fabrication of the given Analog Circuit		
		Fabrication -30 Marks		50
		Component mounting & soldering -20 Marks		
3	4	Testing & Troubleshooting of a given PCB.		
		Testing - 10 Marks		30
		Troubleshooting - 20 Marks		
4	2,3,4	Portfolio evaluation of Practice sessions through Rubrics		
				10
		Total	Marks	100

## 6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl.	Dimension	Beginner	Intermediate	Good	Advanced	Expert	Students
No.							Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
	Average Marks= (8+6+2+2)/4=4.5					5	

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description		
1	Printed Circuits Handbook - 6th edition Clyde F. Coombs,Jr.		
2	PCB Design & Technology - Walter C. Bosshart		
3	Printed Circuit Board by RS Khandpur, Tata McGraw Hill Education Pvt Ltd., New Delhi		
4	Electronic Product Design Volume-I by S D Mehta, S Chand Publications		
5.	Open source EDA Tool KiCad Tutorial: http://kicad-pcb.org/help/tutorials/		
6	PCB Fabrication user guide page:		
	http://www.wikihow.com/Create-Printed-Circuit-Boards		
	http://www.siongboon.com/projects/2005-09-07 home pcb fabrication		
	http://reprap.org/wiki/MakePCBInstructions#Making PCBs yourself		

## 8. SEE Scheme of Evaluation

SL. No.	COs	Particulars/Dimension	Marks
1	1	Identification of Types of PCB.	10
2	2	Design of the given Analog Circuit Schematic Design Layout Design Routing -15 Marks -10 Marks	40
3	3	Fabrication of the given Analog Circuit Fabrication -10 Marks Component mounting & soldering -10 Marks	20
4	4	Testing & Troubleshooting of PCB.	10
5	1,2,3, 4	Viva Voce	20
	•	Total Marks	100

# $9. \ Equipment/software\ list\ with\ Specification\ for\ a\ batch\ of\ 20\ students$

Sl.	Particulars	Specification	Quantity
No.			
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	Open source EDA Tool KiCad.		20
3	Single-sided copper clad sheet.		100
4	Diluted Acidic solution for copper etching purpose with plastic tray.		5
5	Tapes and pads for layout design of different dimensions.		
6	Glossy paper		60
7	Hand drilling/Power drilling machine.		10
8	Tool kit (Tray, Brush, PCB Laminate, tong, hand gloves etc.)		20



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics & Communication	Semester	IV
Course Code	20EC42P	Type of Course	Programme Core
Course Name	Wireless Communication	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

#### 1. Rationale

The purpose of wireless communication is to communicate messages over distances without the use of wires. It includes an exposure to microwave engineering, radar systems, cellular and satellite communication. In the microwave industry, job opportunities are available in assembly, production, installation, repair and maintenance of microwave transmitters and receivers. The knowledge of radar systems allows opportunities with civil and defence organizations dealing with aircraft and shipping. Satellite communication is used to relay signals around the curvature of Earth allowing communication between widely separated points. Mobile communication is a fast changing technology which offers voice and data connectivity between individuals.

#### **2. Course Outcomes:** On successful completion of the course, the students will be able to:

CO-1	Identify the types of wireless communications, list differences and its applications.
CO-2	Identify the components of a given wireless communication system, explain the role of those components in the system and list their characteristics.
CO-3	Build a working model of a wireless communication system to be used for a specific application.
CO-4	Test a given set top Box / mobile phone, identify the problem and troubleshoot to ensure the device is fully functional.

			Lecture	Tutorial	Practice
We			(Knowledge Criteria)	(Activity	(Performance Criteria)
ek	CO	PO		Criteria)	
CK			3 hours/week	1 hour/week	4 hours/week (2 hours/batch twice
					in a week)
1	1,2,	1	1. Wireless communication –		1. Implement WLAN in your
	3		Concept, block diagram,		computer lab.
			types, frequency spectrum used in		
			different wireless communication		
			systems.		
				Refer Table 1	
			2. Wireless metropolitan area		
			network(WMANs), Wireless local		
			area networks(WLANs), Wireless		
			personal area network – (WPANs)		
			3. Wi-Fi- Features and applications,		
			significance of hotspot.		

	4	4.0	1 perp		1401
2	1,2,	1,3	<ol> <li>1. RFID- concept &amp; applications.</li> <li>2 Bluetooth – components, connections, networking &amp; applications.</li> </ol>	Refer Table 1	1. Conduct an experiment to connect PC to internet through bluetooth access point of mobile and transfer a text file/image file/video file.
			3. Waveguides- Need, types, applications.		2. Interface RFID reader for any application using Arduino controller.
3	1,2, 3	1,5	<ol> <li>Microwave signals, microwave devices –Two cavity klystron,</li> <li>Reflex klystron.</li> <li>Magnetron and Travelling Wave</li> </ol>	Refer Table 1	Video demonstration & documentation on working of a. Two cavity klystron.     Reflex klystron.
			Tube (TWT) and their applications.  3. RADAR- principle of operation and applications.		2. Video demonstration & documentation on working of a.Magnetron. b.TWT.
4	1,2,	1,2	1. Radar range equation (no derivation) and factors influencing the radar range.	Refer Table 1	1. Study and measure the characteristics of pulse from signal generator using a CRO.
			<ul><li>2. Pulsed radar system- principle and block diagram, Duplexer.</li><li>3. Antenna scanning and tracking.</li></ul>		2. Conduct an experiment to use a smart phone as CCTV camera (or a CCTV camera) and connect it to another mobile to view the camera feed.
5	1,2,	1,5	<ol> <li>Special purpose Radars- doppler radar, MTI radar-block diagram and their applications.</li> <li>Secondary surveillance radar &amp; ILS.</li> <li>ZigBee –architecture, network topologies, applications.</li> </ol>	Refer Table 1	1a.Video demonstration and documentation to understand radar scanning and tracking systems. b. Video demonstration and documentation to understand the working of secondary surveillance radar.
					2. Interface Zigbee module for any application using Arduino controller.
6	1	1,5	Satellite Communication 1. Satellite - Types, orbits. apogee and perigee, azimuth and elevation angles, sub satellite point, sub satellite paths, ascending and descending nodes.  2. Posigrade and Retrograde orbits, Uplink and downlink, orbital period and radius of geosynchronous satellite, satellite eclipse. Polar and Geostationary satellites - advantages and disadvantages.	Refer Table 1	1. Study the features and working of different sections in a satellite communication trainer kit.  2. Conduct an experiment to Transmit & Receive three separate Signals (Audio, Video, and Tone/ Voice) simultaneously through satellite link and perform Link Fail Operations using satellite communication trainer kit.
			3. LEO, MEO & GEO satellites, Station keeping, Attitude control and thermal control		

7	1,2	1,5	<ol> <li>Satellite communication system-block diagram. Transponder-single conversion, double conversion and regenerative transponder.</li> <li>Increasing channel capacity-frequency reuse and spatial isolation. Communication satellite-satellite subsystems.</li> <li>Earth station- block diagram,</li> </ol>	Refer Table 1	1. Find the delay between Uplink transmitter and Downlink receiver during data transmission using satellite communication trainer kit.  2. Demonstrate working of satellite transponders using satellite communication trainer kit.
8	1,2,	1,5	Applications payload.  1. Global Positioning System (GPS) –features, working.  2. Satellite for TV applications - Direct-To-Home (DTH) and cable TV.  3. Satellite for military applications, VSAT – features & applications.	Refer Table 1	1. Video demonstration and documentation on a. Working of GPS System b. Working of Satellite TV.  2. Conduct an experiment to tabulate latitude, longitude, Plus codes of different locations using a GPS receiver in mobile phone and learn sharing of live locations.
9	1,2, 4	1,5,7	<ol> <li>Satellite for voice and data communication, Earth observation.</li> <li>Set top box -concept, block diagram.</li> <li>Set top box - working.</li> </ol>	Refer Table 1	<ol> <li>Video demonstration and documentation of TV Set top box repair.</li> <li>Test and troubleshoot Set top box.</li> </ol>
10	1,2	1,5	<ol> <li>Cellular networks, cellular concept, frequency reuse.</li> <li>Terminologies used in mobile communication. capacity expansion techniques-cell splitting and cell sectoring.</li> <li>Handoff strategies. working of a typical cellular system.</li> </ol>	Refer Table 1	1. Conduct an experiment to understand the working of different sections in a mobile phone using a mobile phone trainer kit.  2. Conduct an experiment to analyze MIC & Speaker section, Buzzer section using a mobile phone trainer kit.
11	1,2,	7	<ol> <li>GSM services and features.</li> <li>GSM architecture, working.</li> <li>LTE architecture and working.</li> </ol> Mobile servicing	Refer Table 1	1. Conduct an experiment to analyse vibrator section, LED control section using a mobile phone trainer kit.  2. Conduct an experiment to analyse the active mode/sleep mode/Partially ON mode while charging of a mobile phone using a mobile phone trainer kit.  Video demonstration and
12	1,2, 4	/	1. Mobile displays – working principle.		documentation of

			<ul><li>2. Mobile camera – working principle.</li><li>3. Charging ports &amp; battery - concept</li></ul>	Refer Table 1	<ol> <li>Troubleshooting, testing and replacement of display, front camera.</li> <li>Troubleshooting, testing and replacement of charging port, battery.</li> </ol>
13	1,2,	7	<ol> <li>IoT – introduction, characteristics of IoT, internet of things.</li> <li>IoT protocols-MQTT, IoT- functional blocks.</li> <li>IoT communication models, IoT enabling technologies.</li> </ol>	Refer Table 1	1. Build an IoT based simple real time application using Arduino controller and prepare a report.
Total in hours		ours	39	13	52

Note: 1) In Practice sessions Video demonstration should be followed by MCQs/Quiz/Subjective questions and the evaluation has to be documented.

2) In Practice sessions, all circuits should be simulated using suitable software before its construction and verification.

#### **TABLE 1: Suggested activities for tutorials**

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week no.	Suggested activities for tutorials
01	Give a presentation on differences between wired and wireless communication.      Demonstrate the implementation of Wi-Fi hotspot.
	3, Video demonstration & documentation of working of industrial wireless communication.
02	<ol> <li>Give a presentation on Bluetooth specification Standards (IEEE 802.15.1).</li> <li>Build a simple application using RFID.</li> <li>Analyse the CCTV setup in your department/college and troubleshoot the CCTV application if required and submit the report.</li> </ol>
03	Prepare a report on microwave devices- IMPATT & TRAPATT.      Give a presentation on the working of any one type of waveguide.

04	1. Give a presentation on any one application of a Radar system in daily life.
04	2. Give a presentation about the usage of the Radar technology in case of searching a crashed aircraft in the ocean.
	3. Solve problems on radar range equation.
05	1. Prepare and present a report on radar displays.
03	2. Give a presentation on aircraft landing systems (ILS).
	3. Give a presentation on the design & performance analysis of the doppler radar system.
06	1. Give a presentation on applications of LEO & MEO satellites.
	2. Prepare a report on satellites launched by ISRO.
	3. Give a presentation on differences between geostationary and geosynchronous satellite. List some examples of geostationary satellites.
07	1. Present a report on satellite frequency allocation and satellite bandwidth.
07	2. Give a presentation on station keeping.
	3. Give a presentation on the different types of antennas used in earth station.
08	1. Prepare & present a report on GPS applications.
UO	2. Prepare a report on DRONE, its working and various uses.
	3. Prepare a report on different types of launch vehicles used for launching a satellite in India and its significance.
09	1. Differences between cable box & set top box.
09	2. Compare the different set up boxes available in the market.
	3. Prepare a report on the various experimentation and findings being conducted on the surface of MARS by NASA's Perseverance Rover (include actual pictures released from NASA website).
10	1. Prepare a report on different generations of cellular networks.
10	2. Give a presentation on different mobile operating systems.
11	1. Study of SIM card and its detection, SIM reset, SIM clock, SIM data, and SIM supply.
11	2. Give a presentation on CDMA system-services and features.
	3. Give a presentation on LTE system services and features.
12	1. Prepare a report on compatibility of mobile phone battery size and its heat dissipation.
12	2. Discuss the types and characteristics of antennas used in mobile phones.
	-

- 1. Study the latest technological changes in this course and present the impact of these changes on industry.
  - 2. Demonstrate the importance of IoT based health monitoring system.

#### **LINKS**

- 1. <a href="https://youtu.be/Q97bVxd2r10">https://youtu.be/Q97bVxd2r10</a>
- 2. <a href="https://youtu.be/Fvud81pYG0g">https://youtu.be/Fvud81pYG0g</a>
- 3. <a href="https://youtu.be/bUsS5KUMLvw">https://youtu.be/bUsS5KUMLvw</a>
- 4. <a href="https://youtu.be/4-wp\_M1z4ls">https://youtu.be/4-wp\_M1z4ls</a>
- 5. <a href="https://youtu.be/qzBPSG1b5uo">https://youtu.be/qzBPSG1b5uo</a>
- 6. <a href="https://youtu.be/H00\_PVX2bRw">https://youtu.be/H00\_PVX2bRw</a>
- 7. <a href="https://youtu.be/wCcARVbL Dk">https://youtu.be/wCcARVbL Dk</a>
- 8. <a href="https://youtu.be/OpkatIqkL08">https://youtu.be/OpkatIqkL08</a>
- 9. <a href="https://youtu.be/AiT36qdoSCc.">https://youtu.be/AiT36qdoSCc.</a>
- 10. <a href="https://youtu.be/oEa0Pfxl4C8">https://youtu.be/oEa0Pfxl4C8</a>
- 11. <a href="mailto:ttps://youtu.be/1JZG9x-VOwA">ttps://youtu.be/1JZG9x-VOwA</a>
- 12. <a href="https://youtu.be/iS8jmhVAfoQ">https://youtu.be/iS8jmhVAfoQ</a>.
- 13. <a href="https://youtu.be/2UujN pOcYI">https://youtu.be/2UujN pOcYI</a>
- 14. <a href="https://youtu.be/iQeaK0NGMnA">https://youtu.be/iQeaK0NGMnA</a>
- 15. <u>www.ifixit.com-</u>> Repair guides->select the particular model for ref.

#### 4. CIE and SEE Assessment Methodologies

Sl.	Assessment	Test Week	Duration	Max marks	Conversion
No			In minutes		
1.	CIE-1 Written Test	5	80	30	Average of three
2.	CIE-2 Written Test	9	80	30	tests
3	CIE-3 Written Test	13	80	30	30
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two skill
5	CIE-5 Skill Test-Practice	12	180	100	tests
					20
6	CIE-6 Portfolio continuous	1-13		10	10
	evaluation of Activity through				
	Rubrics				
		60			
	Semester End Examinatio	n (Practice)	180	100	40
				<b>Total Marks</b>	100

#### 5. Format for CIE (1, 2, 3) Written Test

Course Nan	ne Wireless Communication	Test	I/II/III	Sem	III/IV
Course Cod	e <b>20EC42P</b>	Duration	80 Min	Marks	30
Note: Answ	er any one full question from each section	. Each full question	carries 10 n	narks.	
Section	Assessment Questions		Cognitive	Course	Marks
			Levels	Outcome	
I	1				
	2				
II	3				

	4		
III	5		
	6		

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

#### 5.(a) Format for CIE-4 Skill Test - Practice

SL.	COs	Particulars/Dimension	Marks
No.			
1	1	Identification of types of wireless communications & its applications.	10
2	2	Identify the various components of a given wireless communication system & their role in the system.	10
3	3	Build and demonstrate a WLAN/ RFID/ZIGBEE communication for a specific application.  Construction / Setting up - 40 Marks.  Result / Output - 30 Marks.	70
4	1,2,3	Portfolio evaluation of Practice sessions through Rubrics.	10
	•	Total Marks	100

#### 5.(b) Format for CIE-5 Skill Test - Practice

SL. No.	COs	Particulars/Dimension	Marks	
1	1	Identification of types of wireless communications & its applications.	10	
2	2	Identify the various components of a given wireless communication system & their role in the system.		
3	3	Demonstrate a wireless communication system for a specific application  Construction - 10 Marks  Output - 10 Marks	20	
4	4	Test a given Set Top Box/Mobile Phone. Testing Steps -25 Marks Troubleshooting Steps -25 Marks	50	
5	1,2,3, 4	Portfolio evaluation of Practice sessions through Rubrics	10	
	•	Total Marks	100	

### 6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl.	Dimension	Beginner	Intermediate	Good	Advanced	Expert	Students
No							Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
				Avera	ge Marks= (8+	6+2+2)/4=4.5	5

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description			
1	Microwave Devices and Components by Sylio, Prentice Hall of India, New Delhi			
2	Wireless Communications (Principles and Practice), by Thedore Rappaport			
3	Wireless Communications and Networking, by William Stallings			
4	Mobile Communication by John Schiller, Prentice Hall of India, New Delhi			

# 8. SEE Scheme of Evaluation

SL.	COs	Particulars/Dimension	Marks
No.			
1	1	Identify the types of wireless communications and its uses/applications	10
2	2	Identify the various components of a given wireless communication system & their role in the system.	10
3	3	Demonstrate a wireless communication system for a specific application  Construction - 15 Marks  Output - 15 Marks	30
4	4	Test a given Set Top Box/Mobile Phone Testing Steps -15 Marks Troubleshooting Steps -15 Marks	30
5	1,2,3, 4	Viva- Voce	20
		Total Marks	100

# 9. Equipment/software list with Specification for a batch of 20 students

Sl No	Particulars	Specification	Quantity
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	MATLAB Software		
3	Dual trace oscilloscope	Up to 20-30MHz	10
4	CAT5 cable		100m
5	RJ 45 connectors		100
6	Arduino microcontroller board		10
7	RFID Reader , Tag		5, 20
8	ZigBee Module		10
9	Satellite Communication trainer kit	Uplink Transmitter, Inbuilt tone generator Satellite Link, Downlink receiver.	5
10	TV Set up box		10
11	Mobile phone trainer kit	Onboard Section: Keypad, Dual SIM, Charging Circuit, User interface: Buzzer, Vibrator, Mic, Speaker, Hands free port and display LEDs	5
12	Not- working mobile phones		5



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics and Communication	Semester	IV
Course Code	20EC43P	Type of Course	Programme Core
Course Name	Embedded C Programming	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

#### 1. Rationale

C is a general purpose programming language which is robust and highly portable used for scripting system applications which form a major part of all operating systems. C language is available on a very wide range of platforms, from embedded microcontrollers to supercomputers. Microcontroller is a compressed microcomputer manufactured to control the functions of embedded systems in various fields such as automobile, aeronautics, robotics, mobile communication, electronic appliances, industrial processing, defense, space, medical applications etc. The future of the micro controller depends on machine learning in embedded systems.

#### **2. Course Outcomes:** On successful completion of the course, the students will be able to:

CO1	Write the code using C constructs for a given requirement, execute the program, debug and to demonstrate that the program produces the required result/output.
CO2	List the various components and the characteristics of each component in a 8051 Microcontroller.
CO3	Write an embedded program for a given requirement, test and troubleshoot to obtain the desired output.
CO4	Identify the right microcontroller/peripheral device using data sheets / specification sheets for a given application.

CO	Р0	Lecture (Knowledge Criteria)	Tutorial	Practice (Performance
			Criteria)	Criteria)
		3 hours/week	1 hour/week	4 hours/week (2 hours/batch twice in a week)
1	1,2,	1. Introduction to C - features,		1. Familiarisation of TURBO
	3	compilation process.		C.
			Refer Table 1	
		2. C tokens, variables and identifiers, constants.		2. Programs to illustrate the use of different data types and verify their memory
		3. Data types - classification, memory requirement, range of values, usage.		size.
		1 1,2,	3 hours/week  1 1,2, 1. Introduction to C - features, compilation process.  2. C tokens, variables and identifiers, constants.  3. Data types - classification, memory	(Activity Criteria)  3 hours/week  1 1,2, 1. Introduction to C - features, compilation process.  2. C tokens, variables and identifiers, constants.  3. Data types - classification, memory

2	1	1,2,	<ol> <li>Operators and Operands- Arithmetic, logical, relational operators.</li> <li>Unary, conditional, assignment and special operators, precedence and associativity.</li> <li>Basic input and output functions, format specifiers, preprocessor directive &amp; library functions</li> </ol>	Refer Table 1	1a.Compute simple interest given the principal, interest rate and duration. b. Compute compound interest given P,t,r,n.  2a. Compute the area of a circle, square, rectangle and triangle. b. Swap contents of two variables without using intermediate variables.
3	1	1,2,	<ol> <li>Flowchart and Algorithm, Structure of a C program, simple C programs.</li> <li>Branching- conditional -if, if-else, example programs.</li> <li>Nested if-else, switch, example programs.</li> </ol>	Refer Table 1	1a.Compute the largest of three numbers using if-else and ternary operators. b.Compute the result of a student using nested if.  2. Given the resistance and tolerance, generate the color bands of the resistor using a switch statement.
4	1	1,2,	<ol> <li>Looping- for, while, do-while loops.</li> <li>Example programs on looping.</li> <li>Arrays- definition, declaration, initializing single dimensional arrays.</li> <li>Examples.</li> </ol>	Refer Table 1	<ul> <li>1a.Compute factorial of a single digit number.</li> <li>b. Compute the sum of digits of a given 3 digit number reducing it to a single digit.</li> <li>2. Sort an array of numbers in ascending order and descending order.</li> </ul>
5	1	1,2,	<ol> <li>Strings- declaration, initialization with an example. Two dimensional arrays- declaration, initialization with an example.</li> <li>Functions- elements of user defined functions, example.</li> <li>Pointers- introduction with example. Structures- introduction with example.</li> </ol>	Refer Table 1	1a. Compute the length of a string and reverse the string using string functions. 1b. Compute the sum of two matrices.  2a. Compute cube of a number using a function. 2b. Store the details of an employee using a structure and print the details
6	2	1	<ol> <li>Introduction to the concepts of embedded systems, microprocessors, microcontrollers.</li> <li>Selection of 8 bit, 16 bit, 32 bit, 64 bit microcontrollers. Introduction to 8051 microcontroller.</li> <li>Architecture of 8051 microcontroller, PSW and special function registers.</li> </ol>	Refer Table 1	1. Identification of program development tools.  2. Familiarization of program development using Keil.
7	2	1,2,	Memory organization, general purpose RAM, bit addressable RAM.      Register banks, Pin details of 8051.	Refer Table 1	1. Familiarize with the structure of the 8051 assembly program and executing it.

			3. Interfacing external data and code		2. Write and execute simple
			memory.		ALP to understand different
8	3	1,2,	1.8051 Addressing modes.		addressing modes.  1.Write and execute an ALP to  (a) Move a block of data within internal RAM
			2. Instruction set- classification, syntax and function of data transfer instructions,	Refer Table 1	(b) Exchange a block of data between internal RAM and external memory.
			3. Arithmetic instructions, Logical instructions.		2. Write an ALP to (a) evaluate simple arithmetic expression such as y= (((5*2)-(4+1))/3) %2.
					(b)Perform addition of three 8-bit BCD numbers to result in BCD form.
9	3	1,2,	1. Bit level instructions, jump instructions.	Refer Table 1	1.Write an ALP to (a) Rotate or shift 16-bit data. (b)Evaluate simple logical expression such as Y=
			2. Introduction to Embedded C and its applicability to 8051.		a&&b  c^!d where a, b, c and d are 8-bit data.
			3. General structure of embedded C program, data types.		2.Write and execute an assembly and embedded C program to convert (a)Packed BCD to unpacked BCD (b) Unpacked BCD to packed BCD.
10	3	1,2,	1. Memory types and models, pointers, pointer's memory type.	Refer Table 1	1. Write and execute a program to search a given 8-bit number in an array of N numbers using embedded C.
			2. Time-delay generation using loops, example program.		2. Write and execute a
			3. Arithmetic and logical operators, example programs.		program to toggle a particular bit in the internal RAM with the use of delay subroutine.
11	3,4	7	1. Features of I/O ports. Interface I/O devices such as LED, buzzer with programs.	Refer Table 1	1. Write and execute an embedded C program to toggle the LED/buzzer with
			2. Polling & interrupt methods, executing an interrupt, IE and IP registers.	TROICE FUDIC 1	tone using push-button switch.
			3. Enabling, disabling and priority setting, example programs.		2. Write ALPs to enable, disable and priority setting of interrupts and

Total in hours		'S	39	13	52
			3. Interfacing 8051 to DC motor, Stepper motor with assembly /C program.		OR Program to generate sine/ rectangular / triangular wave-forms.
			waveform generation using DAC 0808 with assembly/C program.		2. Program to control traffic lights
13	3,4	5,/	<ol> <li>Interfacing 8051 to Multiplexed seven-segment display with assembly/C program.</li> <li>Interfacing 8051 to ADC 0804,</li> </ol>	Refer Table 1	Interfacing experiments  1. Program to control direction and speed of a stepper motor/ dc motor.  Study the data sheets of stepper motor/dc motor.
	3,4	5,7	3. Bit structure of SCON register, SBUF register, TI and RI flags, working of serial port for data transmission and reception.		2. Observe the square wave of the above program on CRO by downloading the program to the microcontroller kit.
12	3,4	7	Bit structure and function of TMOD and TCON registers, mode 1 operation of timers.      Time delay generation & example programs.	Refer Table 1	1. Write and execute an embedded C program to generate a square wave on P1.2 using timer 0 in mode 1 to generate delay.

### **TABLE 1: Suggested activities for tutorials**

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week No.	Suggested Activities for Tutorials
01	<ol> <li>Give a presentation on comparison of different high level languages.</li> <li>Prepare a report on advantages and applications of C.</li> </ol>
02	<ol> <li>Write a program to print a pyramid number pattern and explain it.</li> <li>Explain operator precedence and associativity with examples.</li> <li>Demonstrate implicit and explicit type conversions.</li> </ol>
03	1. Write and explain algorithms and flowcharts for simple programs.

	2. Give a presentation on comparison of switch and if-else statements.
	3. Demonstrate the use of break, continue and goto statements in C.
04	1. Demonstrate the comparison of while, do-while and for loop with an example.
04	2. Write and explain a program to print multiplication tables from 1 to 5.
05	1. Write and explain a program to check whether a given string is palindrome or not.
	2. Write and explain a program to multiply two matrices.
	3. Give a presentation on advantages of user defined functions.
	4. Give a presentation on usage of pointers in C.
06	1. Give a presentation to differentiate RISC & CISC.
	2. Discuss variants of MCS-51 family and their features.
07	1. Give a presentation on applications of microcontrollers.
07	2. Prepare and explain the memory organization diagram.
	3. Explain bit structure of PSW and PCON registers.
08	1. Write and explain examples for different addressing modes.
	2. Find the addressing mode, no. of bytes and no. of machine cycles for different instructions.
09	1. Compare different types of JUMP instructions.
	2. Explain the pros and cons of embedded C.
10	1. Write embedded C programs for time delay generation using loops.
	2. Write and explain instructions for arithmetic and logical operations.
11	1. Give a presentation on the importance of I/O ports in microcontrollers and write programs using I/O ports.
	2. Give a presentation on the need of interrupts in microcontrollers.
12	1. Write and explain bit structures of TCON, TMOD and SCON registers.
	2. Give a presentation on comparison of mode 1 and mode 2 operations of timers.
13	1. Study the latest technological changes in this course and present the impact of these changes on industry.
	2. List any 5 other microcontrollers used in real world applications and interpret their datasheets.

#### 4. CIE and SEE Assessment Methodologies

Sl. No	Assessment	Test Week	<b>Duration</b> In minutes	Max marks	Conversion		
1.	CIE-1 Written Test	5	80	30	Average of		
2.	CIE-2 Written Test	9	80	30	three tests		
3	CIE-3 Written Test	13	80	30	30		
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two		
5	CIE-5 Skill Test-Practice	12	180	100	skill tests 20		
6	CIE-6 Portfolio continuous evaluation of Activity through Rubrics	1-13		10	10		
	Total CIE Marks						
Seme	Semester End Examination (Practice)		180	100	40		
	Total Marks						

# 5. Format for CIE (1, 2, 3) Written Test

Course Name		Embedded C Programming	Test	I/II/III	Sem	III/IV
Course Code		20EC43P	Duration	80 Min	Marks	30
Note: Answ	er a	ny one full question from each section. Eac	h full questio	n carries 10 m	narks.	
Section	Λcc	essment Questions		Cognitive	Course	Marks
Section	ASS	essment Questions	Levels	Outcome	Marks	
ī	1					
	2					
11	3					
II	4					
III	5					
	6					

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional Questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

#### 5.(a) Format for CIE-4 Skill Test - Practice.

SL. No.	COs	Particulars/Dimension	Marks
1	1	C Programming Writing two C programs - 30 Marks Entry & Execution - 30 Marks Output -10 Marks	70
2	2	List the various components and the characteristics of each component in a 8051 Microcontroller.	20
3	1,2	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

# 5. (b) Format for CIE-5 Skill Test - Practice.

SL.	COs	Particulars/Dimension	Marks
No.			
1	2	List the various components and the characteristics of each component in a 8051 Microcontroller.	10
2	3	8051 ALP /8051 C programs for a desired output  Writing program - 20 Marks	40
		Output - 20 Marks	
3	4	Interfacing program for an application  Writing program - 20 Marks  Downloading to kit and Output - 20 Marks	40
4	2,3,4	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

# 6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl.	Dimension	Beginner	Intermediate	Good	Advanced	Expert	Students
No.							Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
	Average Marks= (8+6+2+2)/4=4.5						

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description
1	C Programming By Kernighan and Dennis Ritchie 04.
2	C Programming By Balaguruswamy, TMH Publishers, ISBN-10: 8131716813, 2009.I
3	Scott MacKenzie and Raphael C.W. Phan. The 8051 Microcontroller. (4/e), Pearson education, 2008.
4	Kenneth J Ayala, The 8051 Microcontroller, (3/e), Thomson Delmar Learning.

#### 8. SEE Scheme of Evaluation

SL No.	COs	Particulars/Dimension	Marks
1	1	C Programming Writing Program - 10 Marks Entry & Execution -10 Marks Output - 5 Marks	25
2	2	Identify & Explain the functionality of various components in a 8051 Microcontroller	10
3	3	8051 ALP /8051 C programs for a desired output  Writing program - 15 Marks Output - 5 Marks	20
4	4	Interfacing program for an application  Writing program - 10 Marks  Downloading to kit and Output- 15 Marks	25
5	1,2,3,4	Viva-Voce  Total Mayles	20
		Total Marks	100

# 9. Equipment/software list with Specification for a batch of 20 students

Sl. No.	Particulars	Specification	Quantity
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	TURBO C		
3	8051 Microcontroller kits		20
4	Interfacing kits		5 each



# Government of Karnataka DEPARTMENT OF COLLEGIATE AND TECHNICAL EDUCATION

Programme	Electronics and Communication	Semester	IV
Course Code	20EC44P	Type of Course	Programme Core
Course Name	Industrial Automation	Contact Hours	8 hours/week 104 hours/semester
Teaching Scheme	L:T:P :: 3:1:4	Credits	6
CIE Marks	60	SEE Marks	40

#### 1. Rationale

Automation in the industrial workplace provides the advantages of improving productivity and quality while reducing errors and waste, increasing safety, and adding flexibility to the manufacturing process. Industrial automation results in increased productivity, more efficient use of materials, increased safety, reliability, better product quality, shorter workweeks for labour, profitability and reduced factory lead times. Worker safety is an important reason for automating an industrial operation. A wide range of industrial controls and automation depends on power electronics. PLC is an industrial computer control system used to control the state of output devices based upon a custom program. SCADA is a centralized system that monitors and controls field devices at remote sites.

#### **2. Course Outcomes**: On successful completion of the course, the students will be able to

CO-01	Explain the role and importance of power electronics in today's industrial automation and for a given application list the commonly used components in power electronics.
CO-02	Build a power electronic circuit and demonstrate the working of that circuit for a specific
	application either in a real or simulated environment.
CO-03	Design, test and troubleshoot a given PLC automation system to meet defined operational
	specifications in a simulated environment.
CO-04	Explain the concept of SCADA, DCS and HMI and list their various applications in industry.

We	СО	PO	Lecture (Knowledge Criteria)	Tutorial (Activity Criteria)	Practice (Performance Criteria)
ek			3 hours/week	1 hour/week	4 hours/week (2 hours/batch twice in a week)
1	1	1	<ol> <li>Introduction to industrial automation, need for power devices, features of power diode, power BJT.</li> <li>Features of SCR, IGBT and Power MOSFET.</li> <li>DIAC and TRIAC - working, applications.</li> </ol>	Refer Table 1	Conduct an experiment to find the holding current and latching current of SCR.      Conduct an experiment to determine break-over voltage of an SCR.
2	1,2	1	1. Triggering-Need, Triggering circuits- R-triggering, RC-triggering.	Refer Table 1	Construct a R triggering circuit and verify its working.

	ı	1	0.0.1		
			2. Pulse triggering using UJT		
			relaxation oscillators.		2 0 1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
					2. Construct a R-C triggering circuit
			3. Commutation-Need, natural		and verify its working.
			and forced commutation of SCR.		
			resonant commutation.		
3	1,2	1,3	1. Auxiliary commutation and		
			Complementary commutation.		1. Verify SCR triggering by UJT
					relaxation oscillator using a kit.
			2. Protection of SCR-Snubber		
			circuit- turn ON, turn OFF and		
			over- voltage.	Refer Table 1	2. Construct a full-wave controlled
					rectifier circuit using R-C triggering
			3. Controlled rectifiers- Single		and verify its working.
			phase half-wave controlled		
			rectifier, single phase full-wave		
			bridge controlled rectifier (only		
			resistive load), importance of		
			freewheeling diode.		
4	1,2	1,3	1. Chopper- working principle,		1. Verify the working of a constant
		<b>_</b>	duty cycle, chopper control		frequency voltage commutated
			schemes.		chopper using a kit.
					0.
			2. Chopper classifications, Step-		2. Verify the working of a variable
			up and Step-down choppers.	Refer Table 1	frequency voltage commutated
			1		chopper using a kit.
			3. Working of first quadrant,		
			second quadrant choppers.		
5	1,2	1,3	1. Working of two quadrant and		1. Verify working of series inverter
		<b>_</b>	four quadrant choppers, Buck		using a kit.
			and Boost converters.		
					2. Verify working of full bridge
			2. Inverters- working principle	Refer Table 1	inverter using a kit.
			and types, Half-bridge inverter.		
			3. Full-bridge inverter, Series		
			inverter,		
6	1,2	1,3	1. Variable dc link inverter,		1. Verify PWM techniques in
		,-	voltage source and current		inverters using a simulator.
			source inverters.		
					2. Verify single phase to single phase
			2. PWM techniques used in	Refer Table 1	cycloconverter using a kit.
			inverters.		
			3. Cycloconverter- classification,		
			working of single phase to single		
			phase midpoint cycloconverter.		
7	1,2	1,3	1. Photo-electric control of SCR,		1. Verify light dimmer circuit using
	_,_	-,5	Light dimmer circuit using DIAC		DIAC and TRIAC.
			and TRIAC.		
				Refer Table 1	
			2. Burglar alarm circuit. Need for		2. Simulate and verify the working
			electronic control of motors.		of Burglar alarm circuit/Photo
			3. Armature voltage control		electric control of SCR/Speed
			method and Field control		control of DC shunt motor.
			method for speed control of DC		control of De situate motor.
			shunt motor.		
	<u> </u>	<u> </u>	SHUIR HIOLOI.		

8	2,3	1,3, 5	1. Speed control of DC motors using dual converters, speed control of Induction motor.  2. PLC-introduction, compare Relay logic control and PLC logic control, block diagram of PLC system, PLC scanning.  3. Internal architecture of PLC, memory organization.	Refer Table 1	Verify the speed control of universal motor using a kit.      Verify the speed control of stepper motor using inverter in clockwise and anti-clockwise direction using a kit.
9	3	1,3, 5	1. PLC input devices – switches, proximity sensors, photoelectric sensors, temperature sensors, liquid level sensors.  2. PLC output devices – solenoids, relay, directional control valve, motors & stepper motors.  3. Programming standards, PLC Ladder diagram, ladder diagram for logic gates.	Refer Table 1	<ol> <li>Familiarization of software for PLC simulation (Keyence/Picosoft).</li> <li>Write ladder diagrams and verify the truth table of all logic gates.</li> </ol>
10	3	3,5, 7	1.PLC input instructions and outputs- coils, indicators, Conversion of Boolean functions from word description to ladder diagram and vice-versa.  2.Write the ladder diagrams for different applications Ex i. A system where there has to be no output when any one of four sensors gives an output, otherwise there is to be an output. ii. Staircase light application. iii.Conveyor control application.  3. PLC register basics- Input, Holding, Output, PLC arithmetic functions- addition, subtraction, multiplication & division.	Refer Table 1	1. Write a ladder diagram for DOL starter and test the output using PLC trainer kit module.  2. Simulate and test the following task using PLC,    A signal lamp is required to be switched ON if a pump is running and the pressure is satisfactory, or if the lamp test switch is closed, otherwise the signal lamp should remain OFF.
11	3	3,5, 7	1. PLC Basic comparison functions and its applications.  2. PLC Timer functions- on delay timer, off-delay timer, pulsed timer, one shot, applications of timing functions in process control.  3. PLC Counter functions-up/down counter, applications of	Refer Table 1	1. Write a ladder diagram, timing diagram and simulate a circuit for the following process control application.  There are 3 mixing devices on a processing line A, B and C. After the process begins mixer-A is to start after 7 seconds elapse, next mixer-B is to start 3.6 seconds after A. Mixer-C is to start 5 seconds after B. All of them remain ON until a master enable switch is turned OFF.

		PLC counter functions in process control.		2. Write a ladder diagram and simulate a circuit for a process control application in which a paint spray has to run for 40 seconds when the count reaches the value of 25.
12 3,4	3,5, 7	<ol> <li>PLC and the internet, selection of PLC and its maintenance, PID module.</li> <li>Distributed Control System-Introduction, features, hierarchical architecture, advantages.</li> <li>DCS application in chemical plants/ cement plants/ paper and pulp industries, Introduction to HMI/MMI.</li> </ol>	Refer Table 1	1. Write the ladder diagram and execute the Water level controller/Staircase light controller application using PLC trainer kit module.  2. Video demonstration and documentation of DCS application in any plant.
13 4	3,5, 7	<ol> <li>SCADA-Introduction, background, definition, features, typical SCADA system.</li> <li>SCADA architecture, SCADA hardware &amp; software.</li> <li>SCADA protocols, interfacing PLC with SCADA. applications of SCADA.</li> </ol>	Refer Table 1	Write the ladder diagram and execute the Lift control/Conveyor control application using PLC trainer kit module.      Video demonstration and documentation of the SCADA systems.
Total in h	ours	39	13	52

Note: 1) In Practice sessions Video demonstration should be followed by MCQs/Quiz/Subjective questions and the evaluation has to be documented.

2) In Practice sessions, all discrete circuits should be simulated using suitable software before its construction and verification.

#### **TABLE 1: Suggested activities for tutorials**

The list is shared as an example and not inclusive of all possible activities of the course.

The list of activities for one week can be shared among teams in a batch of students.

Week no.	Suggested activities for tutorials
01	<ol> <li>Give a presentation on constructional features of SCR, its specifications and ratings.</li> <li>Prepare a report on specifications and ratings of Diac and Triac.</li> <li>Discuss feasibility of Germanium Controlled Rectifier.</li> </ol>
02	Discuss the importance of triggering an SCR.     Explain the comparison of natural and forced commutation of SCR.

03	1. List the applications of controlled rectifiers in industries.
05	2. Explain any one real time application of a controlled rectifier.
04	1. Explain any one real time application of choppers.
0.5	Explain the differences between Buck and Boost converters.
05	2. Collect the information on the type and working of inverter used in your lab UPS.
06	1. Differentiate between step up and step down cyclo converter with their applications.
	2. Explain the role of a cycloconverter in the working of a washing machine.
07	1. Construct and demonstrate any one real time application of SCR/TRIAC(ex:Automatic street lighting/Smoke detector).
08	1. List the leading PLC manufacturers around the world and collect information on applications of PLC systems.
	2. Prepare a report on advantages of using PLC in automation.
09	1. Collect information on the specifications/parameters / datasheets of input devices used with PLC.
	2. Collect information on the specifications/parameters / datasheets of output devices used with PLC.
10	1. Develop a fire alarm system which has Fire sensors providing inputs to a SET-RESET function block so that if one of the sensors is activated, the alarm is set and remains set until it is cleared by being reset.
	2. Two Conveyors feed a main conveyor, find the main conveyor count from the count of parts entering the two conveyors.
11	1. Give a presentation on real time PLC Counter applications.
	2. Prepare a report on all types of PLC timer functions.
	3. Give a presentation on PLC advanced comparison functions.
12	1. Give a presentation on applications of HMI/MMI.
	2. Give a presentation on the different levels of industrial control with respect to networking of PLCs.
	3. Prepare a report on DCS system integration and DCS flow sheet symbols.
13	1. Study the latest technological changes in this course and present the impact of these changes on industry.

- 2. Prepare a report on different ways of deploying SCADA systems along with advantages and disadvantages.
- 3. Prepare a report on the security threat and vulnerability of SCADA Systems.

#### 4. CIE and SEE Assessment Methodologies

Sl.	Assessment	Test Week	Duration	Max	Conversion
No			In minutes	marks	
1.	CIE-1 Written Test	5	80	30	Average of three
2.	CIE-2 Written Test	9	80	30	tests
3	CIE-3 Written Test	13	80	30	30
4.	CIE-4 Skill Test-Practice	6	180	100	Average of two skill
5	CIE-5 Skill Test-Practice	12	180	100	tests
					20
6	CIE-6 Portfolio continuous	1-13		10	10
	evaluation of Activity through				
	Rubrics				
		60			
	Semester End Examination	n (Practice)	180	100	40
				Total Marks	100

### 5. Format for CIE (1, 2, 3) Written Test

Course		Industrial Automation	Test	I/II/III	Sem	III/IV
Name						
Course Co	de	20EC44P	Duration	80 Min	Marks	30
Note: Ans	wer	any one full question from each section	n. Each full	question ca	rries 10 ma	rks.
Section		Assessment Questions		Cognitive	Course	Marks
				Levels	Outcome	
I	1					
	2					
II	3					
	4					
III	5					
	6					

Note for the Course coordinator: Each question may have one, two or three subdivisions. Optional questions in each section carry the same weightage of marks, Cognitive level and course outcomes.

# 5. (a) Format for CIE-4 Skill Test - Practice.

SL.	COs	Particulars/Dimension	Marks
No.			
1	1	Identify the various components used in Power Electronics and demonstrate its use for a given application.	20
2	2	Build a power electronic circuit and demonstrate the working of that circuit for a specific application either in a Real or Simulated environment.	
		Construction of circuit diagram -20 Marks Conduction -20 Marks Output -30 Marks.	70
3	1,2	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

# 5. (b) Format for CIE-5 Skill Test - Practice.

SL.	COs	Particulars/Dimension	Marks
No.			
1	3	Design, Test and Troubleshoot a specific PLC Automation System to meet defined operational specifications in a simulated environment.	70
		Writing Ladder diagram (2 applications) - 40 Marks Interfacing to kit - 20 Marks Result - 10 Marks	
2	4	Concept of SCADA/DCS/HMI and list their various applications	20
3	3,4	Portfolio evaluation of Practice sessions through Rubrics	10
		Total Marks	100

### 6. Rubrics for Assessment of Activity (Qualitative Assessment)

Sl.	Dimension	Beginner	Intermediate	Good	Advanced	Expert	Students
No.							Score
		2	4	6	8	10	
1		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	8
2		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	6
3		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
4		Descriptor	Descriptor	Descriptor	Descriptor	Descriptor	2
	Average Marks= (8+6+2+2)/4=4.5						

*Note:* Dimension and Descriptor shall be defined by the respective course coordinator as per the activities

#### 7. Reference:

Sl. No.	Description
1	"Programmable Logic Controllers Principles and Applications" by John W. Webb – Ronald A. Reis. 5th Edition, Published by PHI Publication.
2	"Introduction to PLC's" by Gary Dunning, 3rd Edition, Thomson India Edition
3	"PLC's" by W. Bolton, 4th edition.
4	Programmable Logic Controllers by Frank D Petruzella, 4th Edition, McGraw Hill Publications.

#### 8. SEE Scheme of Evaluation

SL.	COs	Particulars/Dimension	Marks
No.			
1	1	Identify the various components used in Power Electronics and demonstrate	10
		its use for a given application.	
2	2	Build a power electronic circuit and demonstrate the working of that circuit for	
		a specific application either in a Real or Simulated environment.	
		Construction of circuit diagram - 10 Marks	30
		Conduction -10 Marks	
		Output -10 Marks	
3	3	Design, Test and Troubleshoot a specific PLC Automation System to meet	
		defined operational specifications in a simulated environment.	
			30
		Writing Ladder diagram - 10 Marks	
		Interfacing to kit - 10 Marks	
		Result - 10 Marks	
4	4	Concept of SCADA/DCS/HMI and their applications.	10
5	1,2,3,	Viva-Voce	20
	4		
		Total Marks	100

# 9. Equipment/software list with Specification for a batch of 20 students

Sl. No.	Particulars	Specification	Quantity
1	Computers	Intel Core i5 11th gen/8GB RAM/1 TB HDD/256GB SSD/ Graphics 2 GB	20
2	POWERSIM simulation software		
3	Kit for SCR triggering by UJT relaxation oscillator		10
4	Kit for Voltage commutated chopper both constant frequency & variable frequency.		10
5	Series Inverter kit		10
6	Full bridge inverter kit		10
7	PWM inverter kit		10
8	Single phase to Single phase cyclo converter kit		10
9	Light Dimmer kit		10

10	Speed control of universal motor kit	10
4.4		1.0
11	Speed control of stepper motor kit	10
12	PLC kits	10
13	PLC interfacing kits for Lift control, water level control	3 each



# Government of Karnataka Department of Collegiate and Technical Education

Programme	Audit Course	Semester	IV
Course Code	20EC45T	Type of Course	Audit
Course Name	Indian Constitution	Contact Hours	2 hours/week 26 hours/semester
Teaching Scheme	L:T:P :: 2:0:0	Credits	2
CIE Marks	50	SEE Marks	Nil

# **1. Course Outcomes:** At the end of the Course, the student will be able to:

CO-01	CO1	Understand Preamble, salient features and importance of Indian Constitution.
CO-02	CO2	Understand Fundamental rights, duties and Directive principles of state policy.
CO-03		Understand Parliamentary system of governance, Structure, Functions, Power of
	CO3	Central, state governments (Legislative, Executive) and Judiciary.
CO-04	CO4	Understand Panchayat Raj Institutions and Local self-governments, UPSC, KPSC,
CO-04		NHRC, Status of women, RTE etc.

Week	co	Detailed Course Content	Contact Hours
1	1	Introduction to constitution of India-Formation and Composition of the Constituent Assembly-Salient features of the Constitution-Preamble to the Indian Constitution	2
2	1,2	Fundamental Rights- Definition, The right to equality, The right to freedom, The right against exploitation, The right to freedom of religion.	2
3	1,2	Cultural and educational rights and The right to constitutional remedies. Fundamental Duties, Directive principles of state policy.	2
4	1,3	Parliamentary system of governance- Structure of Parliament- Lok Sabha and Rajya Sabha. Functions of parliament- Legislative, Executive, Financial Function Powers of Lok Sabha and Rajya Sabha.	2
5	1,3	Procedure followed in parliament in making law, Annual financial statement (Budget) – procedure in parliament with respect to estimates, Appropriation bill, Supplementary, additional grants, Vote on account, votes on credit and exception grant, special provisions, rules of procedure.	2
6	1,3	Structure of union executive, Power and position of President. Vice President, Prime minister and council of ministers.	2
7	1,3	Structure of the judiciary: Jurisdiction and functions of Supreme Court, high court, and subordinate courts.	2
8	1,3	Federalism in the Indian constitution- Division of Powers: Union list, State list and concurrent list. Structure of state legislation, Legislative assembly and Legislative council.	2
9	1,3	Functions of state legislature, Structure of state executive-Powers and positions of Governor, Speaker, Deputy Speaker, Chief Minister and council of minister.	2

Total in Hours			26 Hrs
13	1,4	National Human Rights Commission Constitution- Powers and function of the Commission-Employee rights- Provisions made, Contractual-Non contractual employee rights-Whistle blowing-definition-Aspects-Intellectual Property Rights (IPR)–Meaning-Need for protection- Briefly description of concept of patents, Copy right, Trademark	2
12	4	Status of Women in India - Women in rural areas, Constitutional Safeguards - Dowry Prohibition act 1961- Domestic violence act 2005- Sexual harassment at work place bill 2006.  Human Rights of Children- Who is a child- list the Rights of the Child- Right to education, Protection of Children from Sexual Offences Act (POCSO)-2012-	2
11	4	Amendment of the constitution, Human Rights-Definition-constitutional provisions-right to life and liberty-Human Rights of Women-Discrimination against women steps that are to be taken to eliminate discrimination against women in Education, employment, health care, Economic and social life,	2
10	4	Local self-government- meaning-Three tier system, Village Panchayat-Taluk panchayat Zilla panchayat, Local bodies-Municipalities and Corporations, Bruhath Mahanagara Palike, Functions of Election commission, UPSC, KPSC.	2

#### **REFERENCES**

- 1. Introduction to the Constitution of India- Dr. Durga Das Basu
- 2. Empowerment of rural women in India-Hemalatha H.M and Rameshwari Varma, Hema Prakashana.

4. CIE and SEE Assessment Methodologies

Sl. No	Assessment	Test Week	<b>Duration</b> In minutes	Max marks	Conversion		
1.	CIE-1 Written Test	5	80	30	Average of three		
2.	CIE-2 Written Test	9	80	30	tests		
3	CIE-3 Written Test	13	80	30	30		
4.	CIE-4 MCQ	6	60	20	Average of two		
5	CIE-5 Open Book Test	12	60	20	CIE = 20		
Total CIE Marks					50		
Semester End Examination (Practice)					-		
Tota	l Marks	50					